The brilliant present and the future perspectives of the Silicon PMs

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Overview

- Introduction
- Physics, Technology and key features
- SiPM Parameters and how to measure them
- Related Electronics
- Selected Applications
- Novel types of SiPM

Photo-detector family tree

Gas

External photoemission

Vacuum devices

External photoemission

hybrid

photocathode +

- multiplication by

ionization in Si

(HPD, HAPD, ...)

Solid state Internal photoemission

- Photo-Diode, PIN, SDD
- APD, GM-APD, SiPM-
- Integrating (CMOS,CCD)

- Quantum well detectors
- Supercond. Tunnel Junc.

 λ [nm]

- Carbon nanotubes, ...

850

gas photoionization (TMAE, TEA, ...)

and/or

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multiplication in gas by avalanche

(MWPC, GEM, ...)

100

250

Dynodes:

- discrete (PMT)

multiplication

- continuous dynode (channeltron, MCP)

400

secondary electron

Anode:

- multi-anode
- strip lines RF

- multiplication by

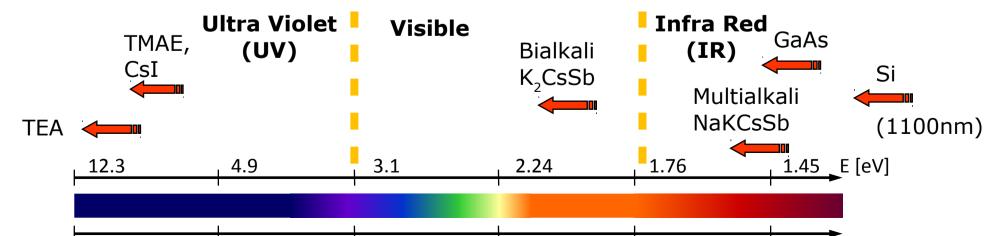
luminescent anodes

700

(light amplifiers:

SMART/Quasar,

X-HPD, ...)



550

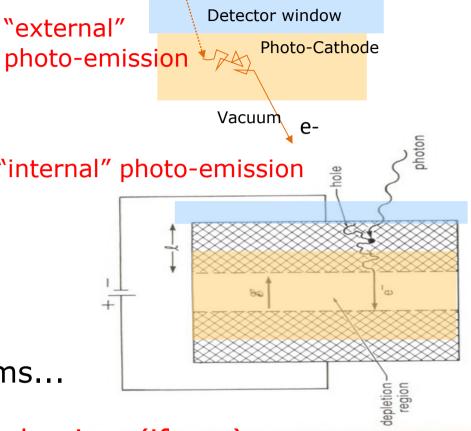
Photo-detection: vacuum vs solid state

1. Photo-electric conversion with or without emission in vacuum

Emission in vacuum implies

- → low detection efficiency
- → low dark count rate

... source of differences between vacuum and solid state devices including multiplication mechanisms...



2. Internal charge multiplication mechanism (if any)

Charge multiplication within the device implies

- → better Signal/Noise ratio (wrt external amplification)
- → intrinsic fluctuations in amplitude and timing → intrinsic Noise (depending on the multiplication mechanism)

Light detection with semiconductors in 3 steps

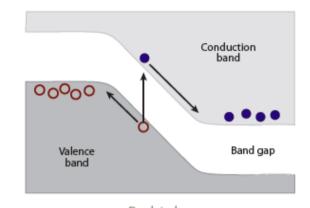
→ Photons convert and generate e/h pairs

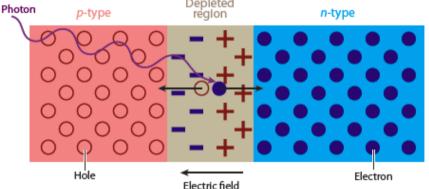
→ The two charge sheets on the n-doped and p-doped sides produce an electric field

→ separate charges (electrons / holes) produced by (photo-)ionization in the depleted region (even without an external E field)

Charges surviving **recombination** are swept to terminals

→ can be detected as an **induced current**



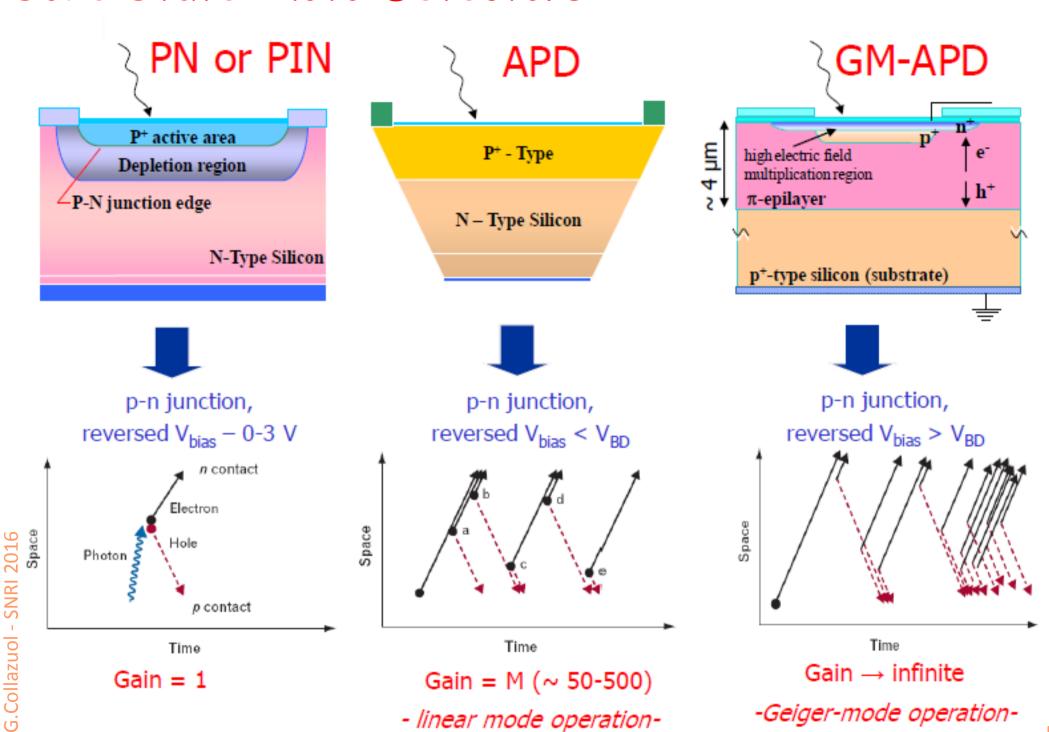


Picture from Krizan, Ann Rev Nucl Phys 2013

Note: Shockley-Ramo theorem

→ e- and h+ give "same sign" contribution to induced current (but integral of current induced on electrodes is Q and not 2Q)

Solid State Photo-Detectors

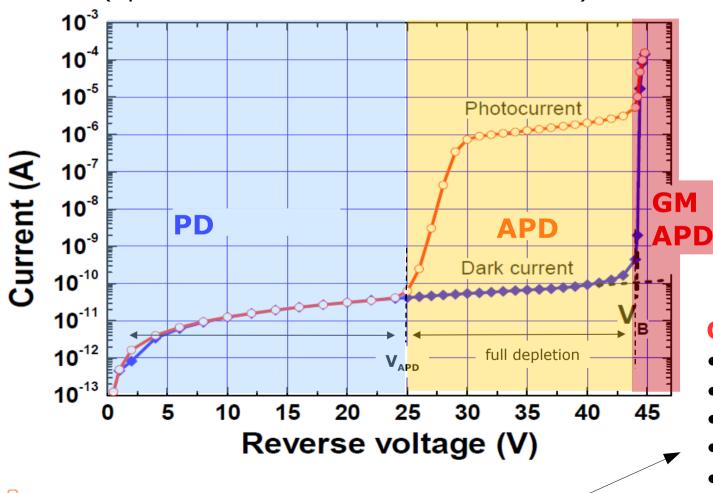


Devices with internal gain

APD: avalanche photo-diode

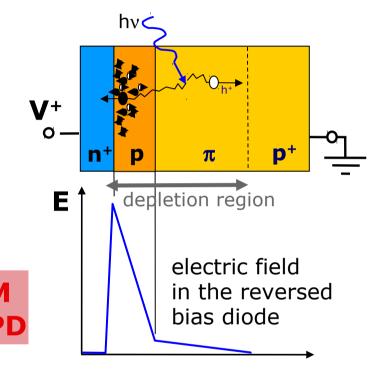
- Bias BELOW V_{bd} $(V_{APD} < V < V_{bd})$
- Linear Mode/ **AMPLIFIER** device
- Multiplication < 10³ (lim. by fluctuations)
- Sensitivity ~ 5 ph.e

(1ph.e. at low T with slow electronics...)



Building Block of SiPM

Reverse biased junction: internal gain via impact ionization in high E field

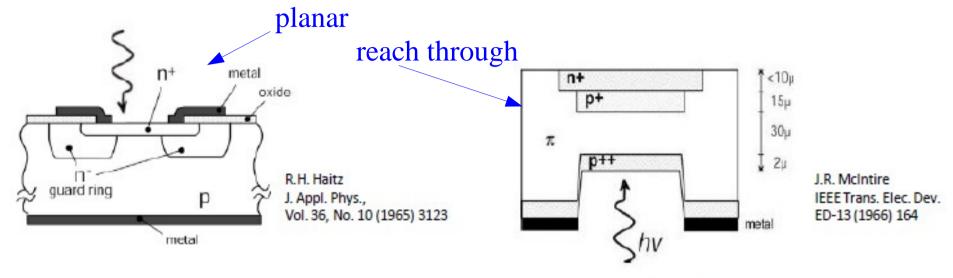


GM-APD: Geiger Mode

- Bias ABOVE V_{bd} (a few V)
- **BINARY** device
- Gain: ~10⁶ (lim. by C)
- Single ph.e. resolution
- Limited by dark count rate
- Need Quenching/Reset

GM-APD → Single Photon Avalanche Diodes (SPAD)

Various types of cell implementation



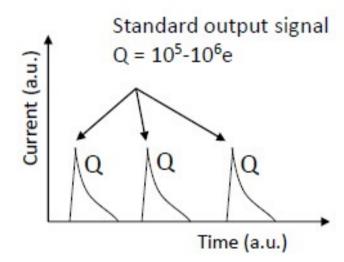


 $-\mathbf{V}_{\mathsf{bias}}$

- Need Quenching and Recharge
- → various implementations
 - Passive quenching: large resistance
 - Active quenching: analog circuits
 - S. Cova & al., App. Opt. 35 (1996) 1956-1976



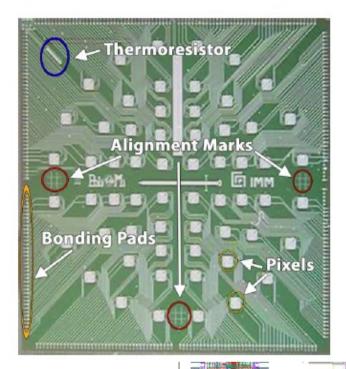
- Wide area
- Multi-photon detection



Binary device

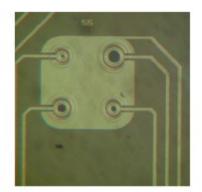
- If one or more simultaneous photons fire the GM-APD, the output is anytime a standard signal: Q~C(V_{bias} - V_{BD})
- GM-APD does not give information on the light intensity,

SPAD Arrays with electronics "integrated"



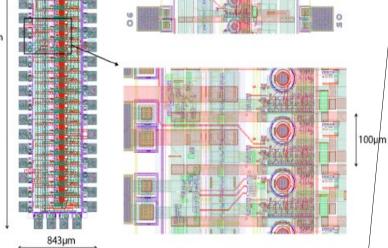
Electronics for

- quenching
- reset
- read-out



• Cova,
Lacaita,
Zappa et al
since early
90-ies
(Politecnico
Milano group)

Guerrieri, NDIP 2008

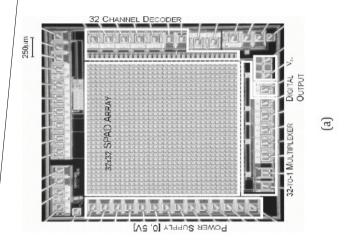


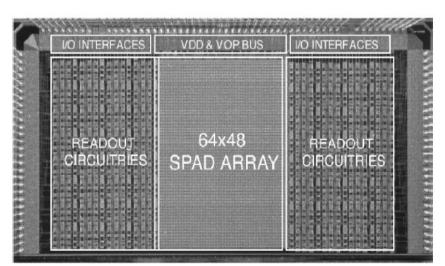
See also:

- Kindt et al
- Jackson et al Staples et al



Niclass, PhD Thesis EPFL (2008)





• Charbon, Rochas, Niclass, et al (EPFL Lousanne group)

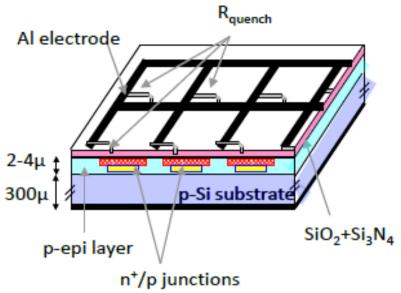
Large area, high efficient devices...

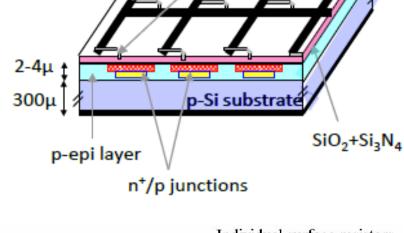
Transition SPAD sparse arrays → thousands of packed GM-APD (SPAD) cells is not just design... need addressing new issues:

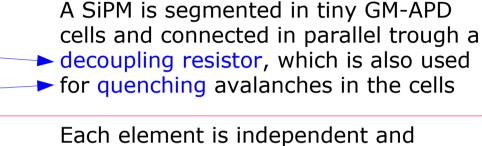
- an additional factor affects the photo-detection efficiency:
 the fill factor that for small cell size can be quite low
- how to control (over large areas) the dark rate because of
- limited space for gettering techniques
- high probability to include noisy cells in a device
- production yield and uniformity affect performances
- optical cross-talk among packed cells
- **electronics** (external, hybrid, integrated, #readout channels)

"Analog" SiPM: array of passively decoupled GM-APD

Single GM-APD gives **no information** on light intensity → use array of GM-APDs' first proposed in the late '80-ies by Golovin and Sadygov

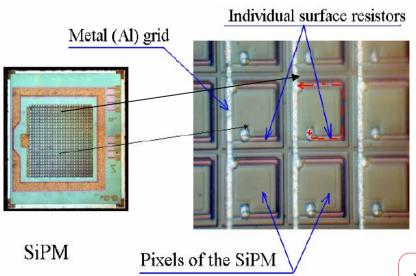


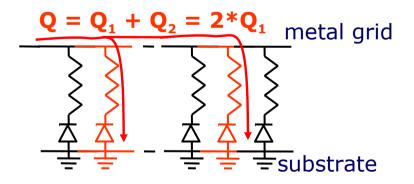




gives the same signal when fired by a photon

 Σ of binary signals \rightarrow analog signal



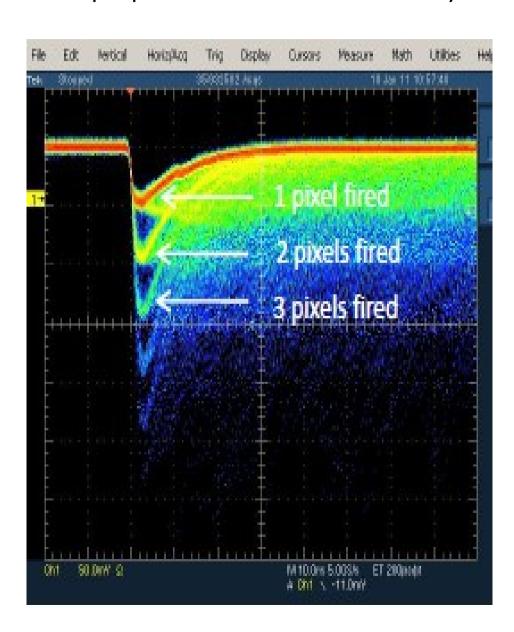


Output ∞ number incident photons

→ Linear response to multi-photon pulse

The Silicon PM: array of GM-APD

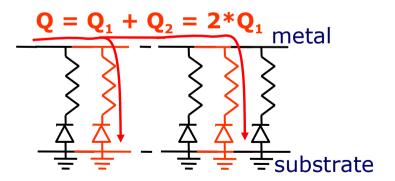
Single GM-APD gives **no information** on light intensity → use array of GM-APDs' first proposed in the late '80-ies by Golovin and Sadygov



A SiPM is segmented in tiny GM-APD cells and connected in parallel trough a decoupling resistor, which is also used for quenching avalanches in the cells

Each element is independent and gives the same signal when fired by a photon

 Σ of binary signals \rightarrow analog signal



Output ∝ number incident photons

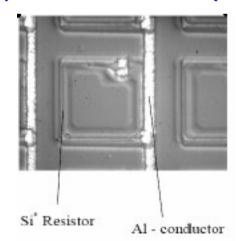
A bit of history

Pioneering work since late 80-ies at Russian institutes

Investigations of various multi-layer silicon structures with local micro-plasma suppression effect to develop low-cost GM-APD arrays

Early devices ageing quickly, unstable, noisy

Dolgoshein - MePhi/Pulsar (Moscow) Poly-silicon resistor (SiPM)

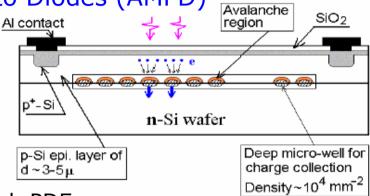


- Low fill-factor
- Simple fabrication technology

e.g., Dolgoshein, NIMA 563 (2006)

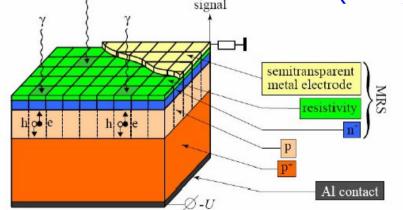
Sadygov – JINR/Micron (Dubna)

Avalanche Micro-channel/pixel Photo Diodes (AMPD)



- High PDE
- Very high density of micro-cells
 eg Sadygov, NIMA 567 (2006)

Golovin - Obninsk/CPTA (Moscow) Metal-Resistive-Semiconductor (MRS)



- High fill factor
- Good pixel to pixel uniformity

e.g., Golovin NIMA 539 (2005)

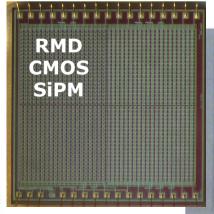
SiPM inexorable development

Many institutes (R&D) and companies involved

- → competition... and silicon price low...
- → asymptotically SiPM O(10€/cm²)
- CPTA, Moscow, Russia
- MePhi/Pulsar Enterprise, Moscow, Russia
- Zecotek, Vancouver, Canada
- Hamamatsu HPK, Hamamatsu, Japan
- FBK-AdvanSiD, Trento, Italy
- ST Microelectronics, Catania, Italy
- Amplification Technologies Orlando, USA
- SensL, Cork, Ireland
- MPI-HLL, Munich, Germany
- RMD, Boston, USA
- **Philips**, Aachen, Germany
- Excelitas tech. (formerly Perkin-Elmer)
- **KETEK**, Munich, Germany
- National Nano Fab Center, Korea
- Novel Device Laboratory (NDL), Bejing, China



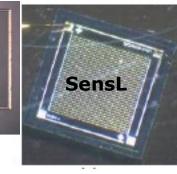


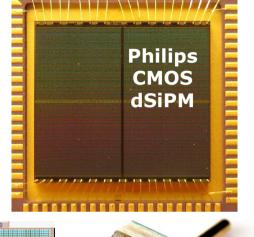


NanoFab

Korea

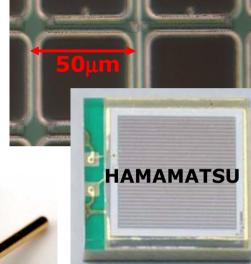


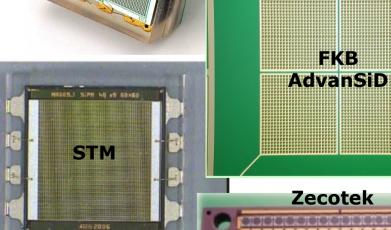




KETEK

HLL







Excelitas

Few samples among many

ZECOTEK MAPD-3N



FBK-AdvanSiD

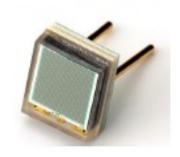


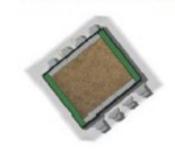
HAMAMATSU S10985



KETEK PM3350

STMicroelectronics





Producer	Reference	Area (mm²)	PDE max @ 25 °C *	Dark Count Rate (Hz) @ 25°C *	Gain *
ZECOTEK	MAPD-3N	3 x 3	30% @ 480 nm 9.10 ⁵ – 9.10 ⁶		10 ⁵
FBK - AdvanSiD	ASD-SiPM4S	4 x 4	30% @ 480 nm	5.5 10 ⁷ - 9.5 10 ⁷	4.8 10 ⁶
HAMAMATSU	S10985-50C	6 x 6	50% @ 440 nm (includes afterpulses & crosstalk)	6.10 ⁶ - 10.10 ⁶	7.5 10 ⁵
KETEK	PM3350	3 x 3	40% @ 420 nm	4.106	2 10 ⁶
STMicrolectronics	SPM35AN	3,5 x 3,5	16% @ 420 nm	7.5 10 ⁶	3.2 10 ⁶

* datasheet data

Ongoing R&D to increase the active area at KETEK, AdvanSiD, Excelitas (6 x 6 mm²)

Other solution to get larger area: connection of several channels of a matrix

Discrete arrays

Producer	Device ID	Picture	Total area (mm²)	SiPM area (mm²/channel)	Nr. channels	μcell size
Hamamatsu	S11064-025P S11064-050P		18 x 16.2	3x3	16(4x4) ch	25x25 μm 50x50 μm
Hamamatsu	C11206-0404DF			3x3	64(8x8) ch	
Hamamatsu	S11834-3388DF	64 mm	72x64.8	3x3	256(16x16)ch	
FBK AdvanSiD	ASD-SiPM4s-P-4x4T- 50 ASD-SiPM4s-P-4x4T- 69		8.2 x 8.2	4x4	16(4x4) ch	50x50 μm 69x69 μm
FBK AdvanSiD	SiPMtile		32.7x32.7	4x4	64(8x8) ch	
SensL	ArraySM-4P9 ArraySB-4P9 (blue sensitive)		46.3 x 47.8	3x3	144(12x12) ch (based on monolithic Array SM4)	35x35 μm

Monolithic Arrays → fill factor, uniformity, yield ...cost

Producer	Device ID	Picture	Effective area (mm²)	SiPM area/channel (mm²)	Nr. channels	μcell size
Hamamatsu	S10984-025P S10984-050P S10984-100P		1x4	1x1	4(1x4)ch	25x25 μm 50x50 μm 100 x 100 μm
Hamamatsu	\$10985-025C \$10985-050C \$10985-100C		6 x 6	3x3	4(2x2)ch	25x25 μm 50x50 μm 100 x 100 μm
Hamamatsu	S11828-3344M		12 x 12	3x3	16(4x4)ch	50x50 μm
FBK AdvanSiD	ASD-SiPM1.5s-P- 8X8A		11.6x 11.6	1.45x1.45	64(8x8) ch	50x50 μm
FBK AdvanSiD	ASD-SiPM3S-P- 4X4A		11.8x 11.8	2.95x2.95	16(4x4)ch	50x50 μm
SensL	Array SM-4 Array SB-4 (blue sensitive)		12 x 12	3x3	16(4x4)ch	35x35 μm

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"Digital SiPM": packed CMOS SPAD array with electronics integrated

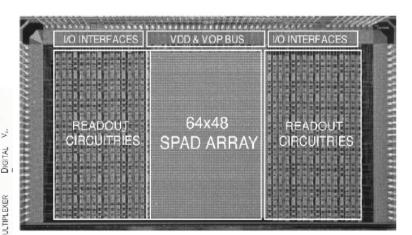
- Kindt et al ... earliest studies
- Staples, Johnson et al

- Jackson et al
- Filkenstein et al
- Stoppa, Pancheri et al

 SPADnet consortium

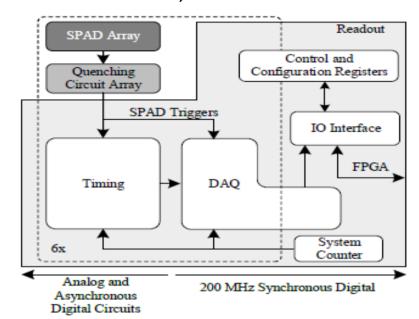
POWER SUPPLY [0, 5V]

• Charbon, Niclass et al (EPFL Lousanne group)

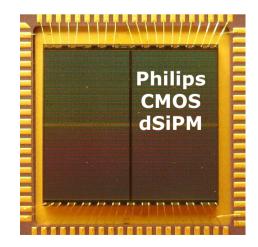


Niclass, PhD Thesis EPFL (2008)

• R.Fontaine, J.F.Pratte et al

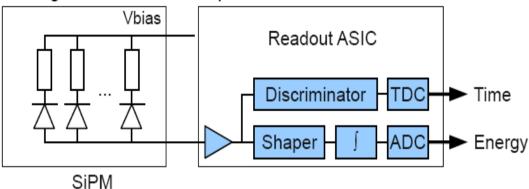


T.Frack et al



Competition: Analog vs Digital SiPM

Analog Silicon Photomultiplier Detector

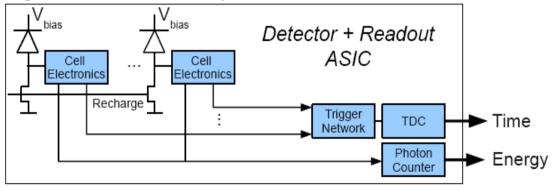


T.Frach - Heraeus Seminar 2013

Analog SiPM (Ta=25 °C, M=7.5 × 10°) Time www.hamamatsu.com • Cells connected to common readout

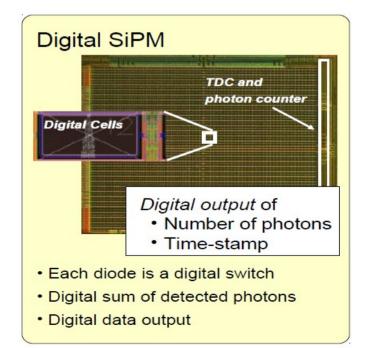
- · Analog sum of charge pulses
- · Analog output signal

Digital Silicon Photomultiplier Detector



d-SiPM:

- for each light pulse → output is:
 time-stamp and number of photons
- control of individual cells
- O(500ns) RO dead time (upon trigger)



Physics & Technology Key features

- Closeup of a cell Custom vs CMOS
- Guard Ring and Optical isolation
- Operation principles of GM-APD and quenching modes

Silicon technologies

Two different approaches for SPAD or GM-APD arrays

Custom technology

- control/tune shape of E field
 - → high PDE
 - → optimized timing resolution
 - → low Dark Count Rate (DCR)
 - → low After-Pulsing (AP)
- possible both Planar and Reach Through
 - → tune spectral sensitivity
- limited integration electronics
 (no libraries for complex functionalities and for deep-submicron features)
 - → simple integrated electronics (few large MOS)
 - → it limits array dimensions and fill factor

Ancillary electronics (quenching/readout):

- → completely external → SiPM
- → hybrid → **SPAD arrays**... complex fabrication

CMOS HV technology

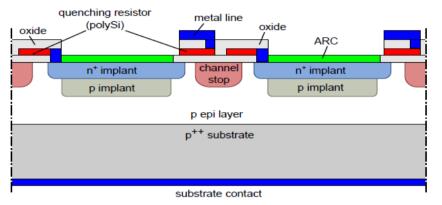
- no optimization of shape of E field
 - + high curvature sub-micron tech.
 - → special care for guard ring (GR) (limited range of GR possible only STI demonstrated ok)
- only Planar structures
 - → better UV/Blue sensitivity
- fully supported sub-micron technology with models and libraries →complex electr.
 - → processing of large amount of data
 - → high density → imaging
 - → ultra-fast timing

Ultrafast and/or imaging monolithic SPAD arrays

Silicon technology - few examples

Custom technology

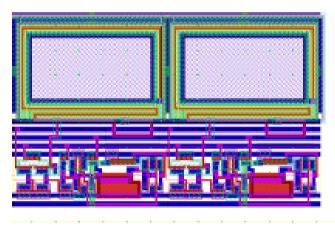
SiPM "RGB" FBK - external electronics



N.Serra et al JINST 8 (2013) P03019

CMOS HV technology

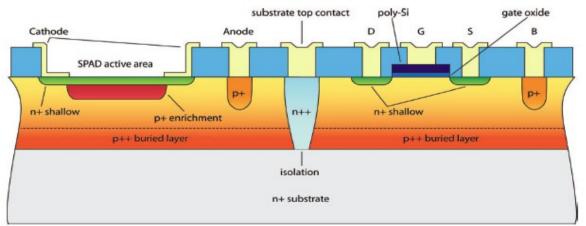
integrated electronics

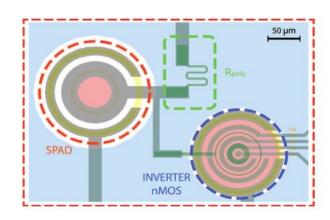


Stapels et al Procs. SPIE 7720 2009

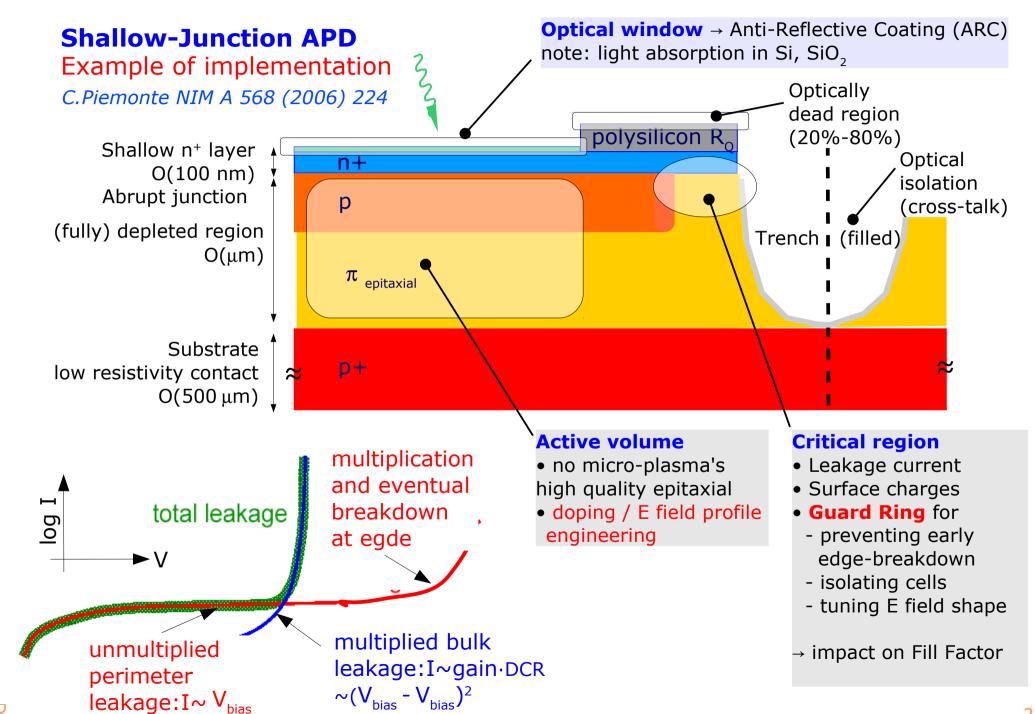
Custom CMOS technology

SPAD array - **hybrid** electronics





Close up of a cell - custom process



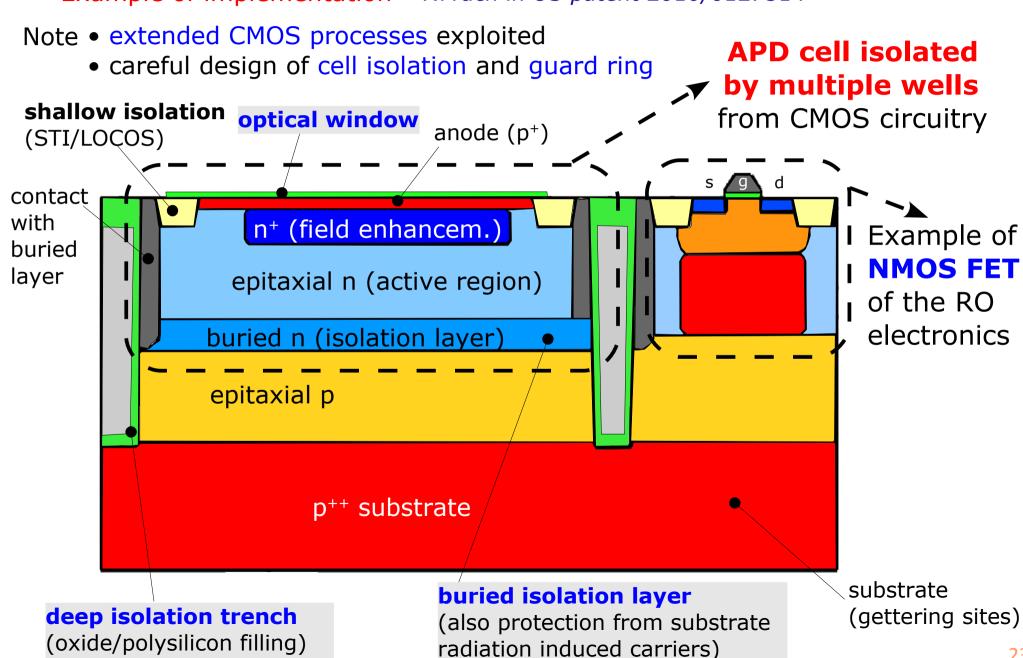
Close up of a CMOS cell

Key elements for CMOS SiPMs

- APD cell isolation from CMOS circuitry
- guard ring

APD integration into CMOS

Example of implementation T.Frach in US patent 2010/0127314



CMOS vs Custom processes

"Standard" CMOS processes _ tunneling high E field __ shallow implant depths lattice stress $(low V_{hd})$ high doping concentrations → (defects/traps) shallow trench isolation (STI) high DCR deep well implants (flash extension) no extra gettering and high T annealing limited PDE non optimized optical stacks (often p-on-n) design rule restrictions limited timing performances (long diffusion tails)

Recent progresses in CMOS APDs due to:

- 1) high voltage (flash) extension often available in standard processes
 - deep wells (needed for the high voltages used in flash memories)
- 2) Additional processes (custom) available:
 - buried implants
 - deep trench isolation
 - optical stack optimization

Key elements for CMOS SiPMs

- APD cell isolation from CMOS circuitry
- guard ring

Physics & Technology Key features

- Closeup of a cell Custom vs CMOS
- Guard Ring and Optical isolation
- Operation principles of GM-APD and quenching modes

The Guard Ring

Guard Ring is needed to:

- avoid premature edge breakdown (due to junction's high curvature)
 - → either reduce electric field at edge (floating GR)
 - → or by terminating electric field lines "within" the high field region
 - → or by exploiting special edge geometry (trenches)
- drain leakage currents (for avoiding its multiplication)
- electrical isolation of cell from electronics
- optical isolation of cell against cross-talk

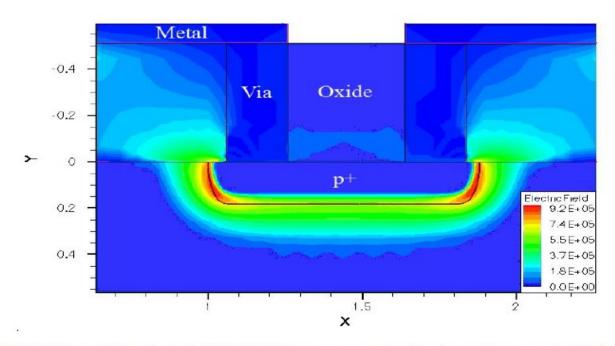
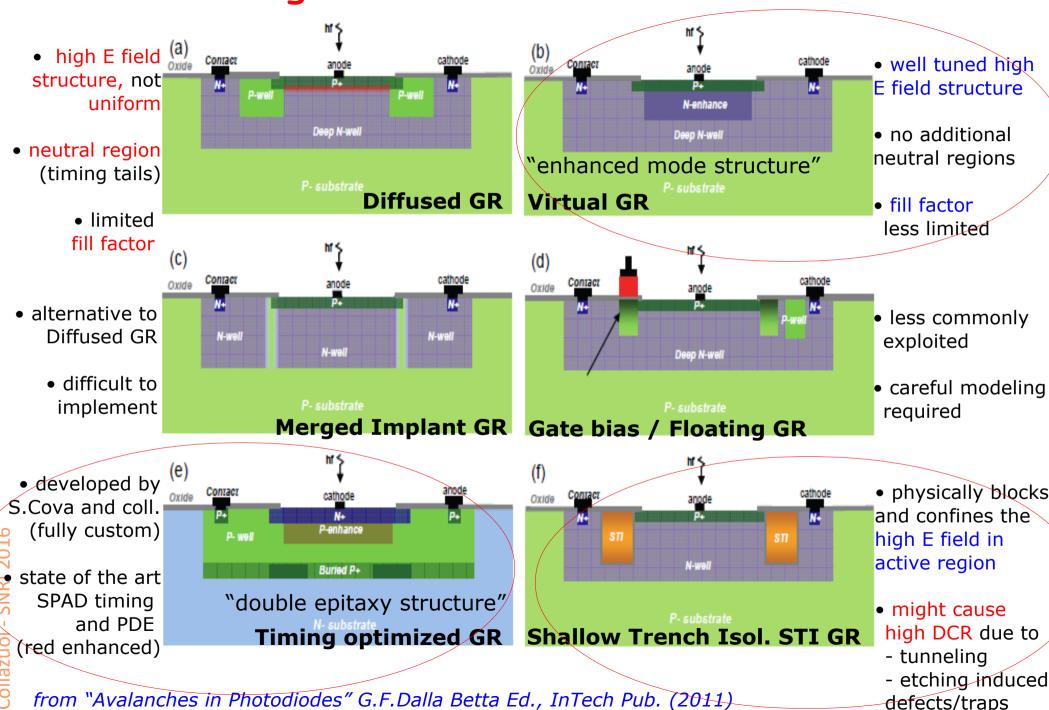


Fig. 1. ISE-TCAD simulation of electric field distribution across a pn junction formed by consecutive implantation and diffusion steps. A uniform field exists in the planar junction region but the field is significantly higher in the curved regions, resulting in premature breakdown and in a higher avalanche probability in these areas. Field strengths are in V/cm and coordinates are in microns.

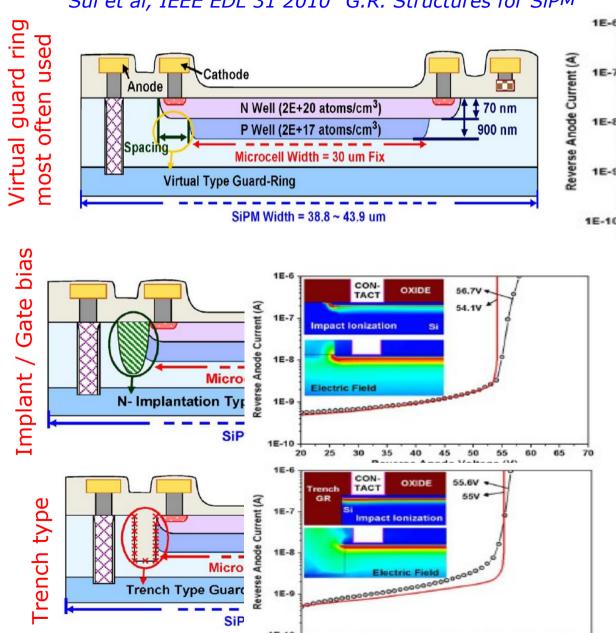
Finkelstein et al. "An ultrafast Geiger-mode SPAD in 180nm CMS technology" Procs. SPIE 6372 2006

The Guard Ring structure

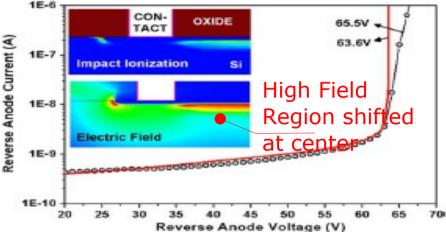


Guard Ring structures in SiPM

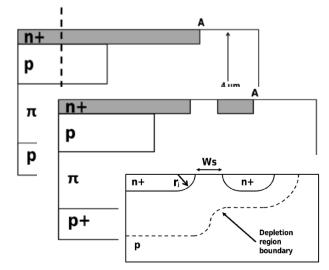
Sul et al, IEEE EDL 31 2010 "G.R. Structures for SiPM"



Reverse Anode Voltage (V) (f)



Maresca et al. Proc. of SPIE Vol. 8072 "Floating field ring ... to enhance fill factor of SiPM"



Physics & Technology Key features

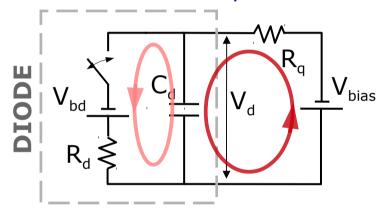
- Closeup of a cell Custom vs CMOS
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Operation principle of a GM-APD

Avalanche processes in semiconductors are studied in detail since the '60 for modeling micro-plasma instabilities

McIntyre JAP 32 (1961), Haitz JAP 35 (1964) and Ruegg IEEE TED 14 (1967)

currents internal / external



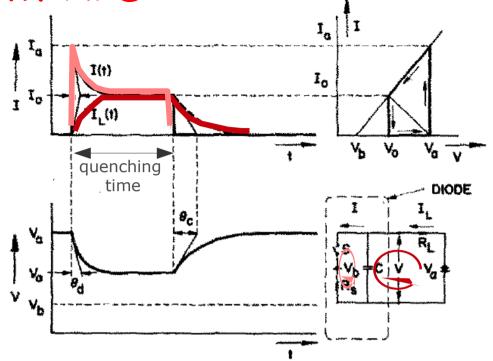


Fig. 3. Shape of current pulse for $\theta_d \ll r_1(I_0)$.

ON condition: avalanche triggered, switch closed C_d discharges to V_{bd} with a time constant $R_dC_d = \tau_{discharge}$ at the same time the external current asymptotic grows to $(V_{bias}-V_{bd})/(R_q+R_d)$

P₁₀ = turn-off probability
probability that the number of
carriers traversing the
high-field region

fluctuates to 0



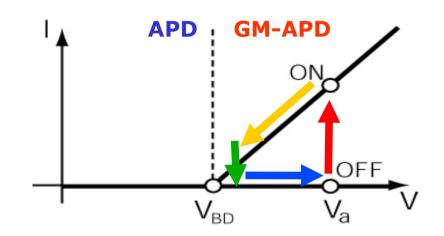
P₀₁ = turn-on probability probability that a carrier traversing the high-field region triggers the avalanche

OFF condition: avalanche quenched, switch open, capacitance charged until no current flowing from V_{bd} to V_{BIAS} with time constant $R_qC_d = \tau_{recovery}$

Operation principle of a GM-APD

Avalanche processes in semiconductors are studied in detail since the '60 for modeling micro-plasma instabilities

McIntyre JAP 32 (1961), Haitz JAP 35 (1964) and Ruegg IEEE TED 14 (1967)



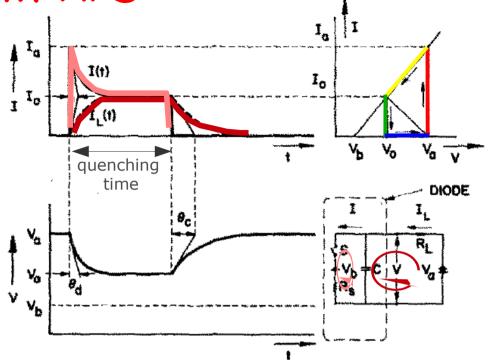
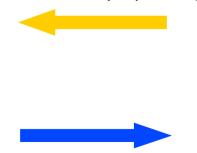


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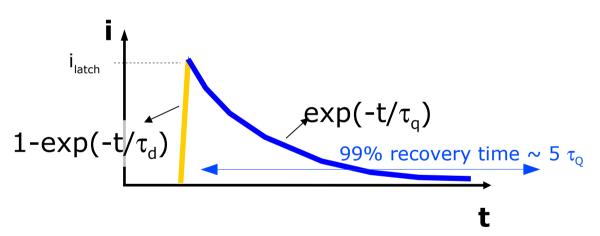
Operation model and ideal pulse shape

Diode (capacitor) fast discharge and slow recharge

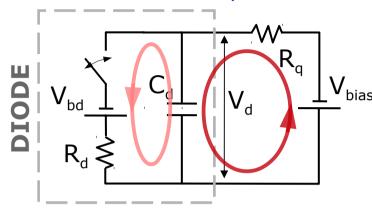
charge stored defines Gain

→ Gain ~ C ΔV

$$\Delta V = V_{\text{bias}} - V_{\text{bd}}$$
 "Over-Voltage"



currents internal / external



Rise time Fall time (recovery)

$$\tau_{\rm d} = R_{\rm d}C_{\rm d}$$

$$\tau_{\rm d} = R_{\rm d}C_{\rm d} \ll \tau_{\rm q} = R_{\rm q}C_{\rm d}$$

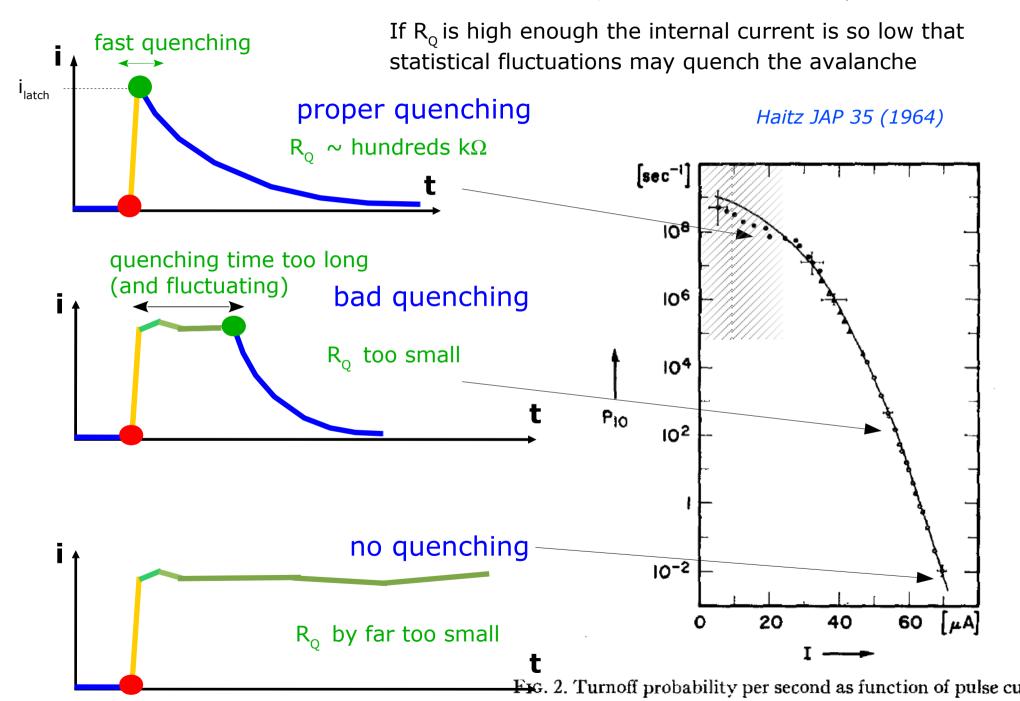
Gain → linear with ΔV (\neq APD)

- → no intrinsic fluctuations !!! (≠ APD)
- \rightarrow independent of T at fixed ΔV (\neq APD)

Rise time T dependence (weak) due to R_d

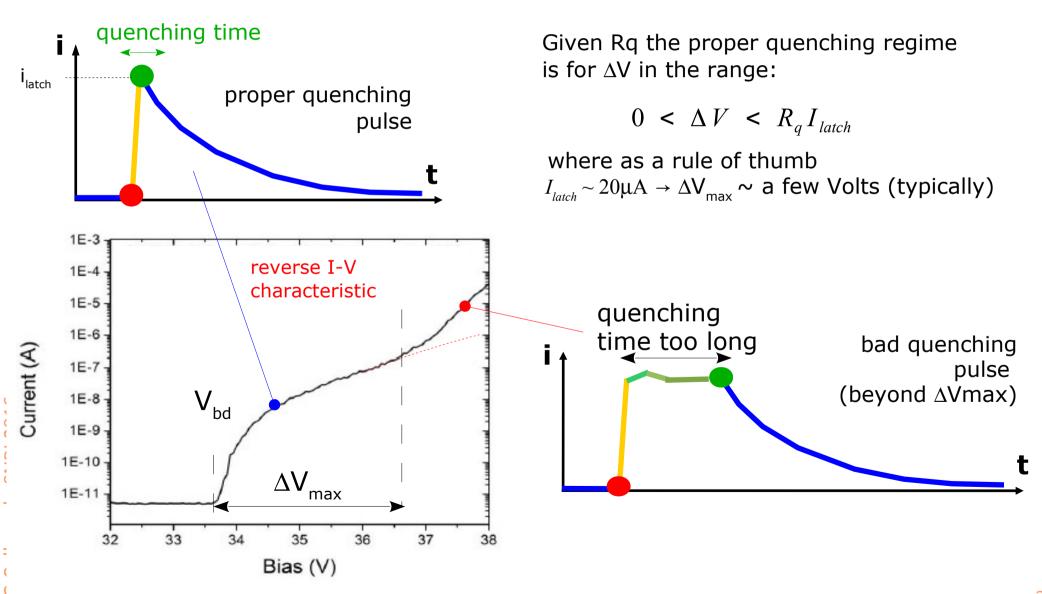
Recovery time T dependence (strong) due to R_a C_d is independent of T

Passive Quenching: tread-off τ_{quench} vs $\tau_{recovery}$



Passive Quenching Regime

Proper value of quenching resistance Rq is crucial to let the internal current decrease to a level such that statistical fluctuations may quench the avalanche → sub-ns quenching time → crucial to have well defined gain



Operative ΔV Range - I_{dark}/DCR

Operative overvoltage (ΔV) range limited by:

- 1) $I_{latch} \sim 20 \mu A \rightarrow \Delta V < I_{latch} R_q$ (non-quenching regime)
- 2) Dark Count Rate (DCR) acceptable level ← PDE vs ∆V ← E field shape
- 3) V_{bd}^{edge} edge breakdown (usually some 10V above V_{bd})

A practical method for estimating the operative range, limited by effect 1), is to measure the ratio R_I of the measured dark current I_D to the dark current I_D calculated from the measured dark rate and pixel count spectra:

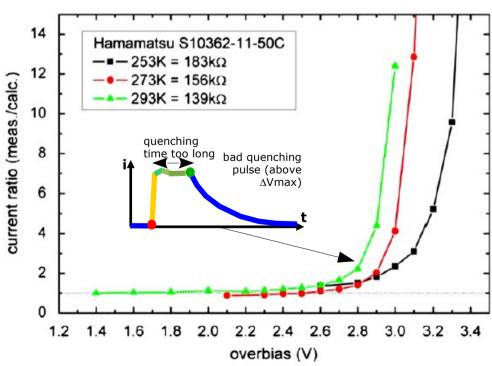
after Jendrysik et al NIM A 2011 doi:10.1016/j.nima.2011.10.007

$$\rho_{I} = \frac{I_{D}}{I_{D}^{'} = DCR \cdot \bar{N} \cdot G \cdot q_{e}}$$

where \overline{N} is the average N of fired cells

Non-quenching regime for values of ΔV when $R_{_{\rm I}}$ deviates significantly from 1

Jendrysik et al suggest R₁=2 as reasonable threshold



Excess Charge Factor (ECF)

The above mentioned current ratio is indeed a measure of total correlated noise ("Excess Charge Factor", after N.Serra et al JINST 8 P03019)

- → It is **not** to be confused with **multiplication noise**!
- → It measures any extra charge due to correlated noises
- After-pulsing
- Cross-Talk
- Limited Quenching

Note: depending on the device type (Temperature and Rq) the kind of "second breakdown" is observed when ECF \neq 1 might be due either to:

- Limited Quenching → Jendrysik et al NIM A 2011
- or After-Pulsing reaching Probability=1

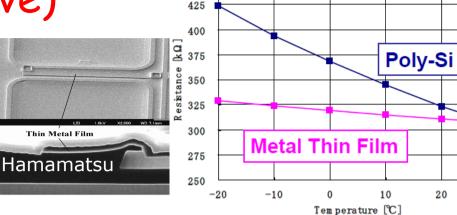
→ A. Nagai, N. Dinu, A. Para, IEEE NSS 2015

G.Collazuol

Passive Quenching (Resistive)

- 1) common solution: poly-silicon
- 2) alternative: metal thin film
- → higher fill factor
- → milder T dependence

Nagano **IFFF** NSS-MIC 2011



- 3) alternative principle: bulk integrated resistor
- → flat optical window → simpler ARC
- → fully active entrance window
 - → high fill factor (constraints only from guard ring and X-talk)
- → diffusion barrier against minorities
 - → less X-talk
- \rightarrow positive T coeff. (R \sim T^{+2.4})
- → production process simplified → cost

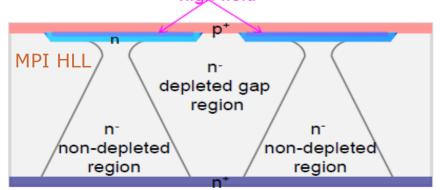
← Rg matching constraints pros cells' pitch/wafer thickness

> ← vertical R is JFET contra-

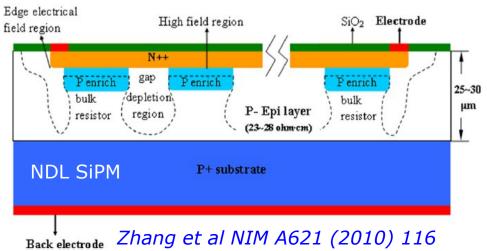
→ non-linear I-V

→ long recovery

Ninkovic et al NIM A610 (2009) 142 and NIM A628 (2011) 407 Richter et al US patent № 2011/0095388

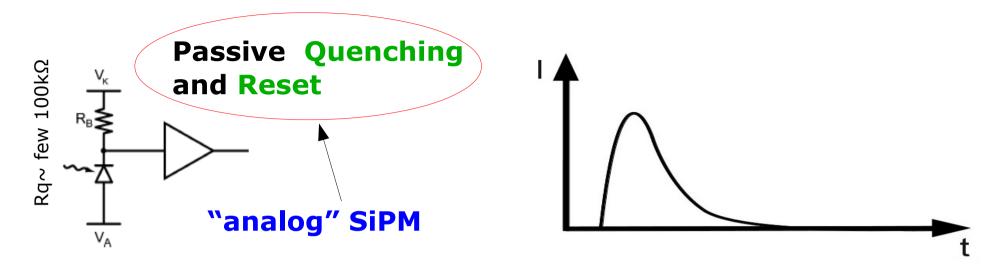


principle proved



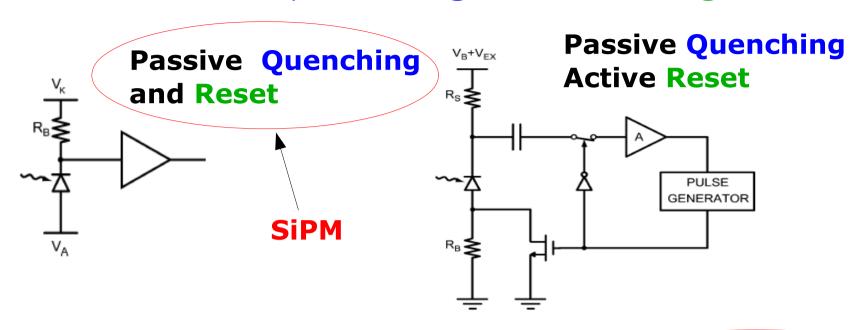
20

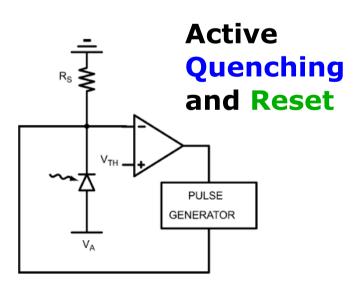
Passive mode: quenching resistor

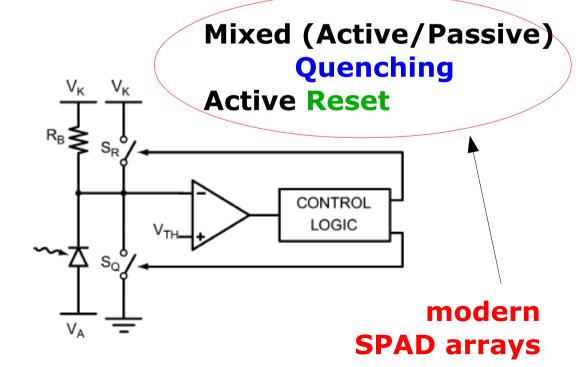


- "Quenching resistor" regulates both quenching and recharge
- Simple concept but high-ohmic resistors needed
- Allows easy implementation of summation (full energy, 'average' time)
- Constraints due to passive mode: latch current level (20μA)
 - → large charge developed before quenching
- \rightarrow limited recharge current (R_q \sim ΔV/20μA for safe quenching \rightarrow I_r < 20μA) ("long" recovery time: τ_r \sim Rq x Cd)
- Output signal compatible with that of PMTs → re-use of readout infrastructure

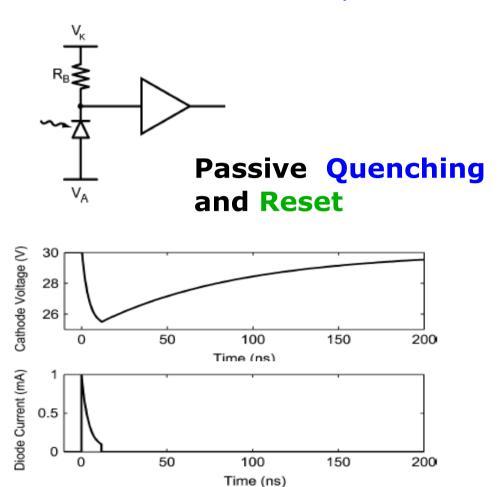
Passive / Active quenching and recharge (reset)

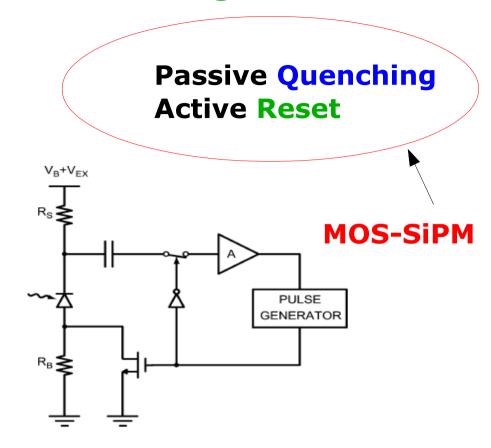






Passive / Active quenching and recharge (reset)





- recharge ok
- hold-off limited by passive recharge

Need active elements for gaining control over:

- quenching time against fluctuations (if Rq small)
 - → avalanche charge (→ limiting AP and cross-talk)
- slow recovery / reset time

 → dead-time and no gating → dead-time and no gating nor hold-off

During the hold-off the detector is kept biased below breakdown (after avalanche quenching) in order to exhaust trapped carriers

MOS-SiPM (new "analog" SiPM structure)

Passive quenching + active recharge

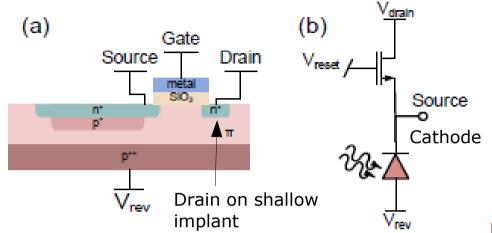


Fig. 1: (a) Structure of the MOS-SiPM cell, showing the transistor partially merged with the SPAD. (b) Schematic circuit of the microcell.

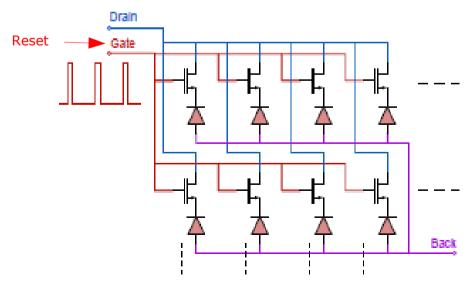


Fig. 2: Schematic circuit of the MOS-SiPM, showing the connections of the microcells.

Gola, Piemonte, Acerbi IEEE NSS 2013 (FBK-Advansid)

- MOSFET transistor replaces quenching R
- custom process
- no losses in Fill Factor
- cheaper than standard analog SiPM
- Operation : periodic reset

Features

- → "hottest" cells self-disabled (like in d-SiPM)
 - → low Dark Count device
- → After-pulsing suppressed almost completely
- → Very fast signal ~2ns width

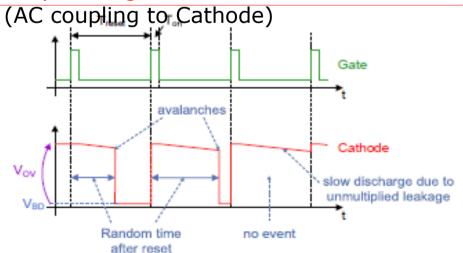
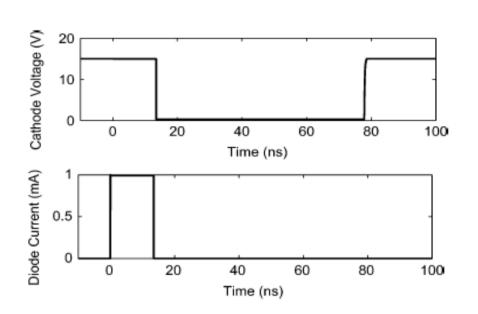
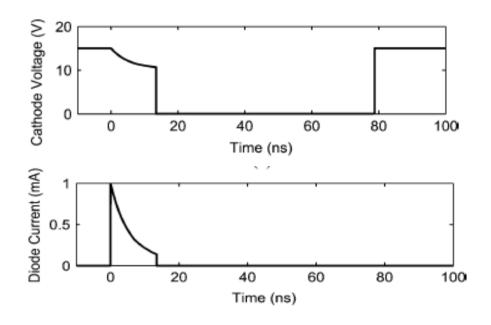
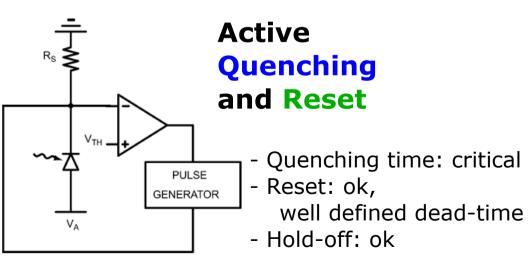


Fig. 3: Working principle of the MOS-SiPM, which is operated in a periodic pulsed reset mode.

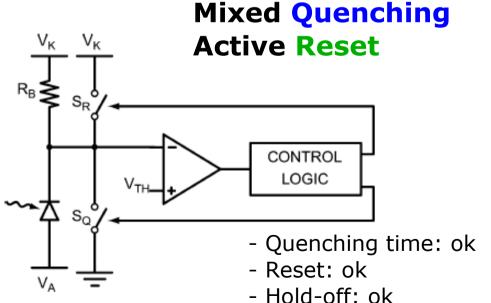
Passive / Active quenching and recharge (reset)







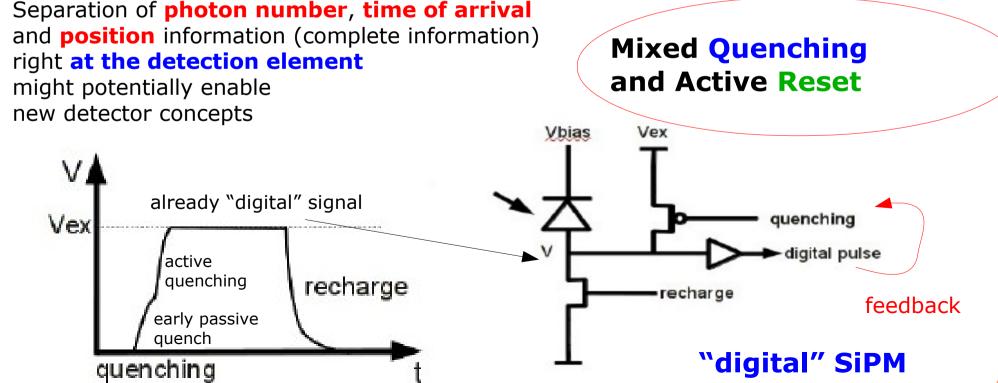
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.. integrated circuits quite complex → arrays: hybrid or CMOS

Active mode: transistors to Quench and Reset

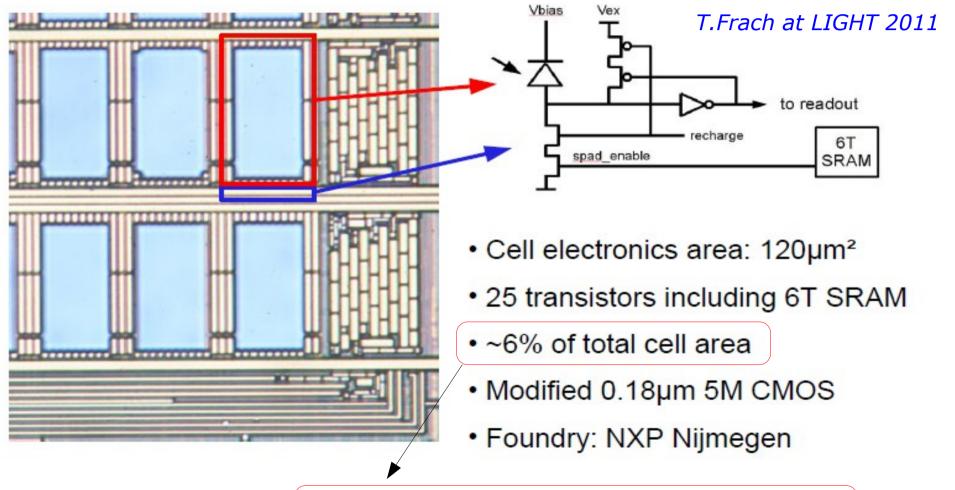
- Sense the voltage at the diode terminal
- Use transistors to actively **discharge/recharge** the diode
 - → controlled amount of charge → reduced after-pulsing and cross-talk
 - → controlled (fast) recovery
- Flexibility: programmable timing possible, disabling of faulty cells
- Electronics area not active (unless 3D integ.): higher cost & lower fill factor
- Electronics exposed to radiation: hardness?
- Fast digital signals (gate delays of ~30ps, rise/fall times ~90ps), low parasitics



Active mode → "digital" SiPM

Philips Digital SiPM
APD cells & integrated electronics

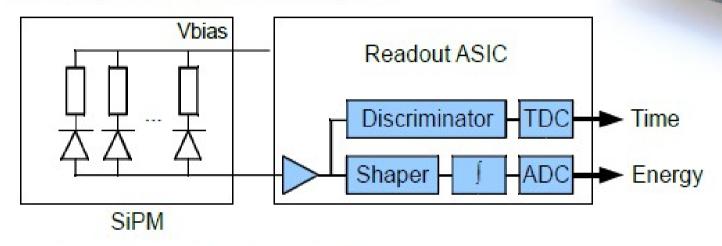
- Cell area $\sim 30 \times 50 \mu m^2$
- Fill Factor ~ 50%



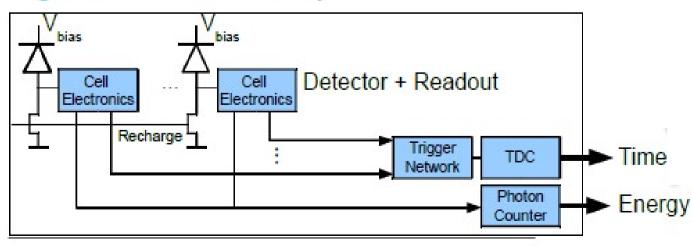
- reduced Fill Factor
- electronics exposed to radiation
 - → additional radiation weakness

Analog vs Digital SiPM

Analog Silicon Photomultiplier



Digital Silicon Photomultiplier



Key features: main parameters vs V_{bd} and T

related to the recharge of the diode capacitance from V_{bd} to V_{bias} during the avalanche quenching time after I_{latch} is reached

Gain, Pulse shape (analog SiPM) Dynamic Range, **Linearity**

pulses triggered by non-photo-generated carriers (thermal / tunneling generation in the bulk or in the surface depleted region around the junction)

Primary **noise**:

- → thermally generated Correlated noise:
- → after-pulses, cross-talk

carriers can be trapped during an avalanche and then released triggering another avalanche

photo-generation during the avalanche discharge. Some of the photons can be absorbed in the adjacent cell possibly triggering new discharges

 $PDE = QE * P_{01} * \varepsilon$

QE = quantum efficiency

 P_{01} = avalanche triggering prob.

 ε = geometrical fill factor

PDE (Photo-detection efficiency)

Related to the photo-generation and to the avalanche propagation

Time resolution

But... wait a minute: ??? how to measure

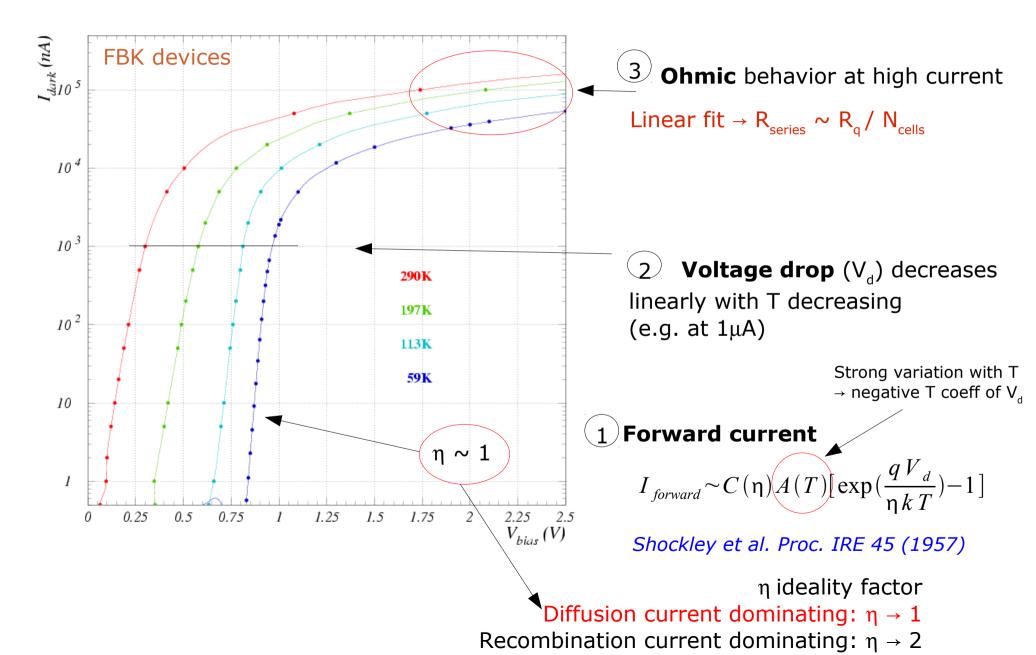
- breakdown voltage V_{bd}
- junction temperature T_j
- quenching resistor R_q

I-V characteristics

```
- Information from Forward current → Rq
- junction Temperature
```

```
- Information from Reverse current → - breakdown V<sub>bd</sub> - T coefficient
```

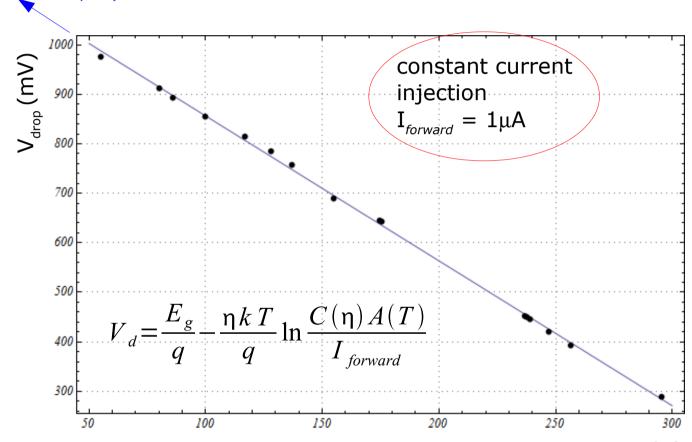
I-V characterization: forward bias



Forward I-V → Junction Temperature probe

Voltage drop at fixed forward current → precise **measurement of junction T**...

for T→0 ideally V_d→ Eg ... otherwise not trivially measured! (freeze-out effects apart)

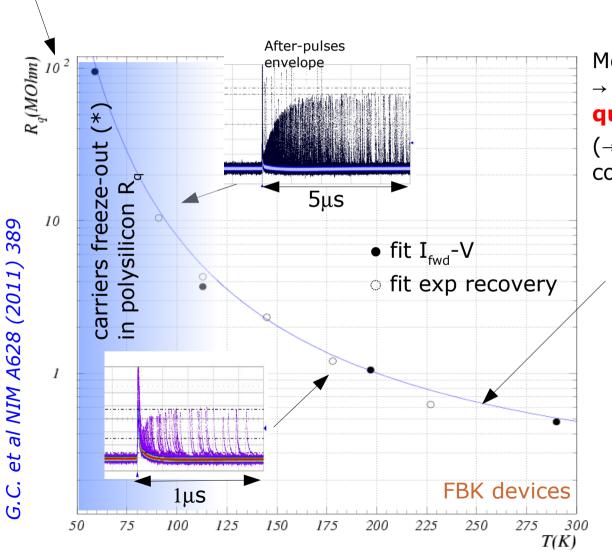


- (almost) linear dependence with slope $dV_{drop}/dT|_{1\mu A} \sim -3mV/K^{T}(K)$ (we don't see freeze-out effects down to 50K)
- direct and precise calibration/probe of junction(s) Temperature

Forward I-V → Series Resistance (vs T)

Two ways for measuring series resistance (R_s)

- 1) Fit forward characteristic high I-V region
- 2) Exponential recovery time (after-pulses envelope)



Measurements (1) and (2) consistent

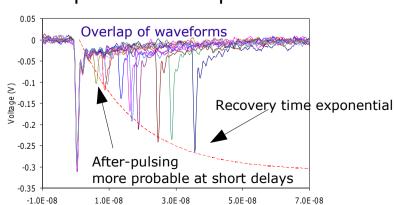
→ dominant effect from
quenching resistor R_a

(→ series R bulk gives smaller contribution)

Empirical fit:

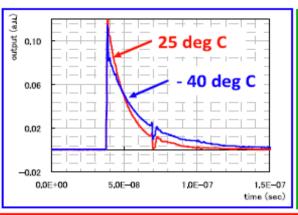
$$R_q(T) \sim 0.13 (1 + 300/T e^{300/T}) M \Omega$$

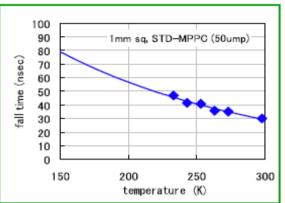
Afterpulses envelope



Note: SiPM for low T applications must have appropriate quenching R (not quenching at room T!)

Quenching resistor

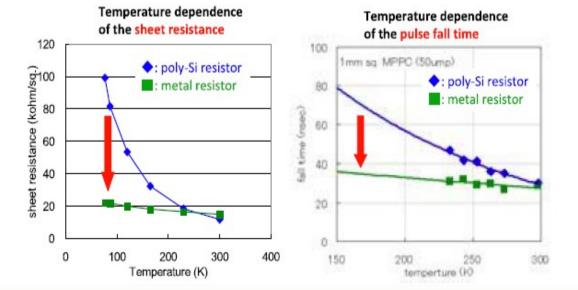




The quenching resistor value increases as environmental temperature decreases. The larger resistor makes the pulse amplitude lower and the tail longer.



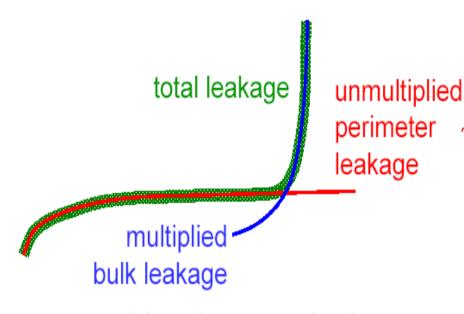
Adopting metal quenching resistor



Improved temperature stability

Metal quenching resistor achieved 1/5 temperature dependence

Reverse I-V



 \sim q · Gain (G) · Dark Count Rate (DCR) \sim q · Δ V · Δ V → quadratic with Δ V

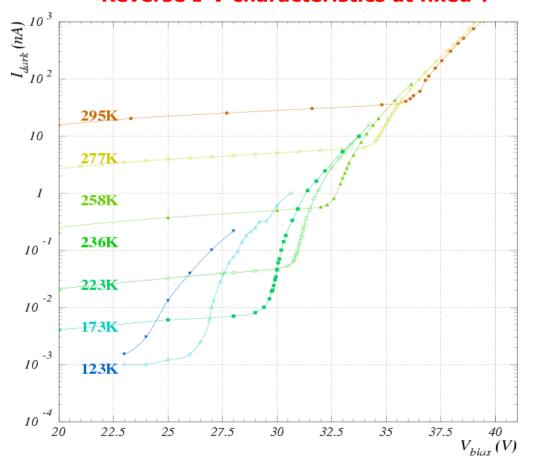
Note:

- G is linear with ΔV
- Dark Count Rate is \sim PDE which is linear with Δ V (at least for few volts)

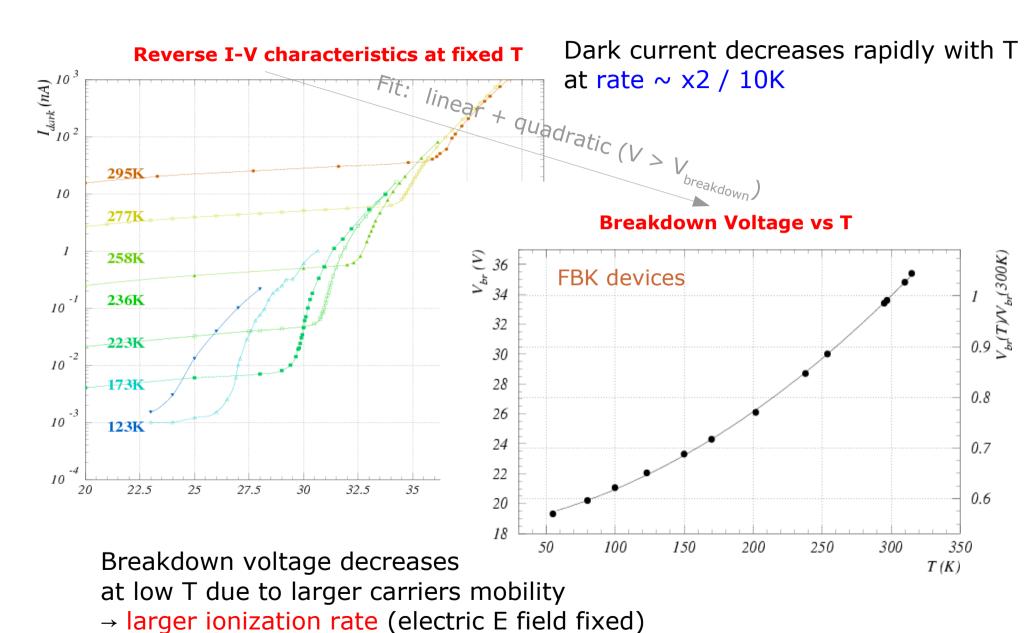
→ Dark Current behaviour and V_{bd} measurement

 \sim linear with Vbias \rightarrow linear with Δ V (overvoltage)

Reverse I-V characteristics at fixed T



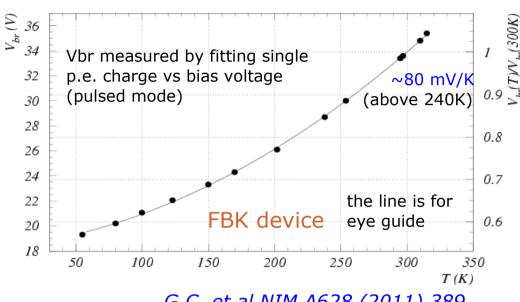
Reverse I-V \rightarrow Dark Current and V_{bd}



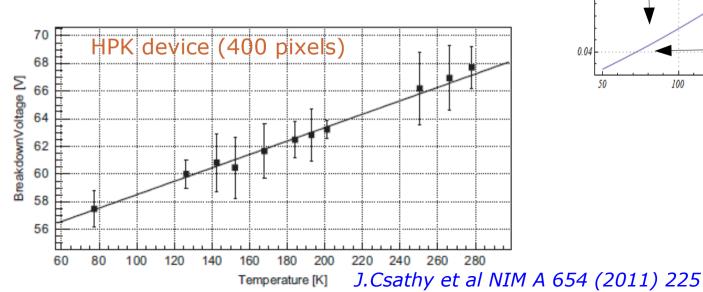
G.C. et al NIM A628 (2011) 389

V_{bd} vs T \rightarrow T coefficient (ΔV stability)

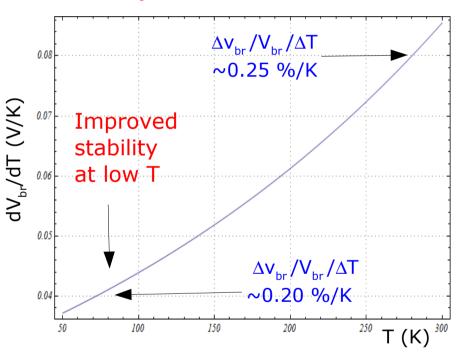
Breakdown Voltage



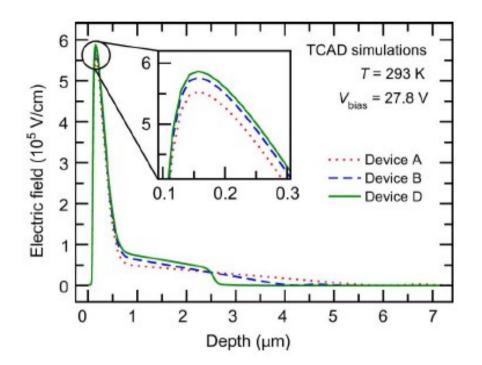
G.C. et al NIM A628 (2011) 389



Temperature coefficient



Depletion layer $\rightarrow V_{bd}$ dependence on T



Narrow depletion layer (high background doping(*) or thin epitaxial layer)

→ minimize V_{bd} dependence on T

$$\rightarrow$$
 gain stability $\frac{\delta V_{bd}/V_{bd}}{\delta T} = \frac{\delta G/G}{\delta T}$

(*) resulting in epitaxial layer not fully depleted at V_{bd}

Trade off against:

- → PDE (thickness)
- → minimum gain (cell capacity) against after-pulses and cross-talk

Serra et. al. (FBK) IEEE TNS 58 (2011) 1233 "Experimental and TCAD Study of Breakdown Voltage Temperature Behavior in n+/p SiPMs"

Note: precise agreement simulation/data is not trivial at all. Definition of ionization coefficients is device dependent...

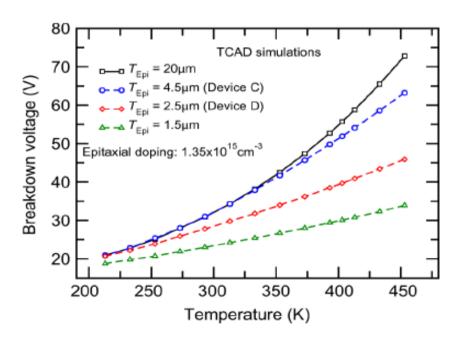
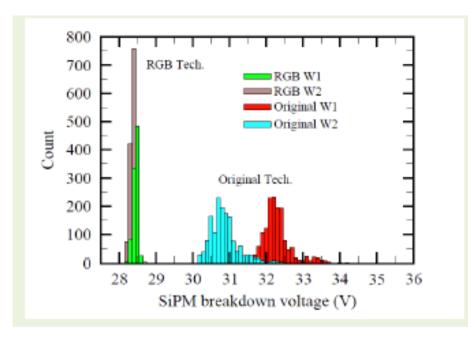


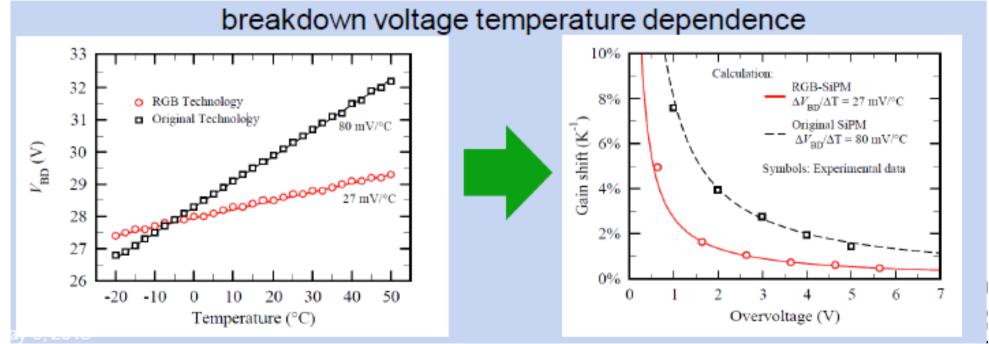
Fig. 9. TCAD simulated $V_{\rm BD}$ in the GM-APDs of this work (see Table I) in an extended temperature range. Two additional epitaxial layer thickness are considered (20 μ m, 1.5 μ m) to emphasize the impact of the depletion layer width on the $V_{\rm BD}$ vs. temperature characteristic.

Improved V_{bd} uniformity and T coefficient



Recent FBK-Advansid devices

breakdown voltage non-uniformity strongly reduced both at wafer level and from wafer to wafer



Methods to measure... which breakdown V_{bd} ?

- 1. "DC mode" → I-V curve fit according to a model
- 2. "Pulsed mode" → fit gain (charge) from single photon spectrum
- ... both methods can be carried on with and without light

Simplest DC model → linear slope (V<Vbd) + quadratic (V>Vbd) ... second order effects should be included, as the following:

- Miller or McIntyre terms for APD gain below breakdown
- turn off V < turn on V (hysteresis effect)
- After-pusing and Cross-talk contribution (far above breakdown)

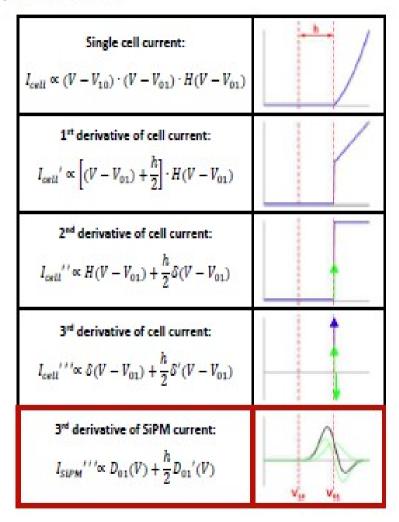
Hysteresis might explain why usually one finds $V_{bd}^{pulsed} < V_{bd}^{DC \, mode}$ Indeed while the pulse charge (gain) is mainly affected by avalanche turn-off probability (vanishing at V_{off}), the current besides gain includes a avalanche turn-on probability $P_{turn-on}$ term vanishing at $V_{on} > V_{off}$

Detailed discussions about these issues might be found in the papers:

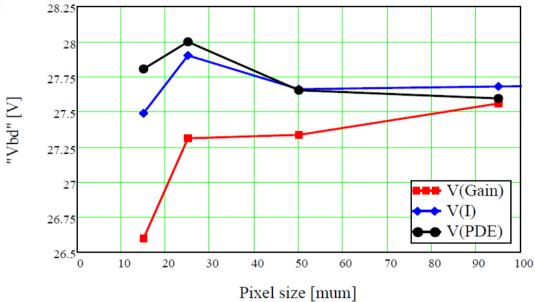
- G.Zhang, D. Han, C.Zhu, and X.Zhai, J.Semicond. 33 2012
- A.Nagai, N.Dinu, A.Para, IEEE NSS 2015 and NIM A 2016
- A.V.Chmill, E.Garutti, et al NIM A 2016
- F.Nagy, G.Hegyesi, G.Kalinka and J.Molnár, arXiv 1606.07805

Methods to measure breakdown V_{bd}

Table 2: Step by step explanation of the DC model. H(V) is the Heaviside step function and $\delta(V)$ is $\stackrel{\iota_{\bullet}}{=}$ derivative, the Dirac delta function.



F.Nagy, G.Hegyesi, G.Kalinka and J.Molnár, arXiv 1606.07805



- $_{\bullet}$ There is a significant difference between $V_{_{\rm I}} V_{_{\rm gain}}$
- ΔV decreases with increasing pixel size
 → to be understood!
- $V_I \approx V_{PDE}$ = Geiger breakdown voltage V_{bd} (cross check: no signal seen on scope for V < VI for many photons from LED)
- V_{Gain} is the voltage relevant for the user Gain \sim (Vbias-VGain)
- Model calculations underway to understand relation Vturn-off vs VGain vs Vbd

A.V.Chmill, E.Garutti, et al VCI 2016

Pulse shape, Gain and Response

!!! mostly for analog SiPMs

- Detailed electrical model
- Pulse shape
- Gain and Gain fluctuation
- Response non-linearity

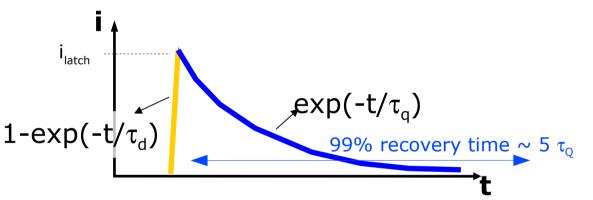
Simple electrical model - ideal signal shape

Diode (capacitor) fast discharge and slow recharge

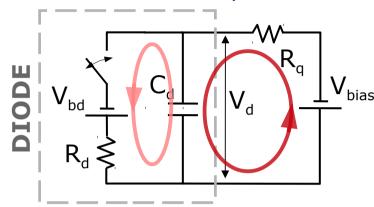
charge stored defines Gain

 \rightarrow Gain \sim C Δ V

 $\Delta V = V_{\text{bias}} - V_{\text{bd}}$ "Over-Voltage"



currents internal / external



Rise time Fall (recovery) time

 $\tau_{\rm d} = R_{\rm d}C_{\rm d} \ll \tau_{\rm q} = R_{\rm q}C_{\rm d}$

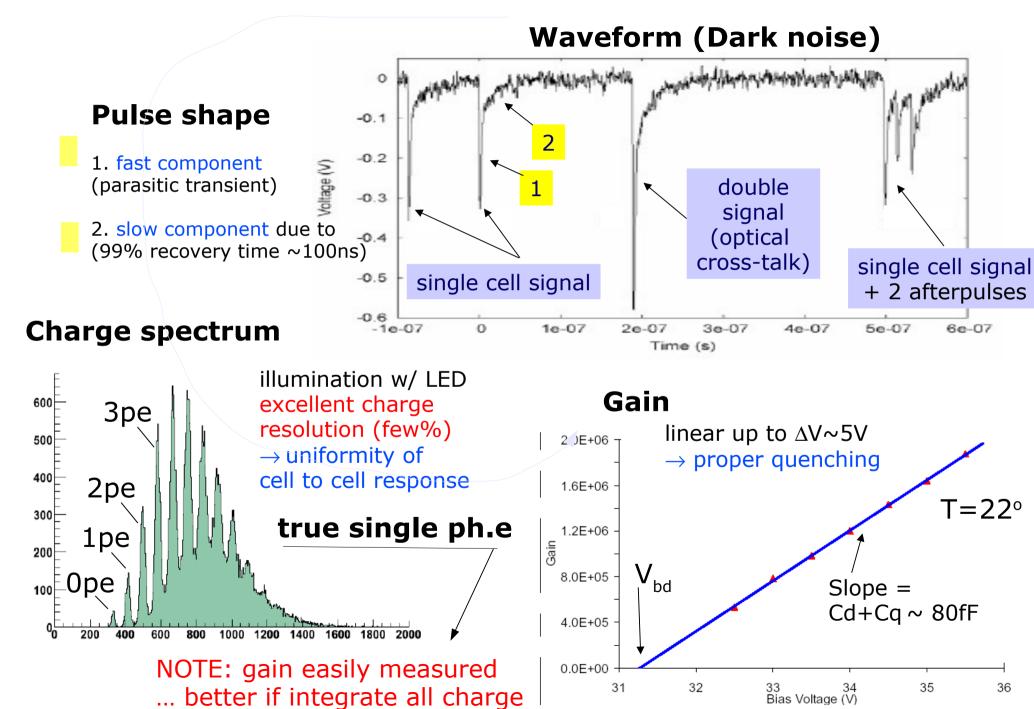
Gain → linear with $\Delta V \neq APD$

- → no multiplication noise (≠ APD) + limited intrinsic fluctuations
- \rightarrow independent of T at fixed $\Delta V (\neq APD) \rightarrow$ higher stability (wrt APD)

Rise time T dependence (weak) due to R_d

Recovery time T dependence (strong) due to R_q
C_d is independent of T

Actual pulse shape and Gain



G.Collazuol - SNRI 2016

SiPM equivalent circuit and pulse shape

Single cell model \rightarrow (R_d||C_d)+(R_q||C_q) SiPM + load \rightarrow (||Z_{cell})||C_{grid} + Z_{load}

Signal = slow pulse $(\tau_{d \text{ (rise)}}, \tau_{slow \text{ (fall)}})$ + + fast pulse $(\tau_{d \text{ (rise)}}, \tau_{fast \text{ (fall)}})$

- $\bullet \tau_{d \text{ (rise)}} \sim R_{d}(C_{q} + C_{d}) \text{ [intrinsic]}$
- • $\tau_{fast (fall)} = R_{load} C_{tot}$ (fast; parasitic spike)
- • $\tau_{slow (fall)} = R_q (C_q + C_d)$ (slow; cell recovery)

F.Corsi, et al. NIM A572 (2007) 416

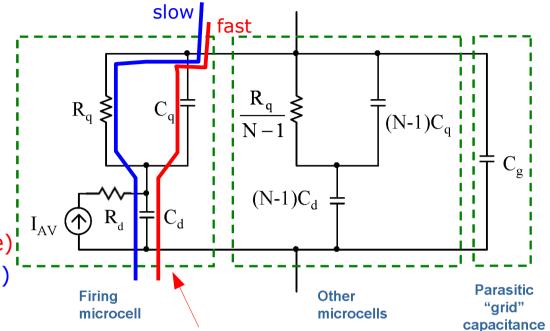
S.Seifert et al. IEEE TNS 56 (2009) 3726

 $R_a = 400 k\Omega$

 $R_{load} = 50\Omega$

 $5.*10^{-9}$

 $1.*10^{-8}$



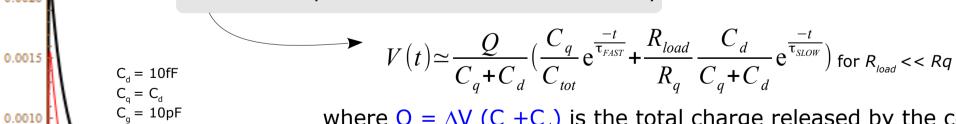
Cq → fast current supply path in the beginning of avalanche

Pulse shape

• Rise: Exponential

Sp.Charge Rd x Cd,q filtered by parasitic inductance, stray C, ... (Low Pass) \rightarrow O(R_{load} C_{tot})

• Fall: Sum of 2 exponentials: transient + recovery



where $Q = \Delta V (C_q + C_d)$ is the total charge released by the cell \rightarrow 'prompt' charge on C_{tot} is $Q_{fast} = Q C_q / (C_q + C_d)$

Gain still well
$$G = \int dt \frac{V(t)}{q_e R_{load}} = Q/q_e = \frac{\Delta V(C_d + C_q)}{q_e}$$
defined:

G.Collazuol - SNRI 2016

0.0005

Pulse shape features

$$V(t) \simeq \frac{Q}{C_q + C_d} \left(\frac{C_q}{C_{tot}} \ \mathrm{e}^{\frac{-t}{\tau_{fast}}} + \frac{R_{load}}{R_q} \frac{C_d}{C_q + C_d} \ \mathrm{e}^{\frac{-t}{\tau_{slow}}} \right) \ = \ \frac{QR_{load}}{C_q + C_d} \left(\frac{C_q}{\tau_{fast}} \ \mathrm{e}^{\frac{-t}{\tau_{fast}}} + \frac{C_d}{\tau_{slow}} \ \mathrm{e}^{\frac{-t}{\tau_{slow}}} \right)$$

$$ightarrow$$
 gain $G = \int dt \frac{V(t)}{q_e R_{load}} = Q/q_e = \frac{\Delta V(C_d + C_q)}{q_e}$ independent of R_q

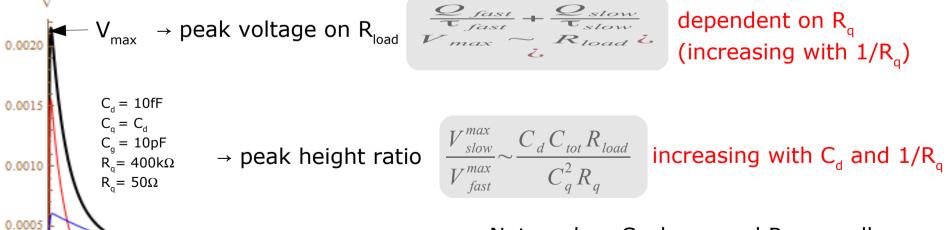
Note: valid for low impedance load $R_{load} << Rq$

•
$$\tau_{\text{fast}} = R_{\text{load}} C_{\text{tot}}$$

• $\tau_{\text{slow}} = R_{\text{q}} (C_{\text{q}} + C_{\text{d}})$

$$\bullet \ \tau_{\text{slow}} = R_{\text{q}} (C_{\text{q}} + C_{\text{c}})$$

 \rightarrow charge ratio $\frac{Q_{slow}}{Q_{fast}} \sim \frac{C_d}{C_a}$



 $1.*10^{-8}$

Note: when C_{tot} large and R_{load} small

$$\rightarrow$$
 R_{load}C_{tot} \sim R_qC_{cell} \rightarrow pole splitting for τ _{fast} / τ _{slow}

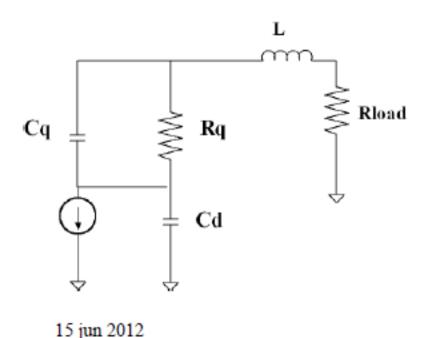
Pulse shape features → SiPM impedance

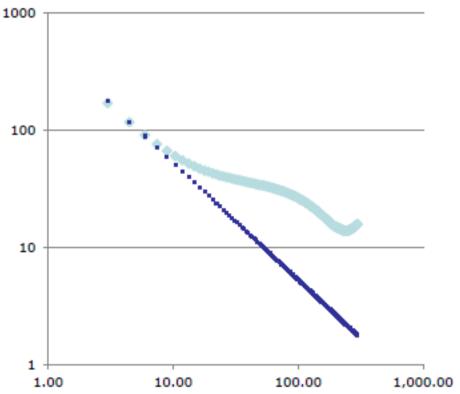
C. de LaTaille - PhotoDet 2012

RLC too simple, inaccurate at high frequency

CdRqCqLR OK

May better explain HF noise behaviour





Measured impedance MPPC HPK 3x3 mm

Line : C = 320 pF

CdLT Photodet conference

→ Ringing effect with low Z Front End !!!

Pulse shape: dependence on Temperature

The two current components behave differently with Temperature

- \rightarrow fast component is independent of T because C_{tot} couples to external R_{load}
- \rightarrow slow component is dependent on T because $C_{d,q}$ couple to $R_q(T)$

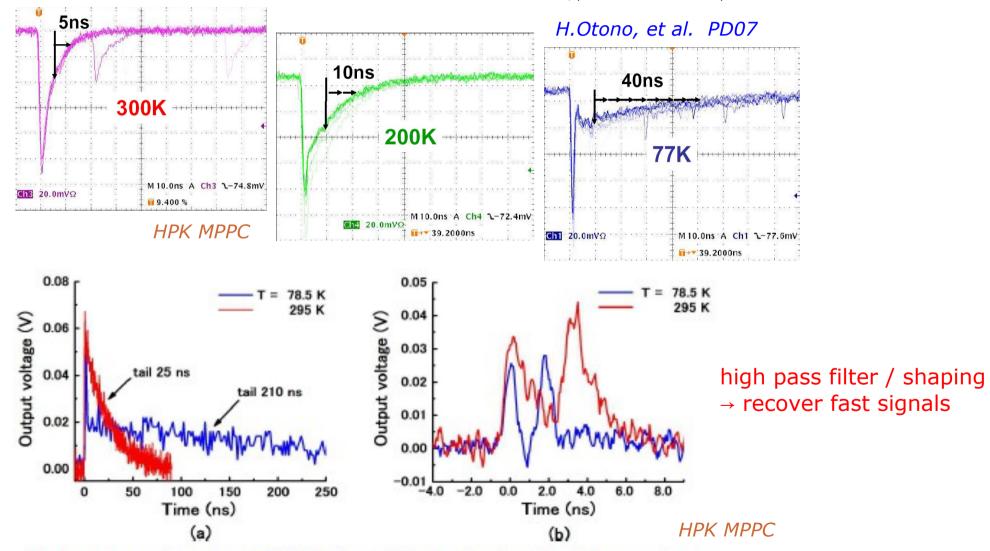
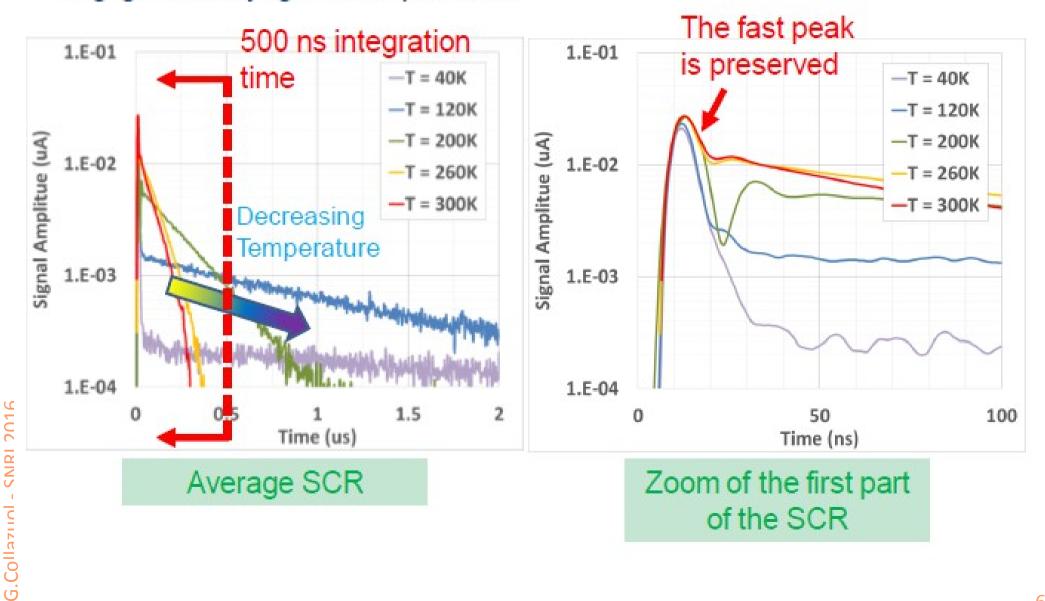


Fig. 2. (a) Output signals from the MPPC when no high-pass filter is used, and (b) output signals from the high-pass filter when two pulses were generated successively.

Akiba et al Optics Express 17 (2009) 16885

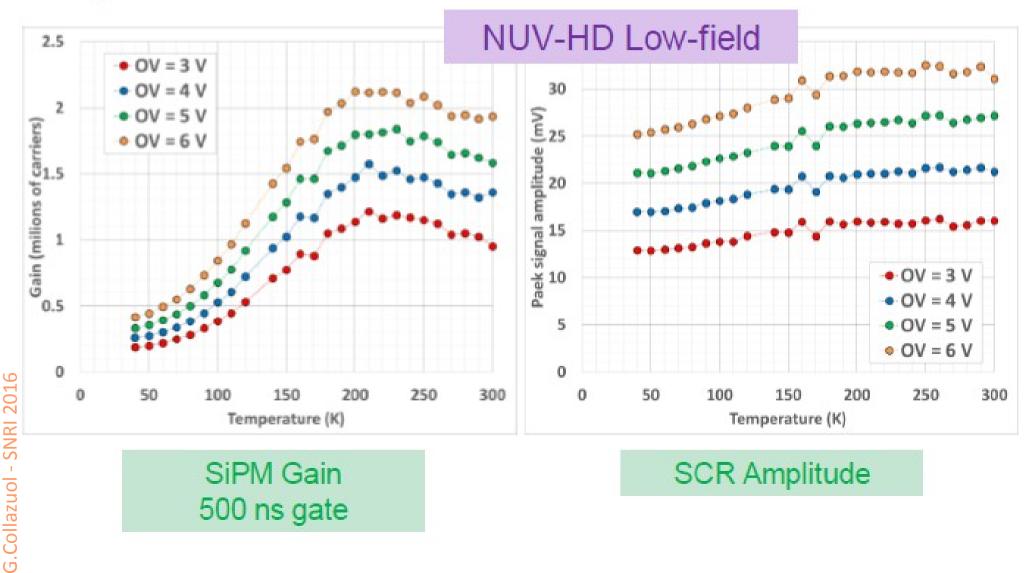
The exponential tail of the single cell response (SCR) becomes almost negligible at cryogenic temperature.



Pulse Charge and Amplitude vs T

Alberto Gola - IEEE NSS-MIC 2015

The SiPM Gain is significantly reduced with temperature but the SCR amplitude is much more stable.

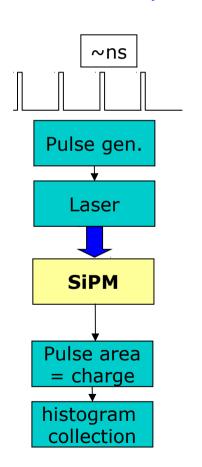


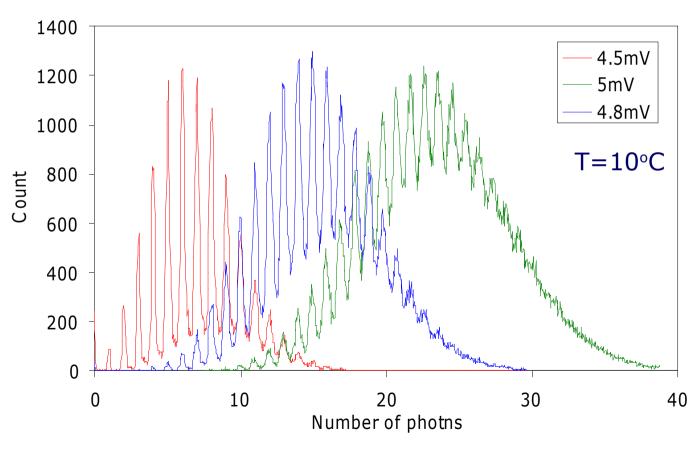
SiPM Gain 500 ns gate SCR Amplitude

Single cell charge resolution - gain fluctuations

Device illuminated with short weak light pulses from a blue LED. Device biased at 3V over-voltage.

Effective quenching and cell-to-cell uniformity!





NOTE: resolution limited by electronic noise

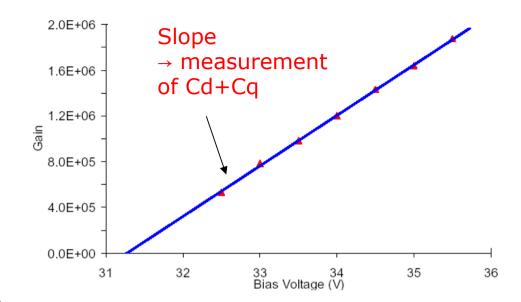
Single photon resolution - Gain fluctuations

$$G = \Delta V(C_q + C_d)/q_e$$

 \rightarrow Gain is linear if ΔV in quenching regime but

there are many sources of response nonlinearity (non proportionality)

SiPM gain fluctuations (intrinsic) differ in nature compared to APD where the statistical process of internal amplification shows a characteristic fluctuations

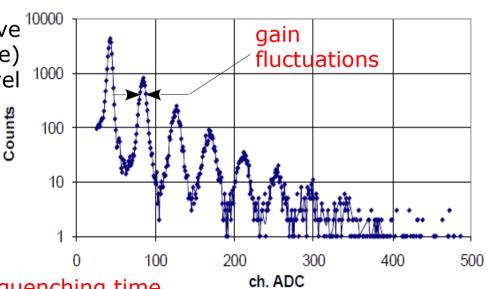


SES MEPHI/PULSAR APD, U=57.5V, T=-28 C

$$\frac{\delta G}{G} = \frac{\delta V_{bd}}{V_{bd}} \oplus \frac{\delta C_{dq}}{C_{dq}}$$
 cell to cell uniformity (active area and volume) control at % level

• doping densities (Poisson): $\delta V_{bd} \ge 0.3V$ Shockley, Sol. State Ele. 2 (1961) 35

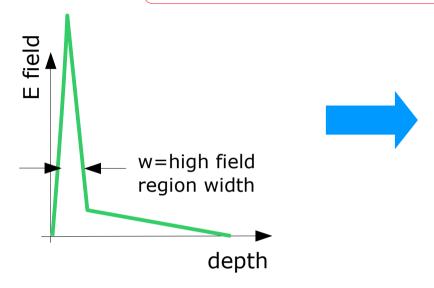
doping, epitaxial, oxide (processing): $\delta V_{bd} \sim O(0.1V)$



In addition δG might be due to fluctuations in quenching time ... and of course after-pulses contribute too (not intrinsic → might be corrected)

Recent improvements in V_{bd} uniformity

Engineering high electric field & depletion/drift layer profiles

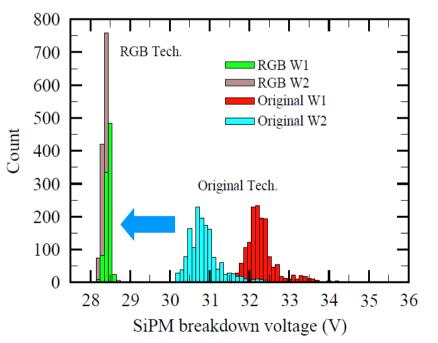


Vbd uniformity easier with lower field over a wider "high field" region depth

- → Improved break-down voltage uniformity
 - at wafer level
 - among wafers

N.Serra: "Characterization of new FBK SiPM technology for visible light detection"

JINST 2013 JINST 8 P03019



Note: also improvement on T coefficient of $V_{bd} \rightarrow stability$

... note

- 1) no multiplication (excess) noise in SER
- 2) SER width due to intrinsic fluctuations in doping densities and variations among cells
- 3) Correlated noise is there (AP, CT)
 - → excess charge factor (ECF)

Not linear with intensity ... but not the only source of non-linearity ...

Non-proportionality of charge output w.r.t. number of photons (i.e. response) at level of several % might show up even in quenching regime (negligible quenching time), depending on ΔV and on the intensity and duration of the light pulse.

Main sources are:

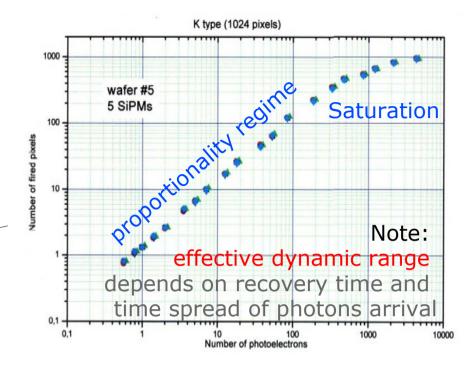
- finite number of pixels
- finite recovery time
- after-pulses, cross-talk
- drop of ΔV during the light pulse in case of large signal current on series (ballast) resistances

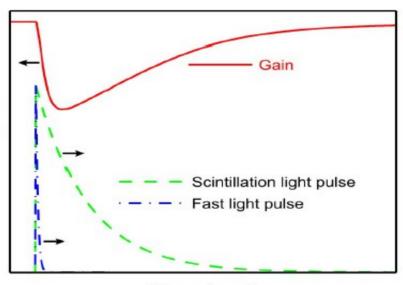
T.van Dam IEEE TNS 57 (2010) 2254 Detailed model to estimate non-lin, corrections

Finite number of cells is main contribution in case number of photons ~ O(number of cells) (dynamic range not adequate to application)

⇒ saturation
$$n_{fired} = n_{all} \left[1 - \exp \left(-\frac{n_{phot.} PDE}{n_{all}} \right) \right]$$

→ loss of energy resolution





Gain (a.u.)

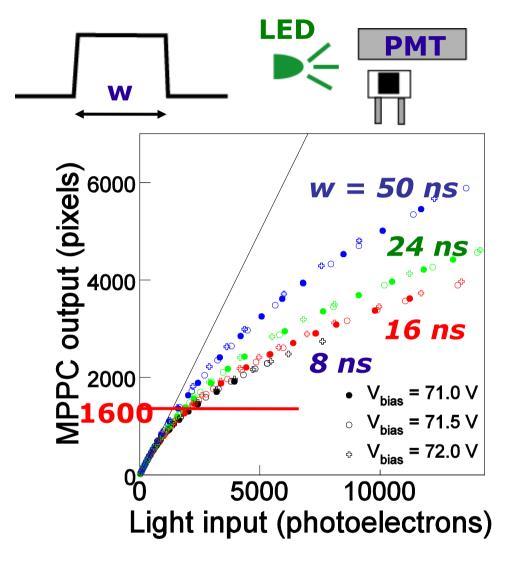
Time (a.u.)

- SNRI 2016

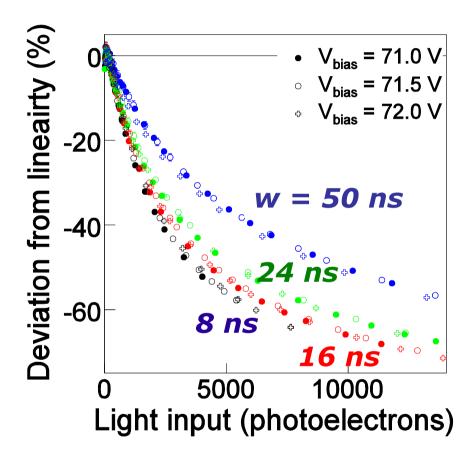
Photonflux (a.u.)

Calibration caveat

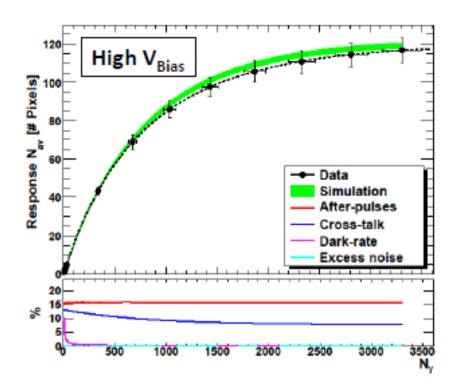
S.Uozumi – PD07 Kobe - 27 June 2007

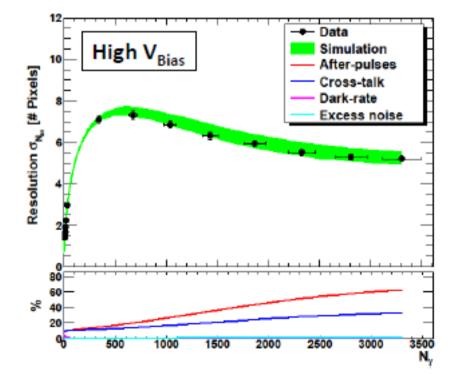


Response curves taken with various width of LED light pulses. (gate width = 100 ns)



- Dynamic range is enhanced with longer light pulse
- Time structure of the light pulse gives large effects in non-linear region
- No significant influence with changing bias voltage
- Knowing time structure of scintillator/WLS light signal is crucial

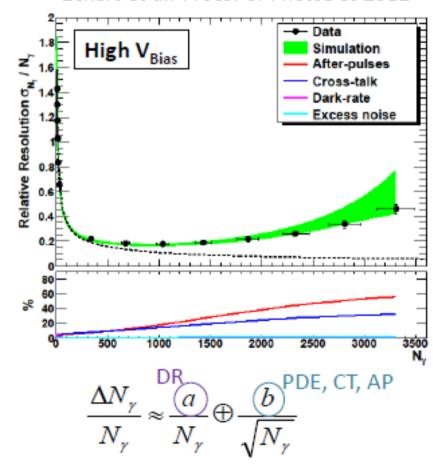




Amplitude fluctuations

finite number of pixels: constraint
→ limit in resolving the number of photons

Eckert et al, Procs. of PhotoDet 2012



see also Musienko et al JINST 2 2007 P0600

- SNRI 2016

Tiny cells: not only wider dynamic range!

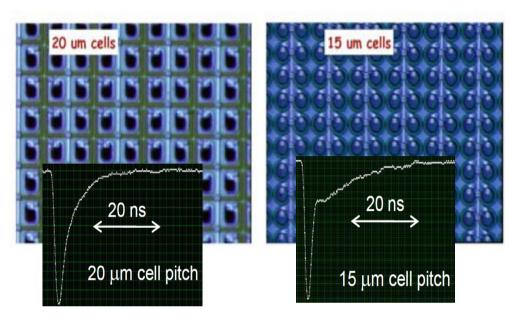
Many small cell SiPM types available

→ Fill Factor improving (> 60%)

- tiny cells (→ 10-15μm)
 - → HPK, FBK-Advansid, NDL, MPI-LL, ... (very fast recovery ~ ns)
- micro cells (→ μm)
 - → Zecotek, AmpliticationTechn.

(warning: very slow recovery ~ ms)

tiny cell MPPC (2012) by Hamamatsu

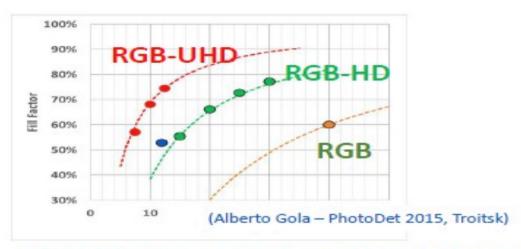


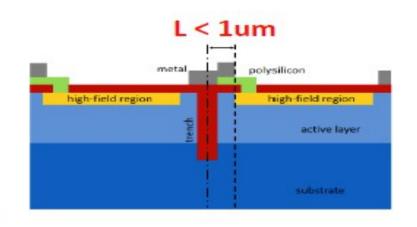
N,×PDE

SiPM-1, 2500 cells, U=26.5 V, Y11 light, Gate=100 ns SiPMs NDL (Bejiing) Equivalent number of fired 3000 Ideal linearity line Zhang et al NIM A621 (2010) 116 2500 SiPM response Han at NDIP 2014 2000 1500 • type: n-on-p, Bulk Rq high cell density (10000/mm²) 1000 • fast recovery (5ns) → dynamic 500 low gain Measurements by Y.Musienko range better less after-pulsing 50Ø 1000 1500 2000 2500 timing → less cross-talk

→ mitigate effects of radiation damages

High Dynamic range SiPMs (example FBK)

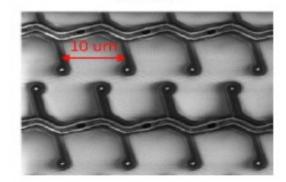




Cell sensitive area vs. trench Finished 10 µm cell pitch width

L = 0.75

SiPM



Fill Factor vs. trench width

L (um)	Fill Factor
0.75	57.1%
1	48.8%
1.25	40.3%
1.5	32.6%

RGB-HD

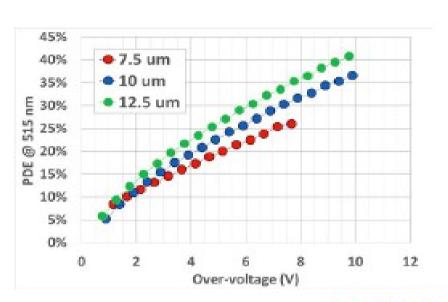
cell pitch (μm)	cells/mm ²
12	7000
15	4500
20	2500
25	1600
30	1100

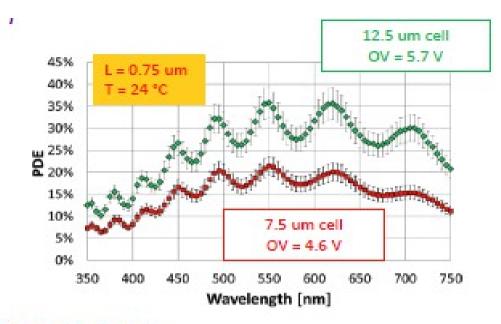


RGB-UHD

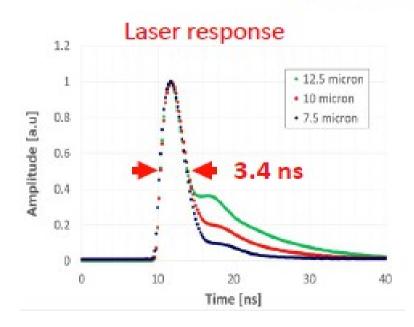
cell pitch (μm)	cells/mm ²
7.5	20530
10	11550
12	7400

FBK RGB-UHD parameters

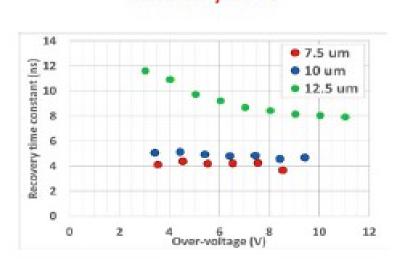




(Alberto Gola - PhotoDet-2015, Troitsk)



Recovery time



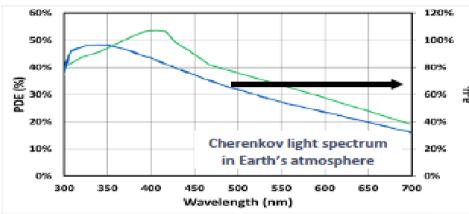
High Dynamic range SiPMs (FBK)

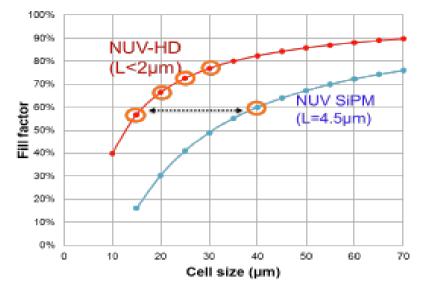
NUV High-Density (HD) technology:

Lower dead border region → Higher Fill Factor

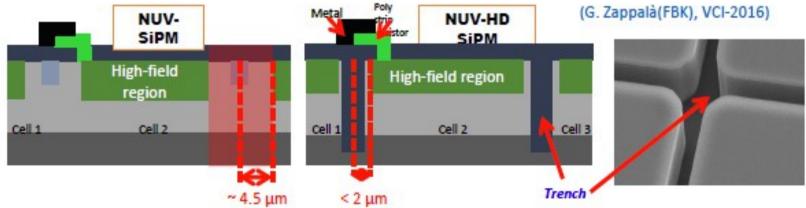
Trenches between cells → Lower Cross-Talk

NUV-HD 30µm Cell Pitch PDE, 10V OV





30 μm cell pitch SiPMs: GF=77% → PDE>50 % !!



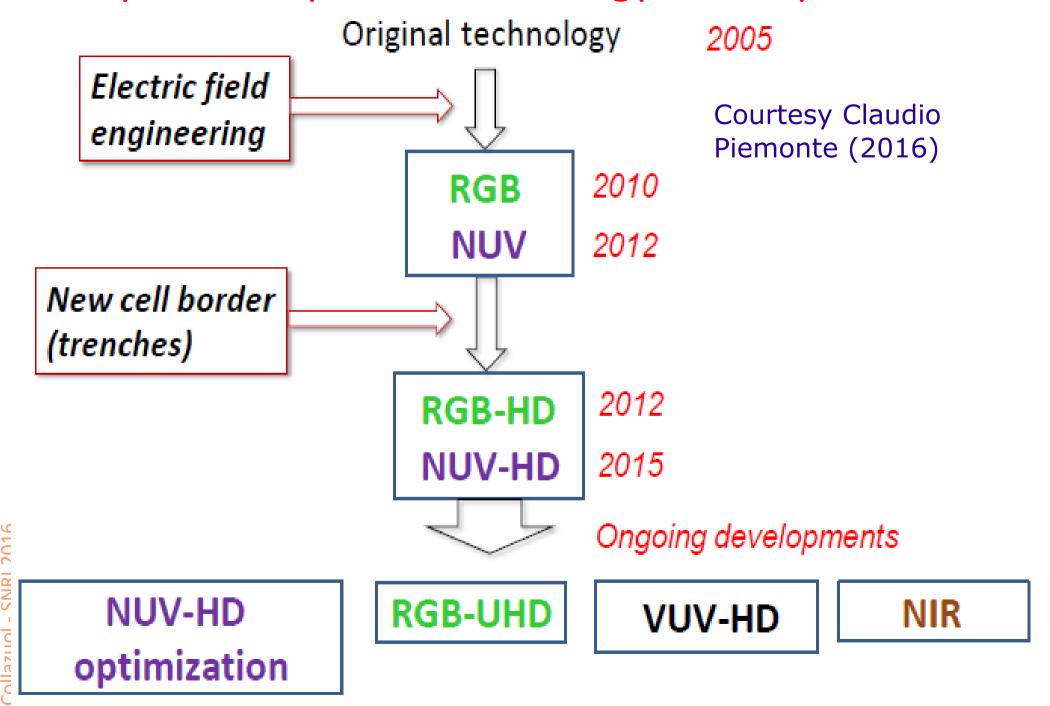
High Dynamic Range

High PDE

SPAD Pitch	15 µm	20 μm	25 μm	30 µm	35 µm	40 µm
Fill Factor (%)	55	66	73	77	81	83
SPAD/mm ²	4444	2500	1600	1111	816	625

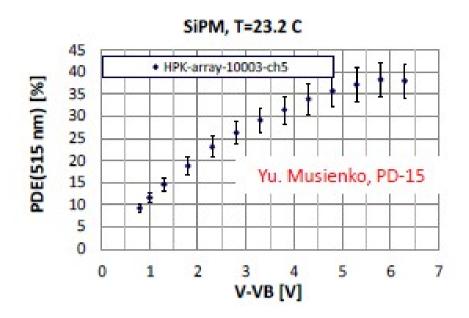
NUV-HD

Example of 10 years' technology development (FBK)

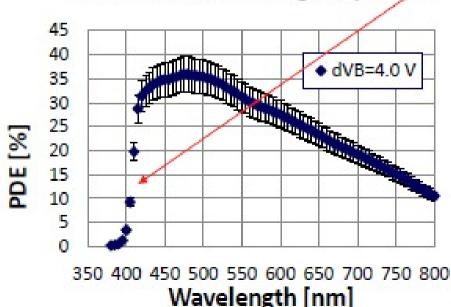


8:

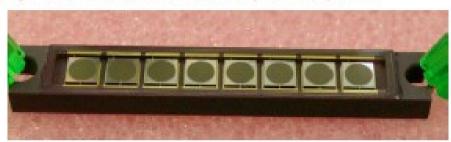
Large Dyn. Range SiPM for HCAL CMS upgrade



1400 SiPM arrays have been delivered to CERN during this year

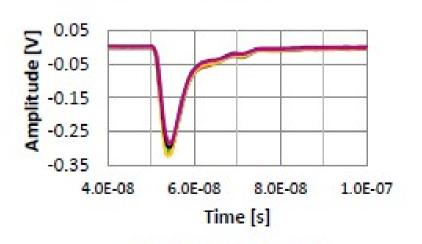


8-ch. SiPM array for the CMS HE HCAL Upgrade project: Ø2.8 mm SiPMs, 15 μm cell pitch



Glass widow with special filter was designed by HPK to cut off UV light which can be produced by muons and hadrons in plastic fibers

SiPM laser response



Recovery time 7-8 ns

Noise in SiPMs

Primary noise

→ dark counts

pulses triggered by non-photo-generated carriers (thermal / tunneling generation in the bulk or in the surface depleted region around the junction)

Correlated "excess" charge:

- → After-pulsing •
- → Cross-Talk.

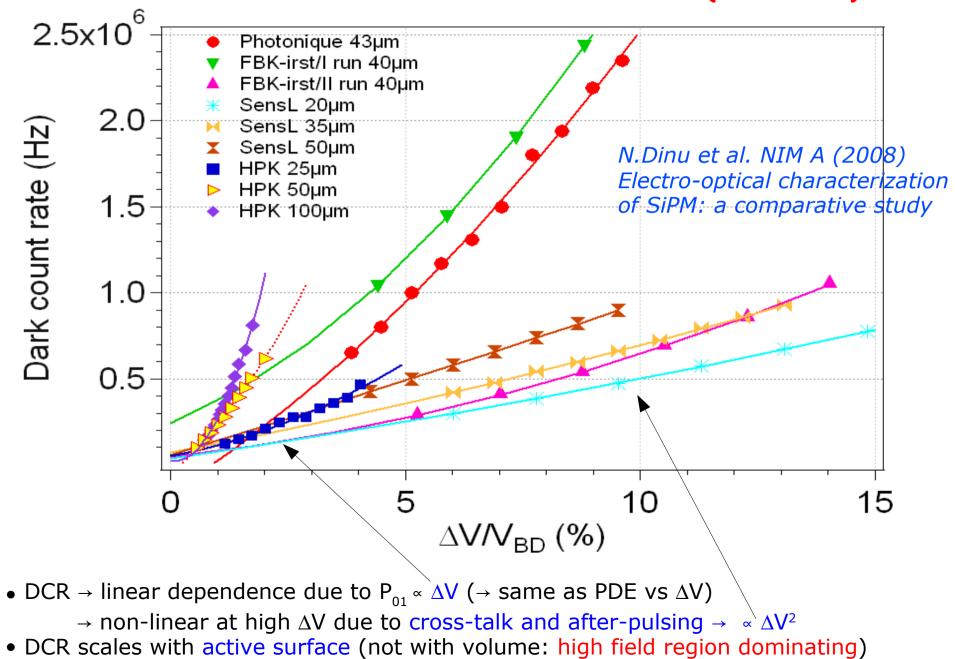
"optical"

carriers can be trapped during an avalanche and then released triggering another avalanche

photo-generation during the avalanche discharge. Some of the photons can be absorbed in the adjacent cell possibly triggering new discharges

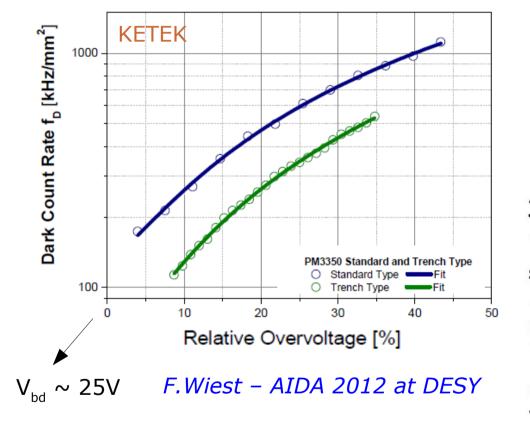
Dark Count Rate

from MHz/mm² (old devices) to few 10 KHz/mm² (recent)



Recent improvements against Dark Counts

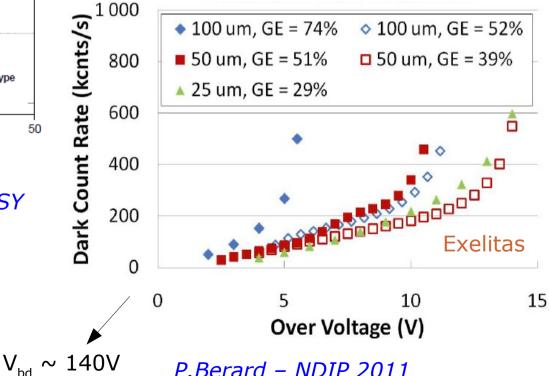
KETEK PM 3350 (p+-on-n, shallow junction) 3x3mm² active area pixel size 50x50 μm²



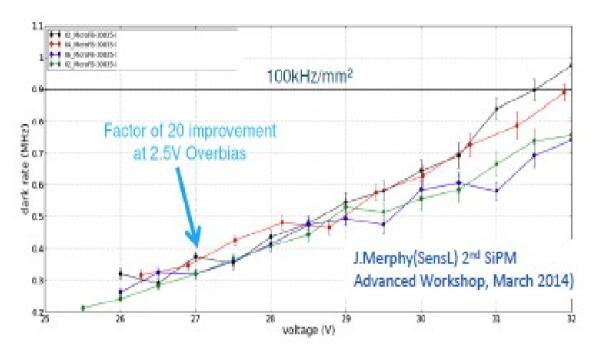
Critical issues:

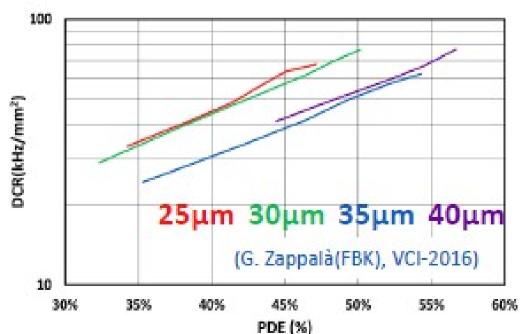
- quality of epitaxial layer
- gettering techniques
- Electric field → tunneling

Exelitas 1st generation SiPM 2011 $(p^+-on-n) 1x1mm^2$

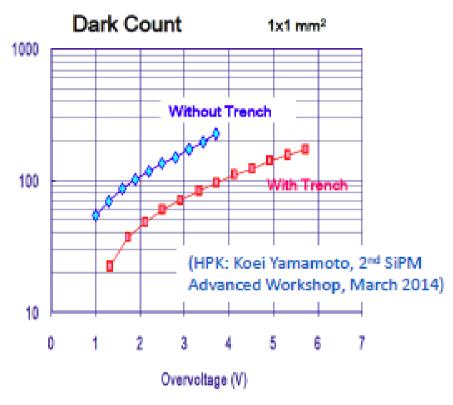


Recent improvements against Dark Counts





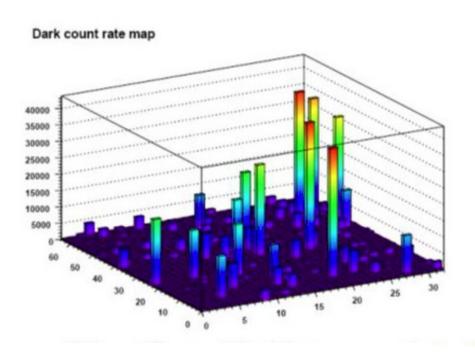
G.Collazuol - SNRI 2016

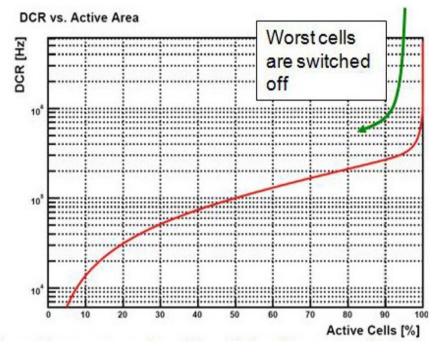


Various devices show DCR at Troom at level of ~30kHz/mm² in extended over-voltage range

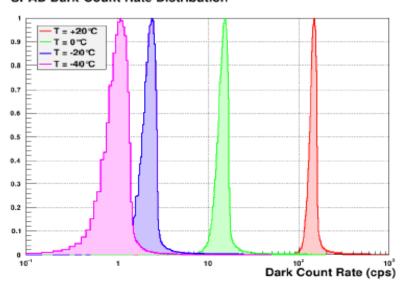
DCR - digital-SiPM (Philips)

Control over individual SPADs enables detailed device characterization





SPAD Dark Count Rate Distribution



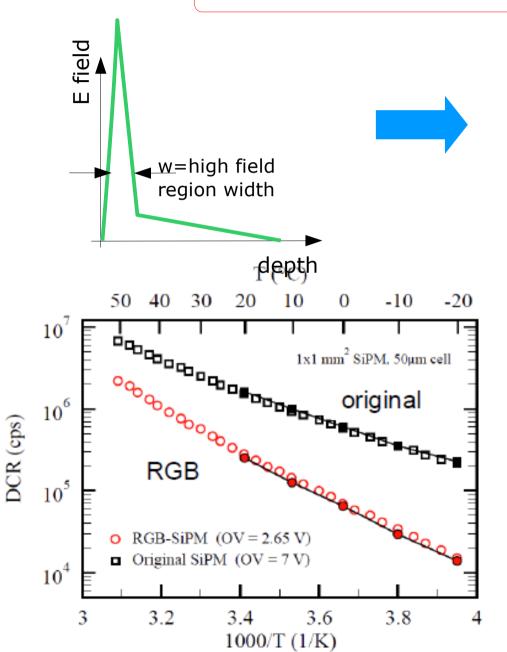
- Over 90% good diodes (dark count rate close to average)
- Typical dark count rate for ∆V=3.3V
 ~150 Hz/diode at 20°C
- Low DCR ~1-2 Hz/diode at -40°C

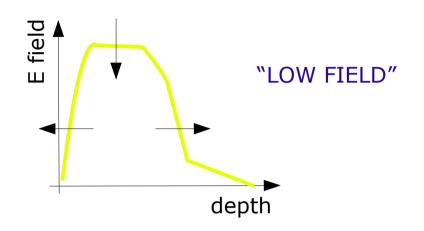
T.Frach at Heraeus Seminar 2013

Can disable bad cells (eg 10%) loose in PDE (10% relative)

Recent improvements against Dark Counts

Engineering high electric field & depletion/drift layer profiles

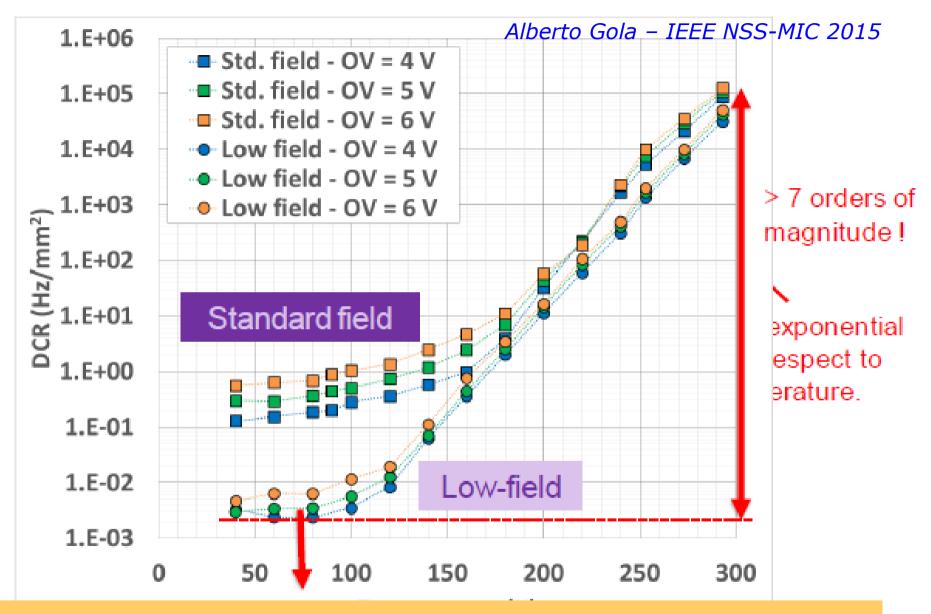




RGB has a much lower noise and a steeper temperature dependence:

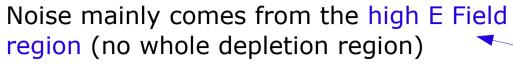
→ less tunneling

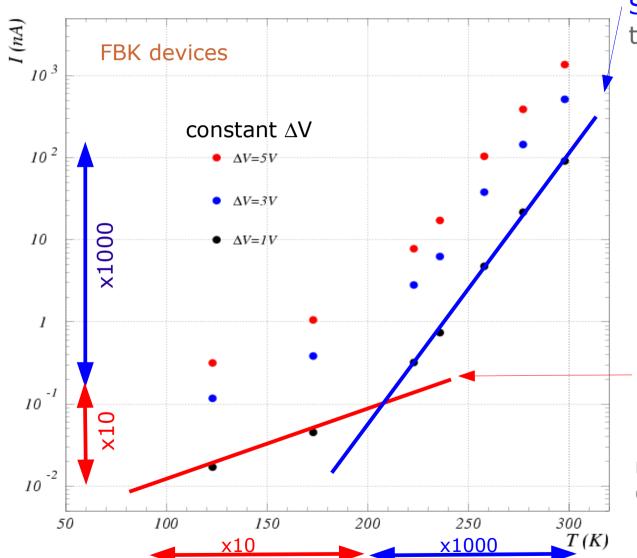
FBK NUV SiPM optimized for cryogenic operation



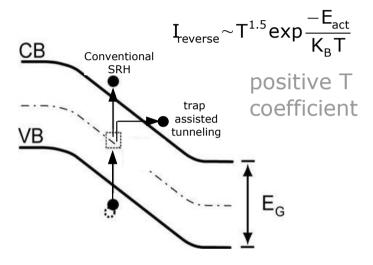
A 10x10 cm² SiPM array would have a total DCR < 100 Hz!







1) Generation/Recombination SRH noise (enhanced by trap assisted tunneling)



2) Band-to-band Tunneling noise (strong dependence on the Electric field profile)

negative T coefficient

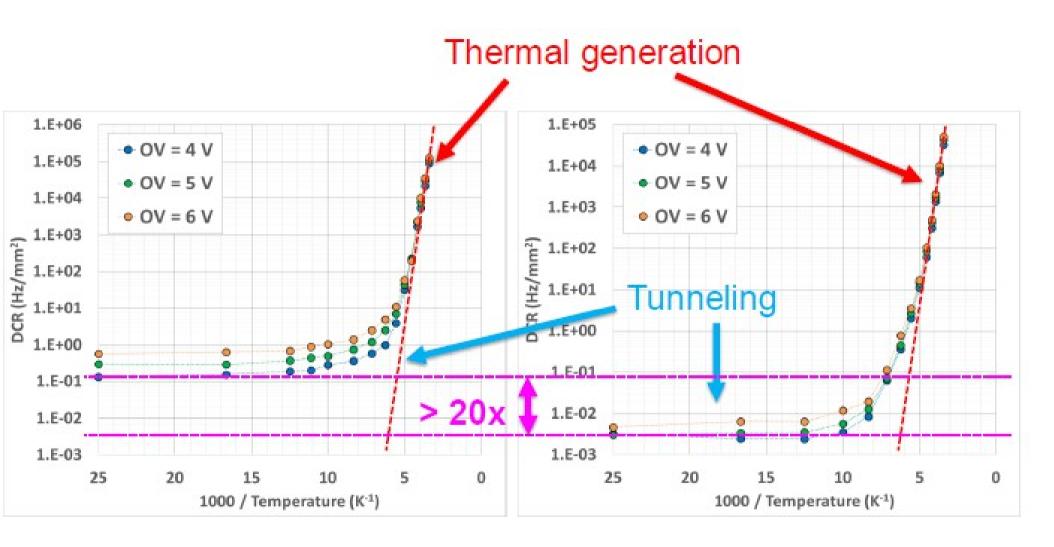
Efield engineering is crucial for min. DCR (esp. at low T)

Tunneling noise dominating for T<200K (sharp high E field region → higher noise)

center

FBK NUV SiPM optimized for cryogenic operation

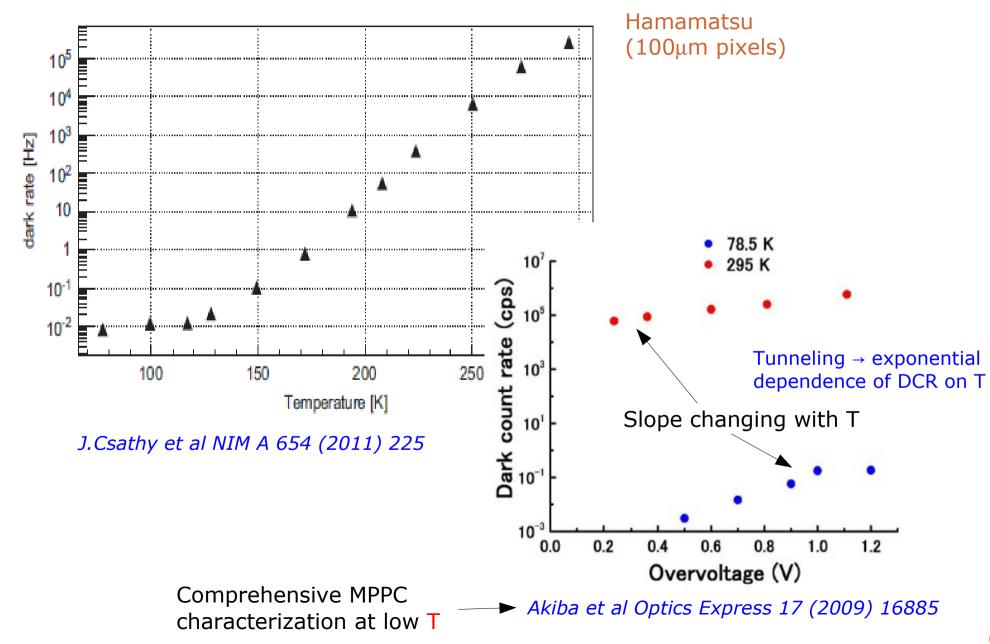
Alberto Gola - IEEE NSS-MIC 2015



Standard field

Low-field

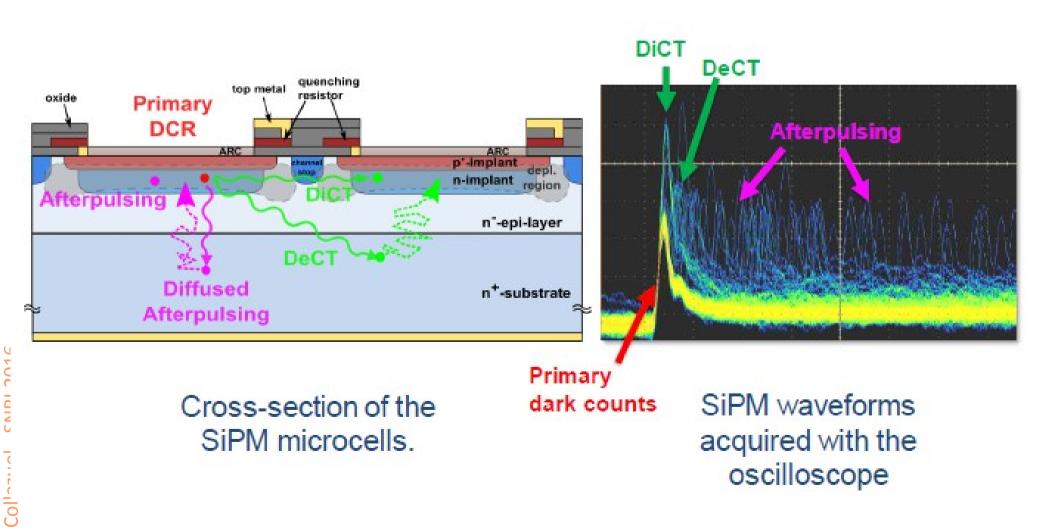
Dark Count Rate vs T



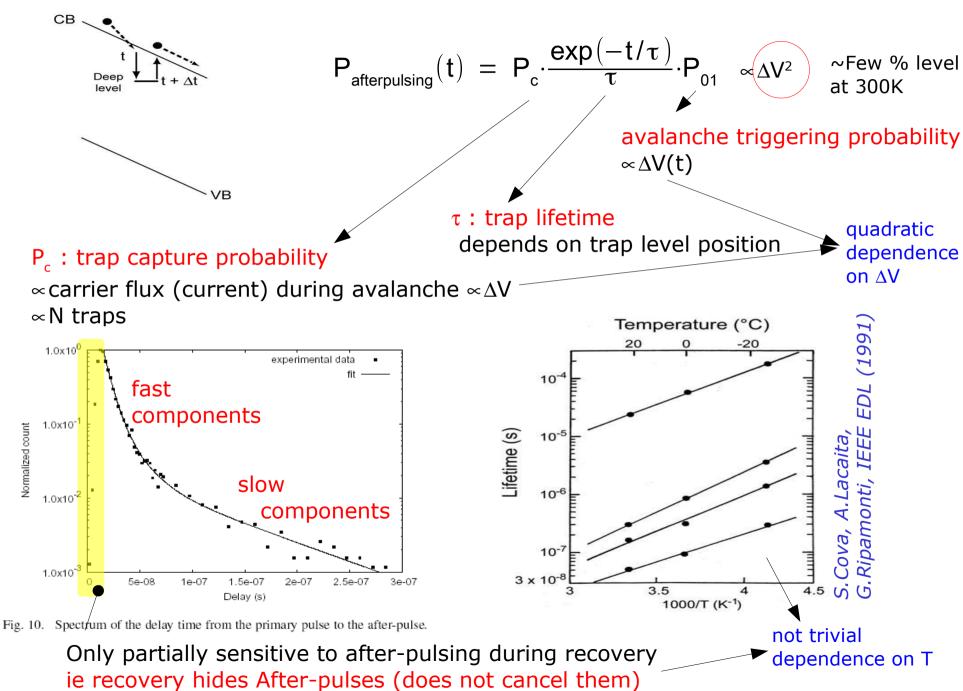
After-pulsing and Cross-talk

Alberto Gola - IEEE NSS-MIC 2015

Different SiPM noise components are related to different physical phenomena.



After-Pulsing Carrier trapping and delayed release

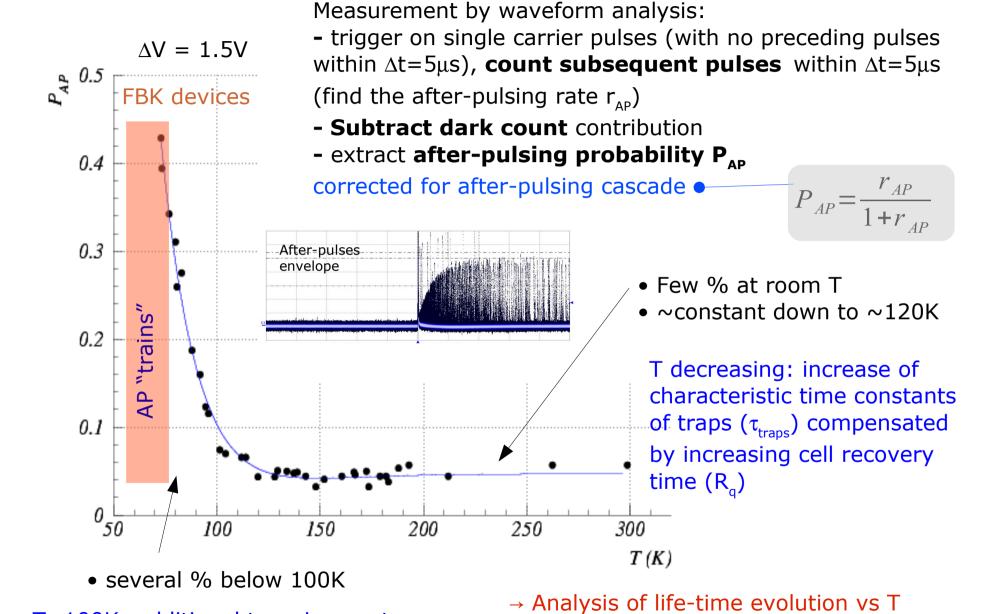


After-Pulses vs T (constant DV)

T<100K: additional trapping centers

of carriers freeze-out

activated possibly (?) related to onset



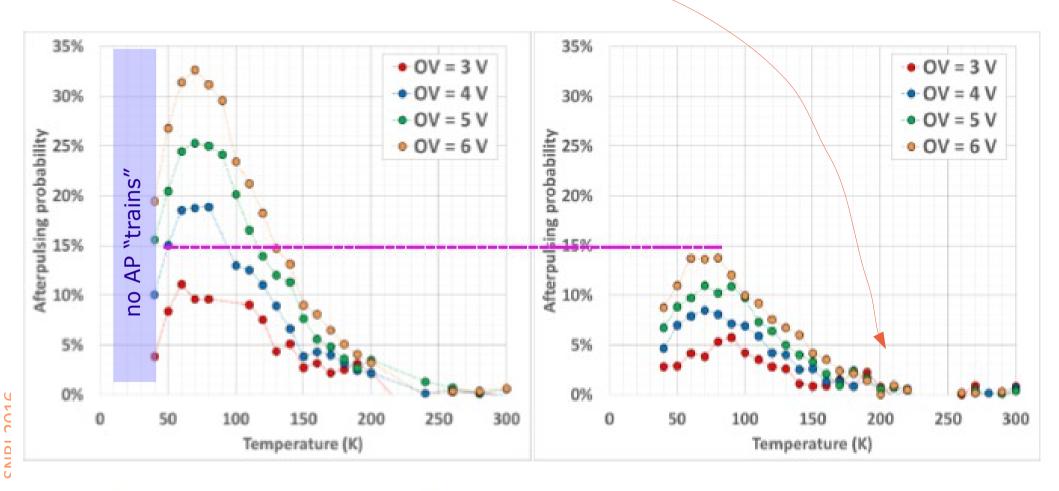
G.C. et al NIM A628 (2011) 389

of the various traps (at least 3 types at T_{room})

After-Pulsing vs T (constant DV)

Alberto Gola IEEE NSS-MIC 2015

The growth of the microcell recharge time constant helps reducing the afterpulsing at low temperature.



Standard field

Low-field

Optical cross-talk

Avalanche luminescence (NIR)

Carriers' luminescence (spontaneous direct relaxation in the conduction band) during the avalanche: probability 3.10⁻⁵ per carrier to emit photons with E> 1.14 eV

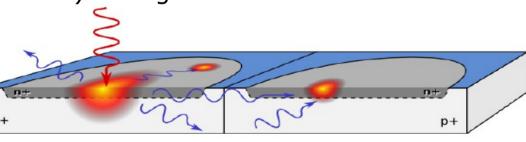
A.Lacaita et al. IEEE TED (1993)

Photons can induce avalanches in neighboring cells. Depends on distance between high-field regions

ΔV^2 dependence on over-voltage:

• carrier flux (current) during avalanche ∝ ΔV

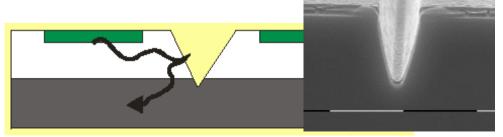


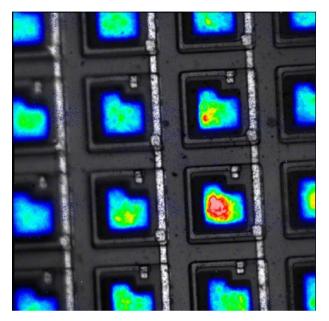


Counteract:

• optical isolation between cells by trenches filled with opaque material

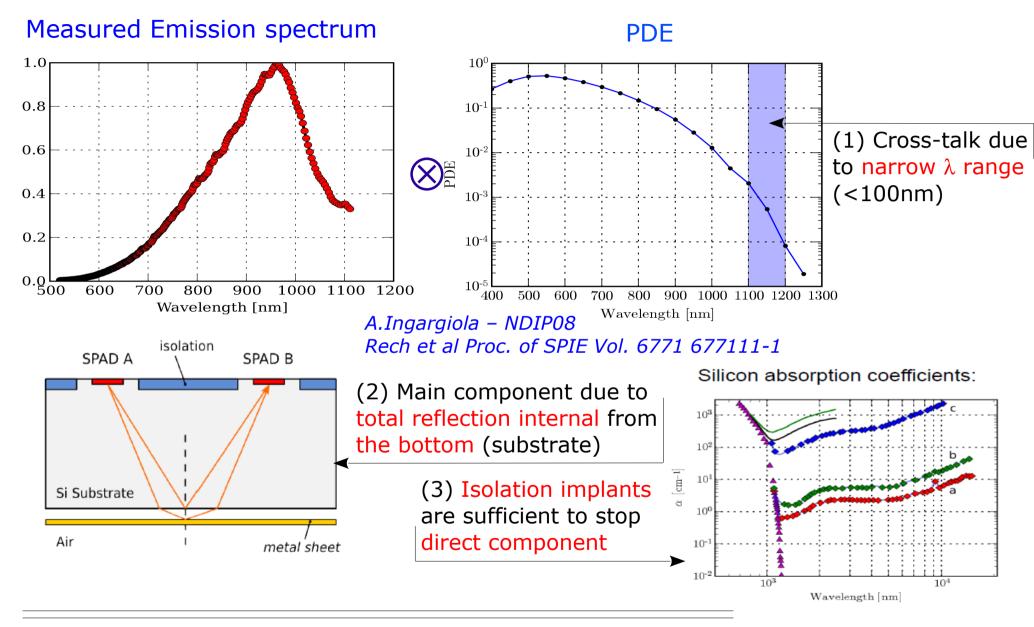
low over-voltage operation helps





N.Otte, SNIC 2006

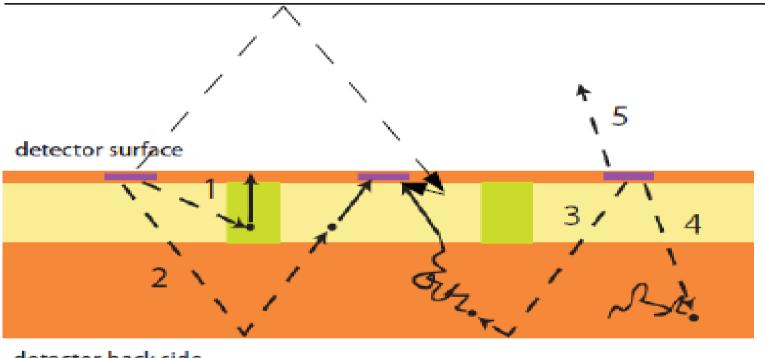
Optical cross-talk: also reflections from the bottom



- → Crosstalk can't be eliminated simply by means of trenches
- → Main contribution to crosstalk comes from bottom reflections (using trenches)

Reflections and "external" cross-talk

Outer reflective surface (active material, scintillator, ...)

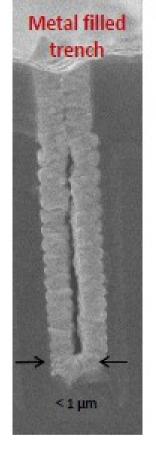


detector back side

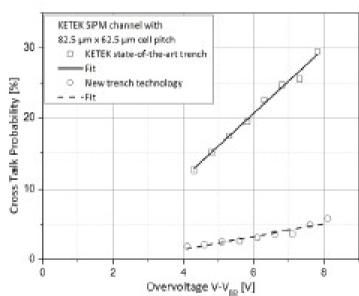
Additional components:

- reflections of avalanche photons on external surfaces
- delayed avalanches (see also F.Retiere Procs. of PhotoDet 2012)

Cross-talk reduction

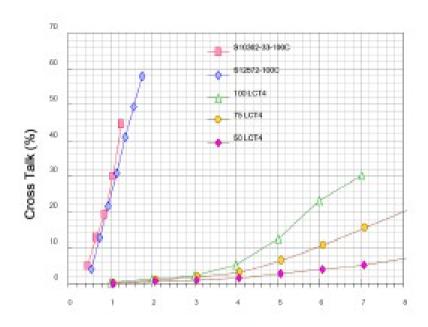


The way to reduce X-talk: trench filled with non-transparent material (tungsten)

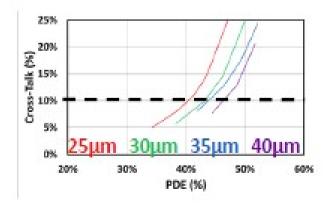


(KETEK - Photodet-2015 (Troitsk))

X-talk was reduced from 20+30% to 3+5% at dVB=4+5 V



Overvoltage (V) (HPK: Koei Yamamoto, 2nd SiPM Advanced Workshop, March 2014)



(FBK: G. Zappalà, VCI-2016)

Parameter overview - Recent FBK devices

Parameters (@ room T)	RGB-HD Std. field		NUV-HD Std. field	NUV-HD Low-field	
Cell Size	25 μm	/	25 μm	25 μm	١
Fill Factor	73%		73%	73%	1
Breakdown Voltage	28 V		26.5 V	32 V	
Max PDE	45%		50%	50%	
Peak PDE λ	550 nm		410 nm	410 nm	
DCR (20°C)	< 300 kHz/mm ²	\	< 150 kHz/mm ²	< 150 kHz/mm ²	/
DiCT	20%	'	25%	25%	r
DeCT + AP	20%		2%	2%	

Tested Devices

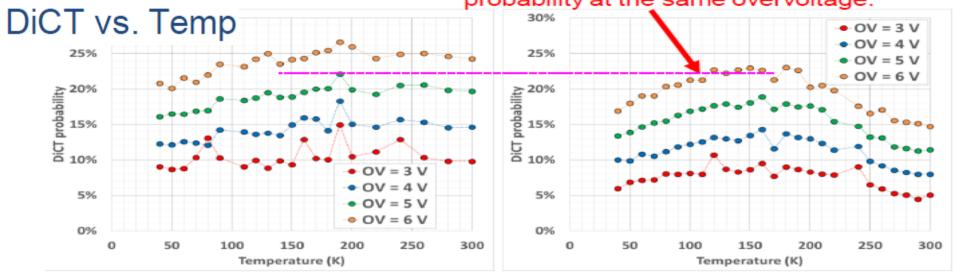
Optimized for low temperature operation

Cross-talk components vs T

The direct crosstalk probability has only minor variations with respect to

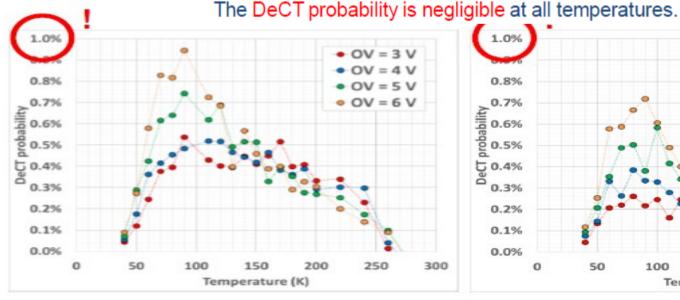
temperature.

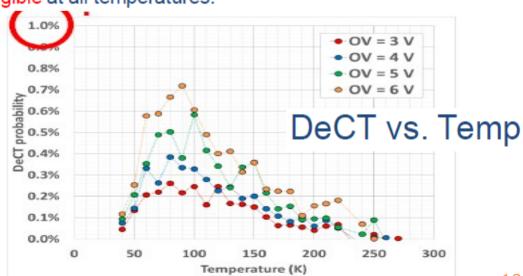
Slightly lower gain and triggering probability at the same overvoltage.



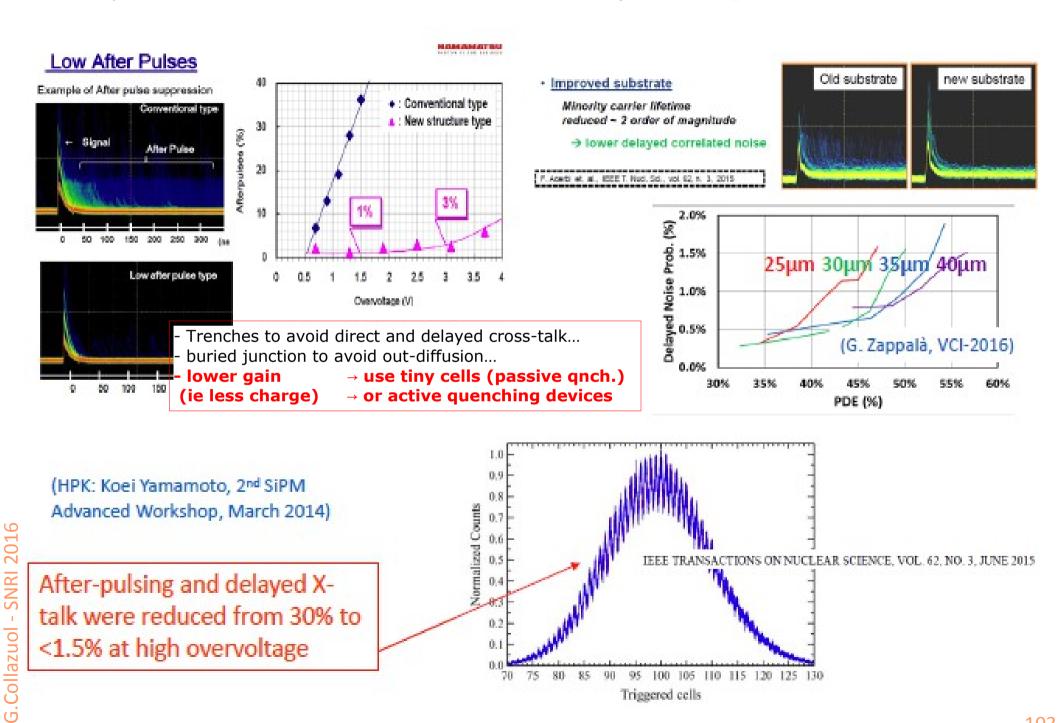
Standard field

Low-field





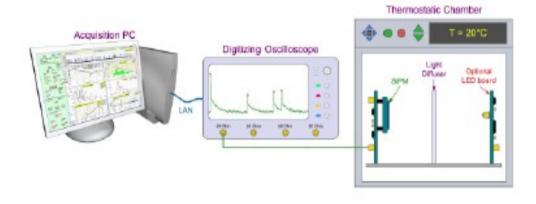
Delayed Cross-talk and after-pulsing reduction



OK, but... wait a minute: ??? how to measure

- SiPM noise
- disentangling its components

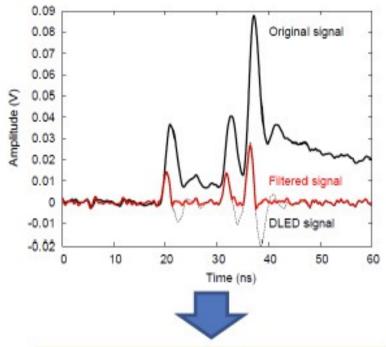
How to measure SiPM noise components



We acquire ms-long waveforms

Signal filtered to reduce pulse length





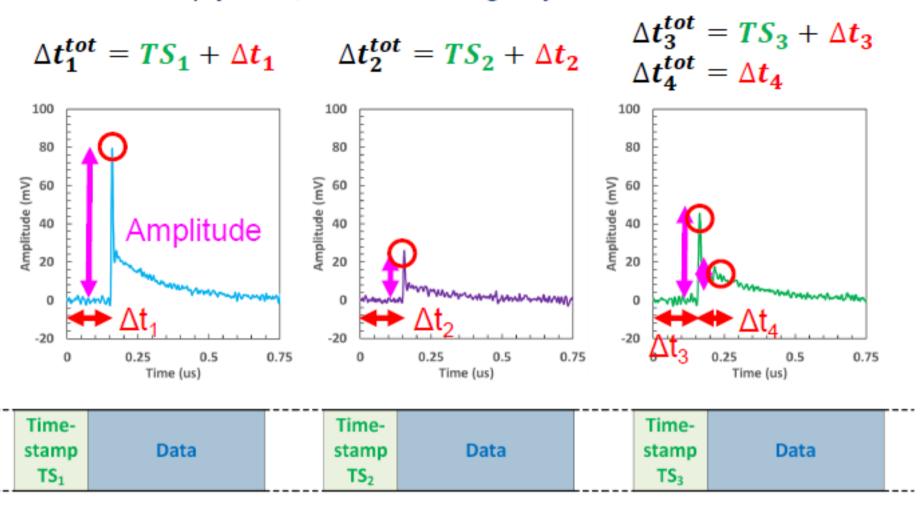
C.Piemonte - Scuola Nazionale Rivelatori LNL 2013

→ amplitude array

→ time delay array

How to measure SiPM noise components

Memory segmentation is employed to avoid storing unnecessary large amount of "empty" data, when measuring very low DCR.



How to measure SiPM noise components from Alberto Gola - IEEE NSS-MIC 2015

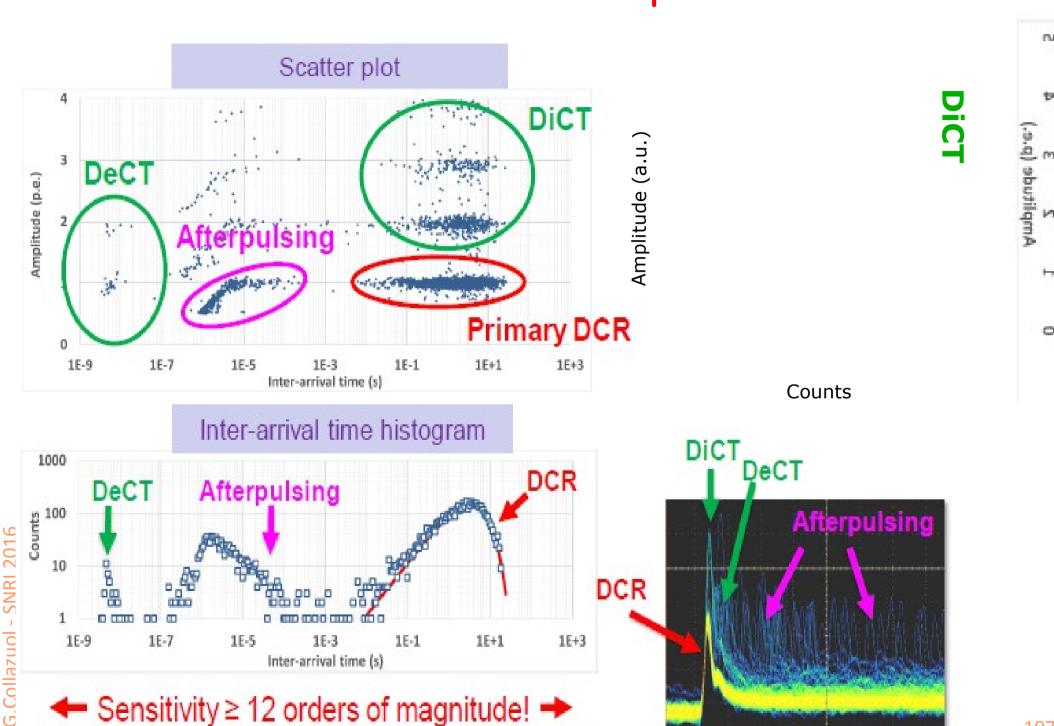


Photo-Detection Efficiency (PDE)

"External factors"

- → active area
- → light transmission
- → passivation layers (Ox/Nx)
- → surface recombination
- → built-in E fields

"Internal factors"

- → light absorption in depletion/neutral regions
- → charge transport (drift/diffusion)
 - + internal E fields (multiplication)
- → charge recombination/trapping

PDE = QE \cdot P₀₁ · FF

OE: carrier Photo-generation

probability for a photon to generate a carrier that reaches the high field region

- $\rightarrow \lambda$ and T dependent
- $\rightarrow \Delta V$ independent if full depletion at V_{bd}

P₀₁: avalanche triggering probability

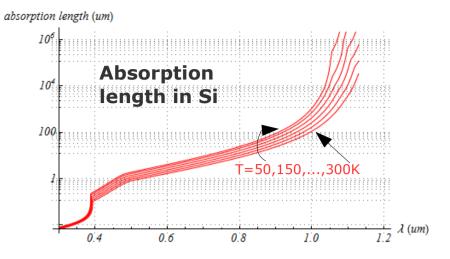
probability for a carrier traversing the high-field to generate the avalanche

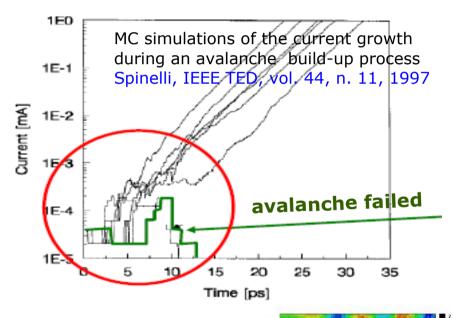
 $\rightarrow \lambda$, T and ΔV dependent

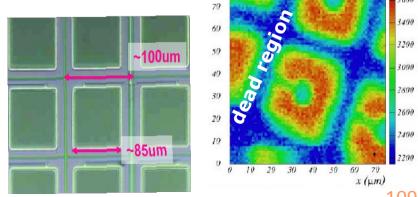
FF: geometrical Fill Factor

fraction of dead area due to structures between the cells, eq. quard rings, trenches

→ moderate ∆V dependence (cell edges)



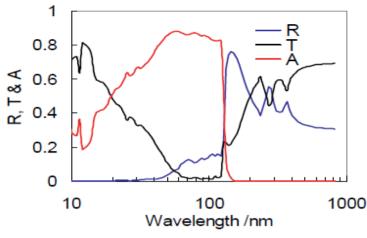




QE factors

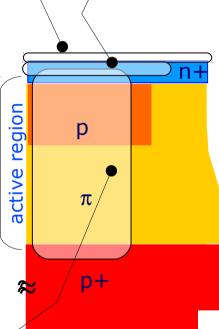
optical T,A,R of the entrance window (SiO₂/Si₃N₄ dielectric on top of Si)

→ angular and polarization dependence



R,T,A coeff. in SiO₂ (example: 30nm SiO₂ on Si layer)

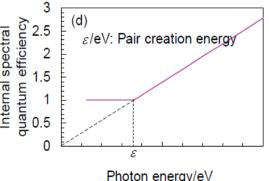
carrier recombination loss: collection efficiency (CE) front, depl. region, back



- **Collection Efficiency** Collection Efficiency $S_f = 5x10^4 [cm/s]$ 8.0 0.6 0.1 0.4 0.2 $S_f=10^a [cm/s]$ 100 1000 100 Wavelength /nm 10 10 1000 (a) (b) Wavelength /nm
- \rightarrow front region critical for 60nm < λ < 400nm
- → CE depends on surface recombination velocity S_f
- → freeze-out at low T

internal quantum efficiency: prob. to photo-generate an e-h pair ~ E_{photon} (above threshold)

G.Collazuol - SNRI 2016

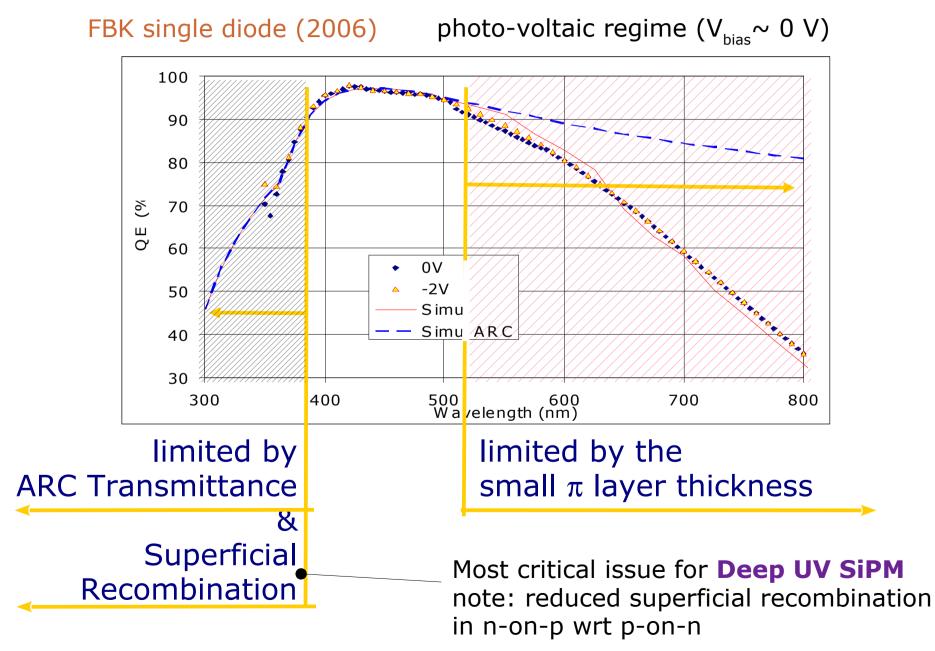


Commercial devices PDE → 0 in VUV due to:

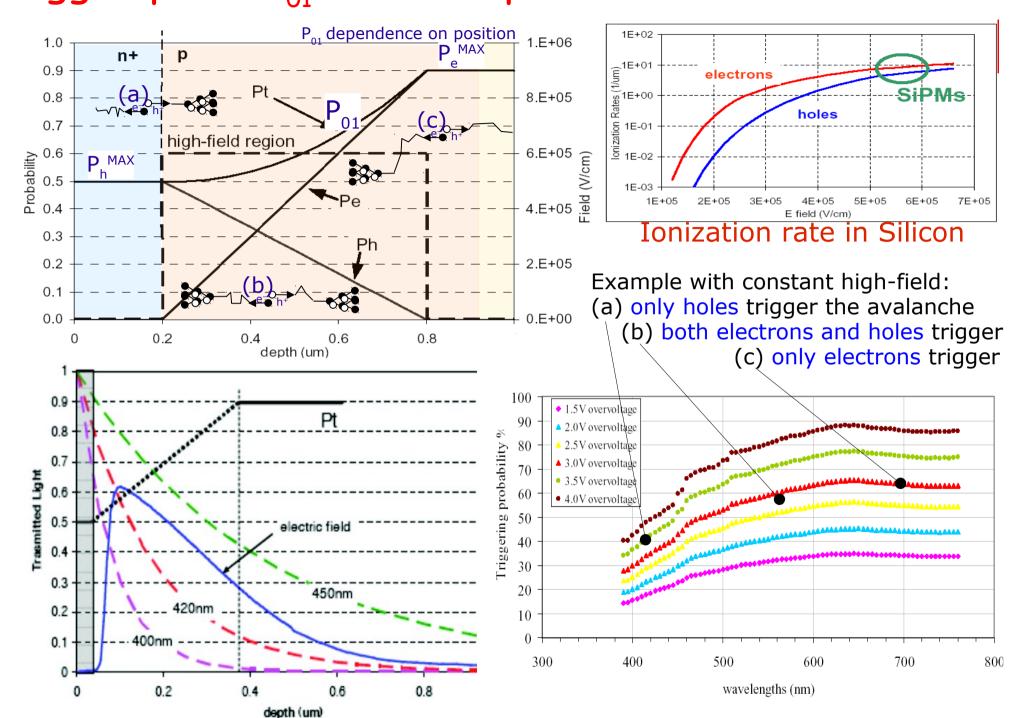
- 1) protection coating (epoxy resin/silicon rubber)
- 2) reflectivity of Oxide/Nitride layers
- 3) insensitive top layer (p+ layer with $E_{field} \sim 0$)
- 4) high reflectivity for VUV on Si surface
- 5) absorption length in Si VUV photon: a few nm
- 6) superficial recombination

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QE -> PDE dependence on wavelength I



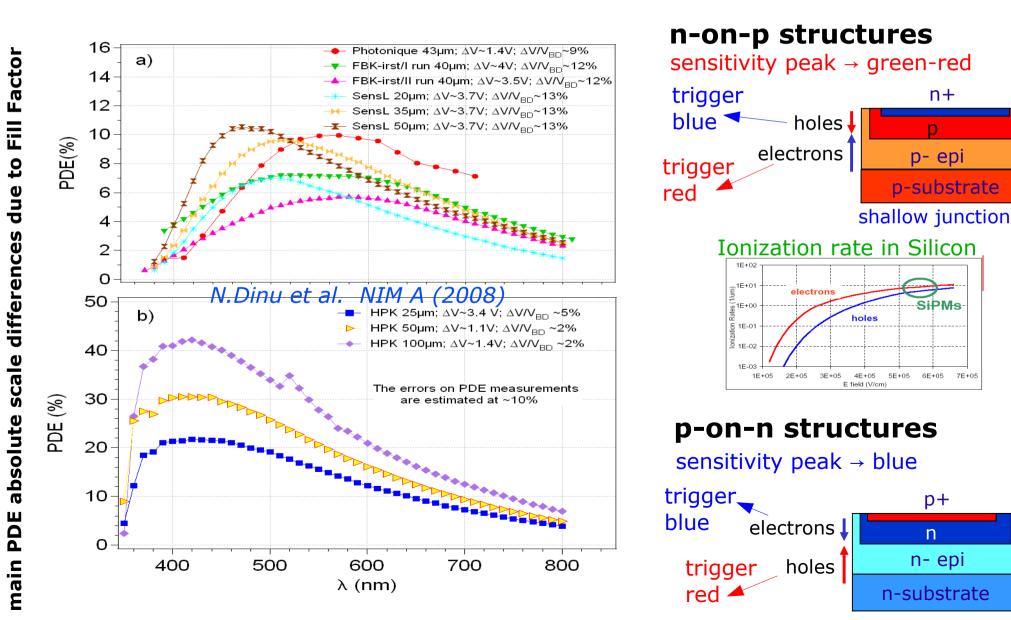
Trigger prob. $P_{01} \rightarrow PDE$ depends on I and DV



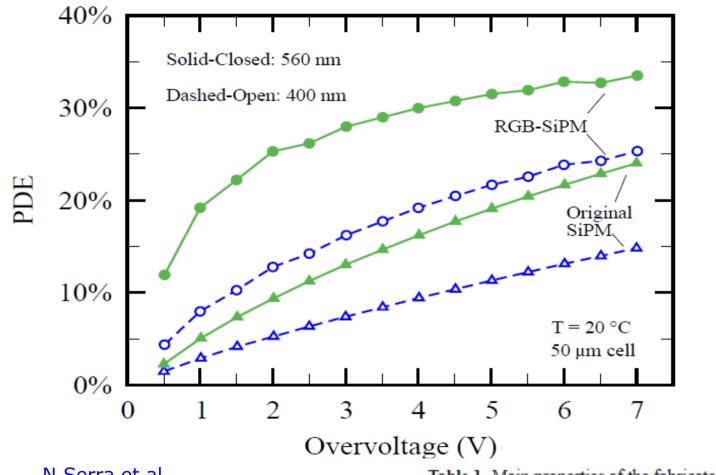
Avalanche Triggering Probability \rightarrow PDE shape vs λ

Tuning PDE spectrum: (matching applications)

- junction depth (shallow → reach trough)
- junction type (p-on-n or n-on-p)



Improving PDE - (1) E field engineering



"RGB" FBK devices
vs
older devices

(same fill factor)

N.Serra et al JINST 8 (2013) P03019

Table 1. Main properties of the fabricated RGB-SiPMs.

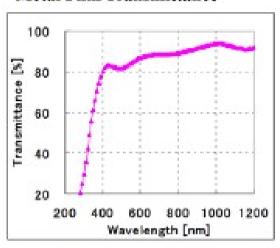
FBK n-on-p RGB-SiPMs.				
SiPM size (mm ²)	1×1 , 3×3 , 4×4	Gain (1)	$4 \cdot 10^{6}$	
Cell pitch (µm)	25-50-70-100	R quenching (at 20°C)	$500~\mathrm{k}\Omega$	
Fill-factor (%)	21-45-58-72	Cell capacitance (1)	170 fF	
<i>V</i> _{BD} (at 20°C)	28.5 V	Rise time $(1)(2)$	5.6 ns	
Dark count rate (1)	480 kHz	Recovery time (1)(2)	350 ns	

(1) $1 \times 1 \text{ mm}^2 \text{ SiPM}$, 50 μm cell at 20°C, OV=4 V; (2) Single-cell pulse, see figure 2.

Improving PDE - (2) Metal Film quenching R

Quenching resistors occupy some of the cell's sensitive area. They are non-transparent for UV/blue/green light. The loss of sensitivity can be significant (especially for small cells).

Metal Film Transmittance



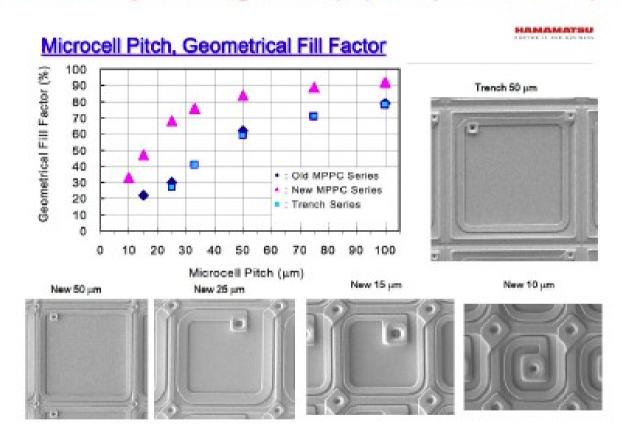
(HPK: Koei Yamamoto, 2nd SiPM Advanced Workshop, March 2014)

Good Uniformity of resistance (full 6-inch wafer)

Width	Poly-Si	Metal
2 μm	19%	9%
1 µm	37%	11%

Low Temperature coefficient of resistance

Poly-Si	Metal
-2.37 kΩ	-0.43 kΩ

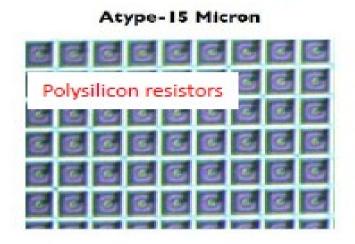


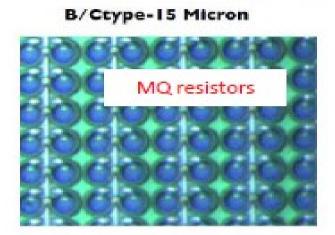
Another advantages of MFQ resistors are better uniformity and relatively small temperature coefficient → smaller cell recovery time change with temperature

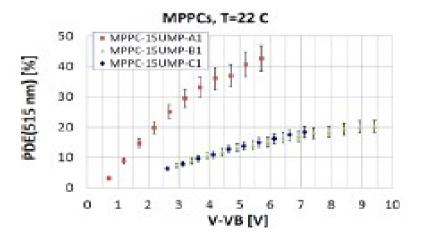
Y.Musienko – SiPM Review - RICH 2016

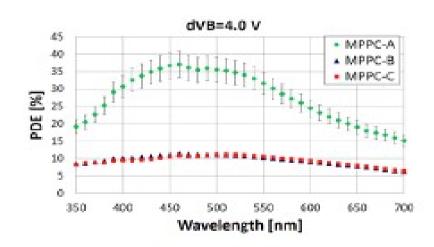
Improving PDE - (2) Metal Film quenching R

MPPCs developed by HPK for the CMS HCAL Upgrade project



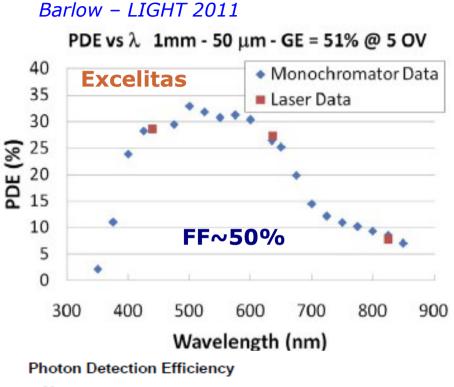


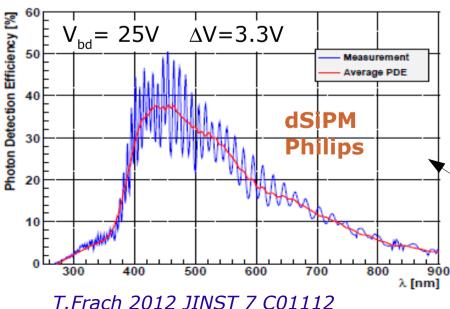




PDE(515 nm)>30% for 15 μm cell pitch MQR MPPCs. It was improved by a factor of >3 in comparison to the 15 μm cell pitch MPPCs with polysilicon quenching resistors.

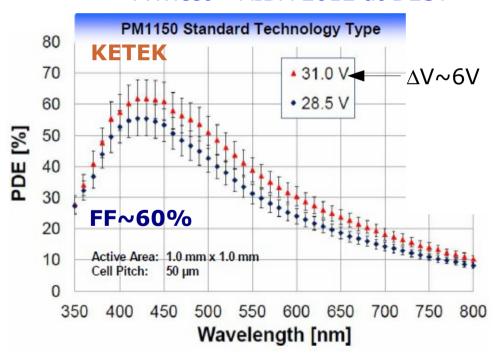
Examples of improvement in PDE





- → PDE peak constantly improving for many devices
- → every manufacturer shape PDE for matching target applications

F.Wiest - AIDA 2012 at DESY



dSiPM (latest sensor 2011)

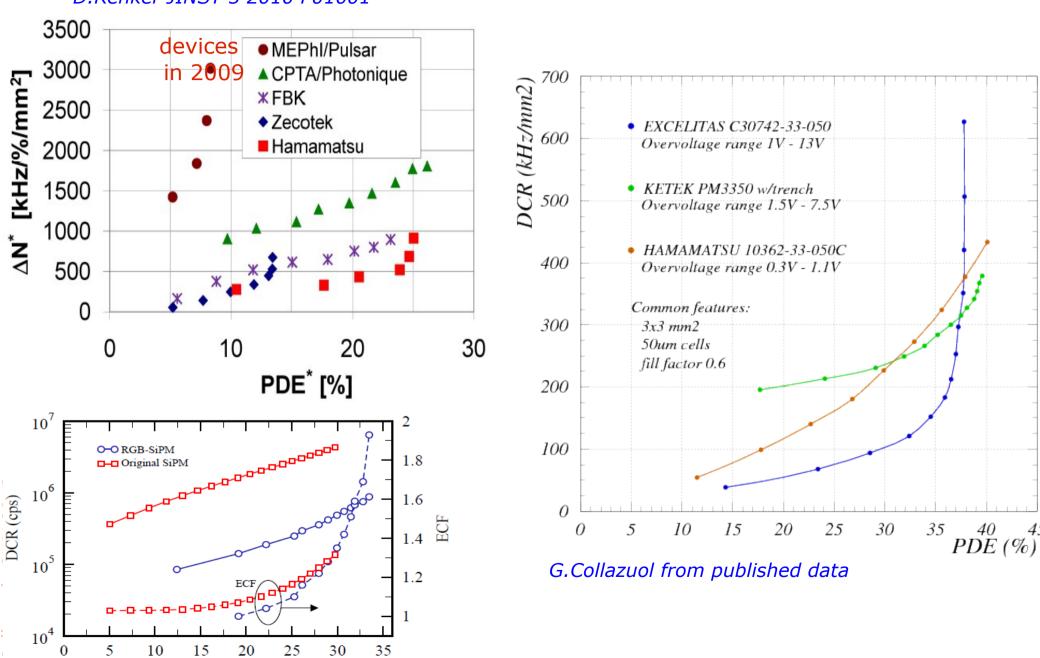
- → up to now no optical stack optimization
- → no anti-reflecting coating
- → potential improvement up to 60% peak PDE (Y. Haemish at AIDA 2012)

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Note: fair comparison plot → PDE vs Noise

D.Renker JINST 5 2010 P01001

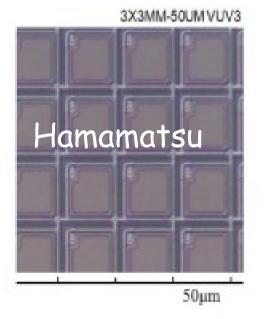
PDE (%)

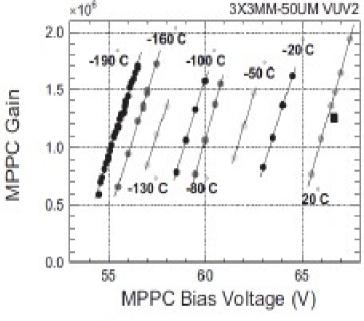


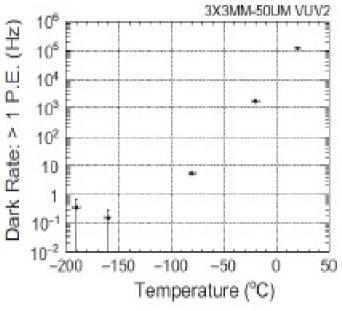
Serra et al (FBK) JINST 8 2013 P03019

New development: VUV SiPM

- LAr, LXe and LKr scintillation → VUV sensitive SiPM!







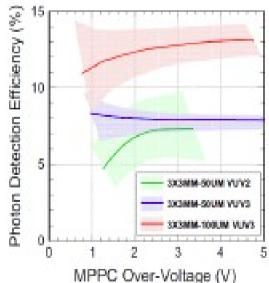
→ removal of protection coating

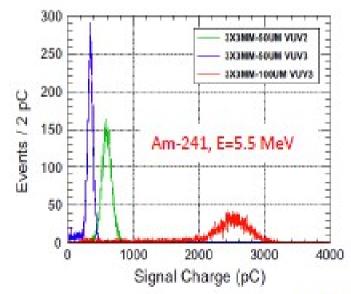
- proper passivation

- thinner junction

→ optimization of the parameters

- superficial layer optics & E field





The PDE(128 nm) was measured ~8% for 50 um pitch SiPMs and ~13% for 100 µm pitch SiPM at dVB=3 V



(NIM A833 (2016) 239-244)

VUV SiPM - Hamamatsu development w/MEG exp.

→ LXe

Basic performance of MPPC have been measured by using 20 LXe chamber.

- LED and alpha source are used as light sources
- 1 p.e. peak is clealy resolved for large area (12×12 mm²) MPPC.

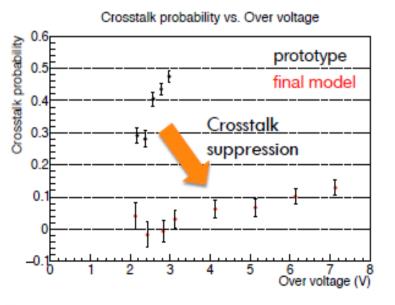
S.Ogawa – VCI 2016

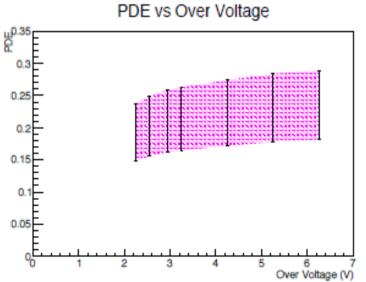
Example of the charge distribution using LED

Op.e. 1p.e.

Excellent performance of MPPCs have been measured.

- □ Gain: 8.0×10⁵ (@ Vover=7V, series connection)
- Low crosstalk probability (~15% @ Vover=7V)
 and wider operation voltage thanks to the crosstalk suppression
- Sufficient PDE for Xe scintillation light (PDE > 15%)







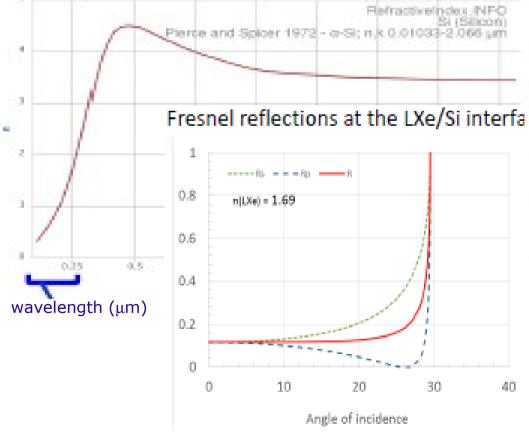
U

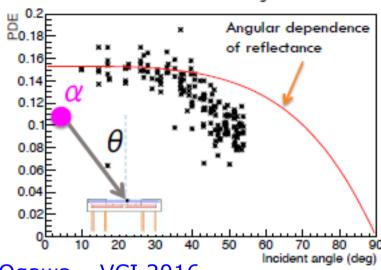
Note: Optics in LXe (Si worse than quartz)

V.Chepel - Weizmann Ins. Sci. 2015

n(Si for 177 nm) = 0.832Strong refraction index mismatch n(LXe for 178 nm) = 1.69(Solovov e.a., NIMA516(2004)462; Hitachi, e.a. JCP133(2005)234508) Critical angle 29.5° Integrated solid angle 0.13×2π Adding 12% reflection > 11% of isotropic light can enter Si

Refraction index of Si (amorphous)





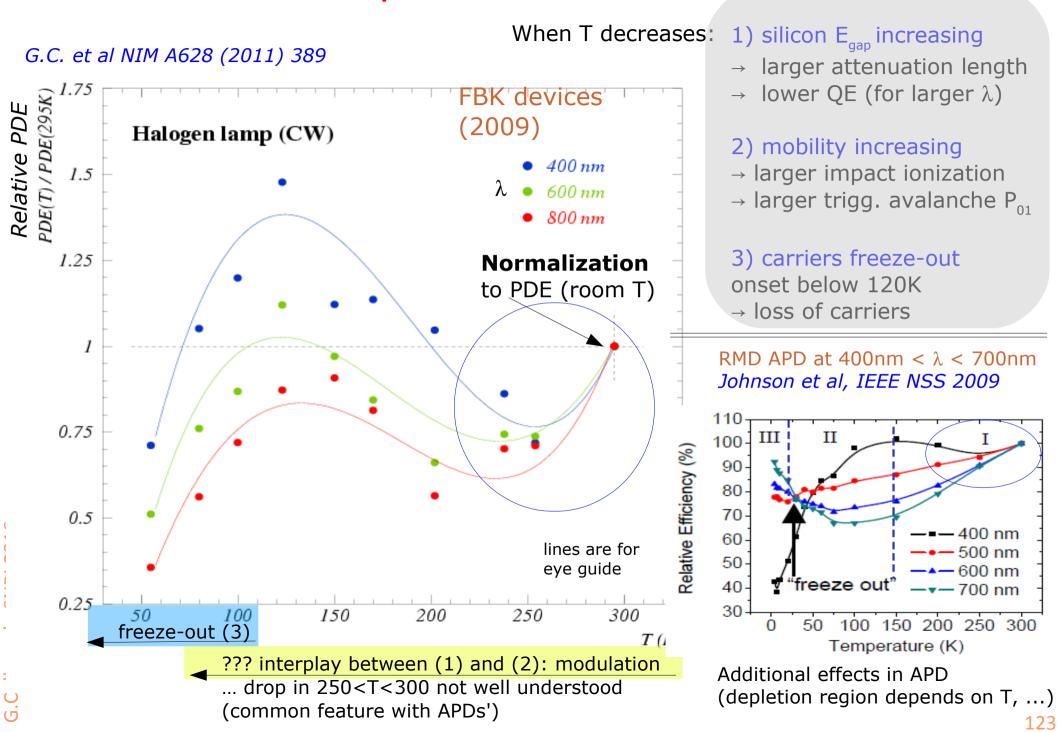
S.Ogawa - VCI 2016

- We found that PDE has larger incident angle dependence.
 - Larger than the angular dependence of the reflectance at the Si surface.

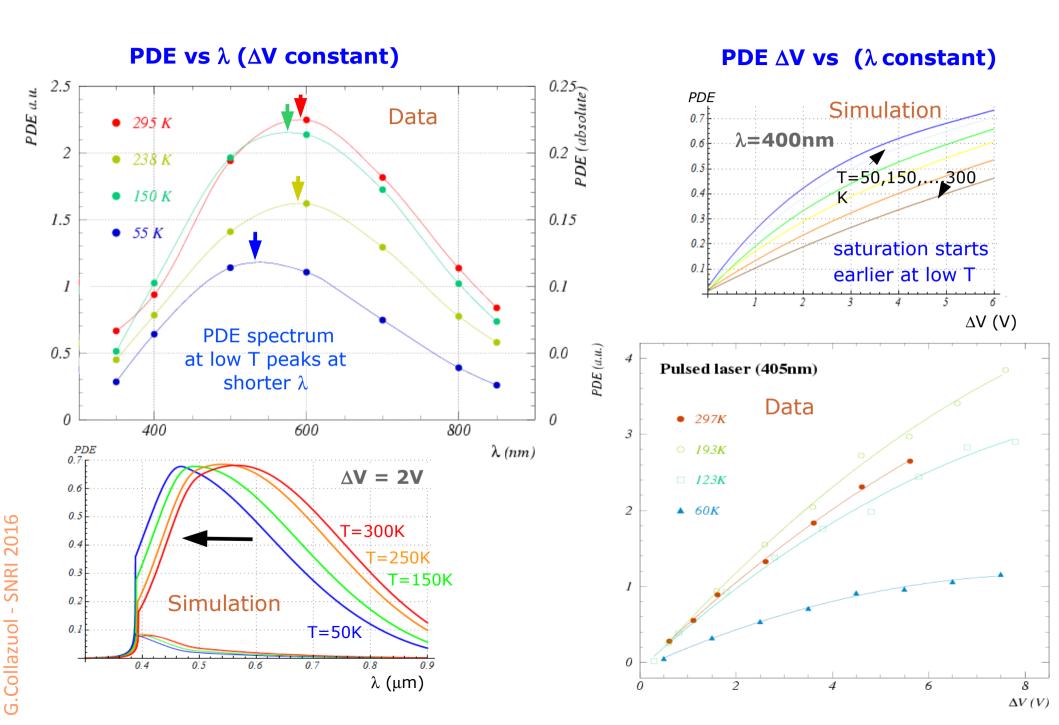
- Effect to the final detector performance has been estimated by MC simulation.
 - Reconstructed depth is biased to shallower, if the larger angular dependence is NOT correctly included in the reconstruction.
 - We are planning to measure the angular dependence in a dedicated setup.

See.

Note: PDE vs Temperature (△V constant)

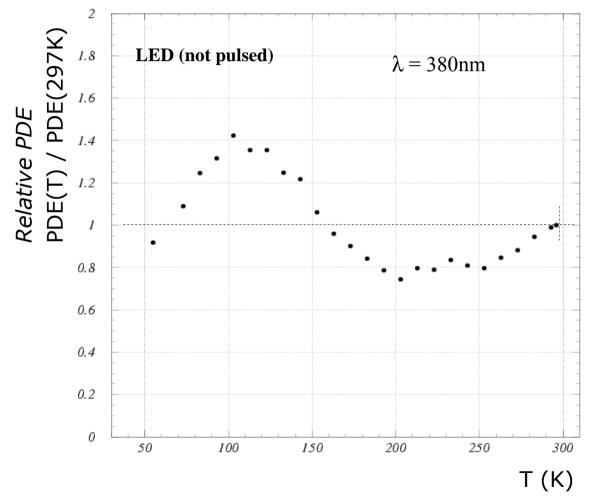


PDE shape: changing with Temperature

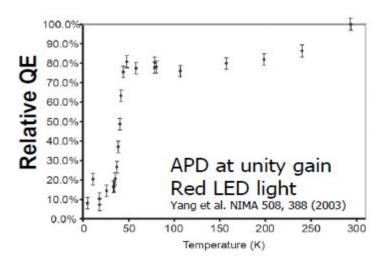


PDE vs Temperature ($\Delta V = 2V$) - LED and Laser

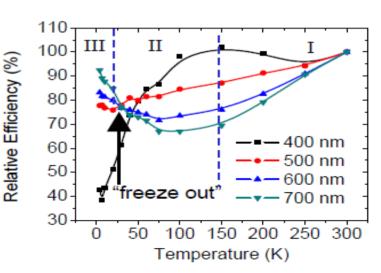
PDE dependence on T at constant gain: similar results with LED (cont. light - 380nm) and Laser (pulsed light - 405nm)



 $PDE(T) \equiv I_{SiPM}(T) / I_{LED}$ Normalization with PDE at T=297K Some common features with APDs (proportional mode)



APD at $400 \text{nm} < \lambda < 700 \text{nm}$ Johnson et al, IEEE NSS 2009



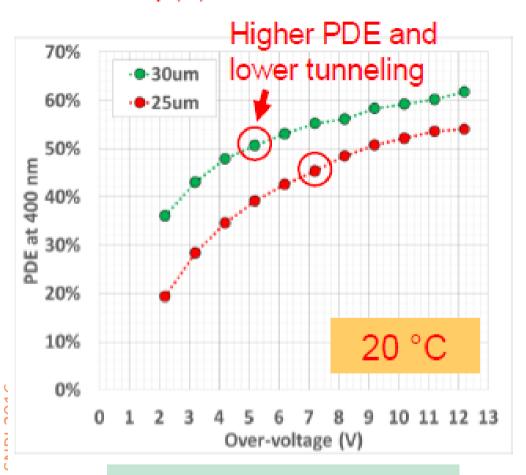
Additional effects in APD (depletion region depends on T, ...)

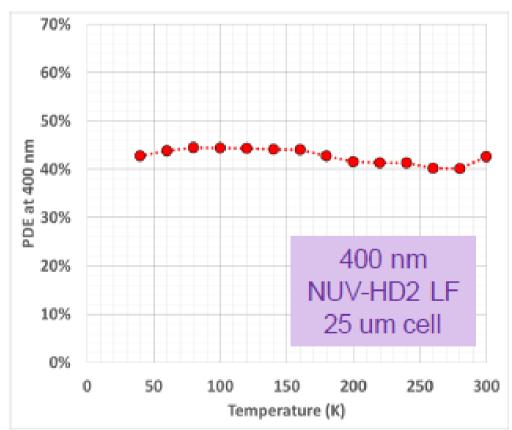
PDE vs Temperature - A.Gola et al @ LNGS

A.Gola et al - IEEE NSS 2015

We used a pulsed, low-level light source and the p(0) method to calculate the PDE.

TPB emission between 400 and 450 nm





PDE for different NUV-HD LF cell sizes.

PDE variations with temperature

Timing

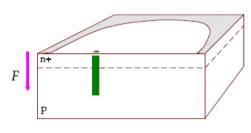
1) SiPM are intrinsically very fast

Two timing components (related to avalanche developement)

- prompt → gaussian time jitter below **100ps** (depending on ΔV , and λ)
- delayed \rightarrow non-gaussian tails up to **few ns** (depending on λ)
- 2) Factors affecting practical timing measurements

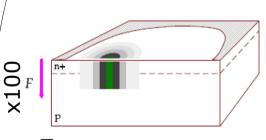
3) Optimization of devices for timing

GM-APD avalanche development



Longitudinal multiplication

Duration ~ few **ps**Internal current
up to ~ few **uA**



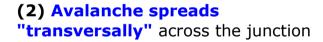
Transverse multiplication

Duration ~ few **100ps**

Internal current up to ~ several **10μA** (1) Avalanche "seed": free-carrier concentration rises exponentially by "longitudinal" multiplication

(1') Electric field locally lowered (by space charge R effect) towards breakdown level

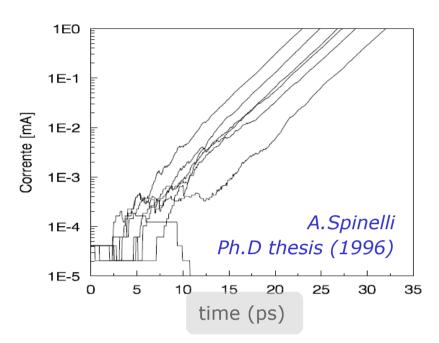
Multiplication is self-sustaining Avalanche current steady until new multiplication triggered in near regions

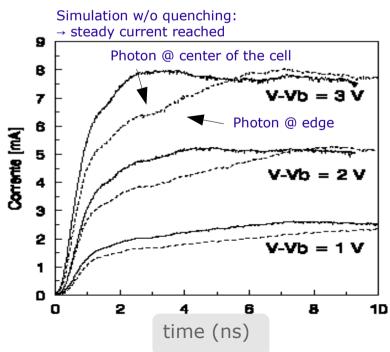


(diffusion speed ~up to 50μm/ns enhanced by multiplication)

(2') Passive quenching mechanism effective after transverse avalanche size ~10μm

(if no quench, avalanche spreads over the whole active depletion volume → avalanche current reaches a final saturation steady state value)



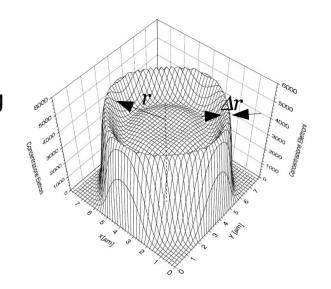


GM-APD avalanche transverse propagation

Avalanche transverse propagation by a kind of shock wave: the wavefront carries a high density of carriers and high E field gradients (inside: carriers' density lower and E field decreasing toward breakdown level)

$$\frac{dS}{dt} = \frac{d}{dt} 2 \pi r(t) \Delta r = 2 \pi v_{diff} \Delta r = 4 \pi \Delta r \sqrt{\frac{D}{\tau}}$$

Rate of current production:
$$\frac{dI}{dt} = \frac{dI}{dS} \frac{dS}{dt} \sim \frac{\sqrt{D}}{R_{sp} \sqrt{\tau}}$$



$$\frac{dI}{dS} = J = \frac{V_{bias}}{R_{sp}(S)}$$

Internal current rising front: the faster it grows, the lower the jitter dI/dt → understand/engineer timing features of SiPM cells

 $S = \text{surface of wavefront (ring of area } 2\pi \text{ r}\Delta\text{r})$

 $R_{sp}(S)$ = space charge resistance ~ $w^2/2\varepsilon v$ ~ $O(50~k\Omega~\mu m^2)$

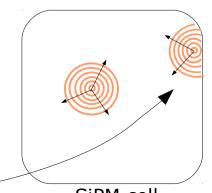
 $v_{diff} \sim O(\text{some 10}\mu m/\text{ns})$

 $D = \text{transverse diffusion coefficient} \sim O(\mu m^2/\text{ns})$

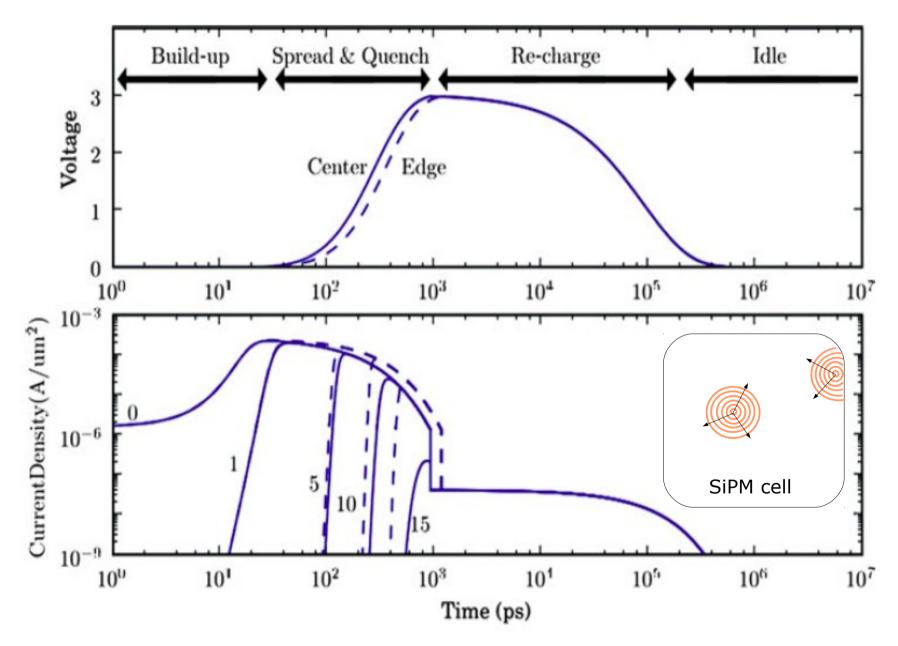
 τ = longitudinal (exponential) buildup time ~ O(few ps)

$$\tau \sim \frac{1}{1 - (E_{max}/E_{breakdown})^n}$$

- → timing resolution improves at high V_{bias}
- \rightarrow E field profile affects τ and R_{sp} (wider E field profile \rightarrow smaller R) (should be engineered when aiming at ultra-fast timing)
- \rightarrow T dependence of timing through τ and D
- → slower growth at GAPD cell edges → higher jitter at edges reduced length of the propagation front



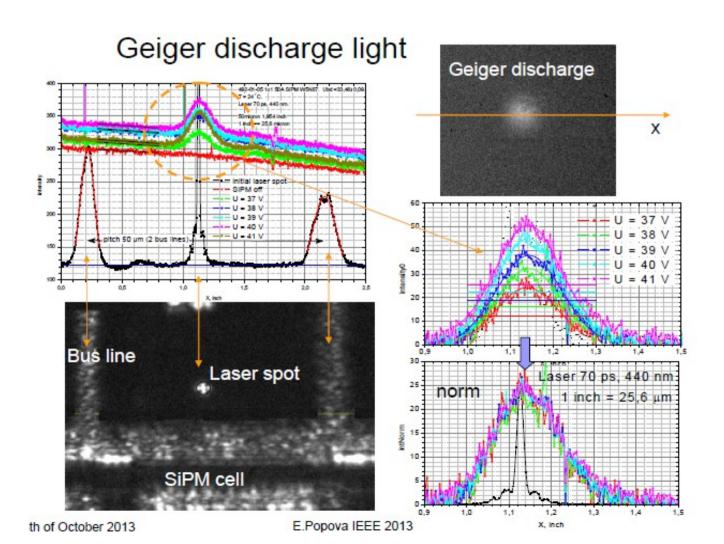
Avalanche transverse propagation (simul.)



Slower growth at GAPD cell edges → larger cells ↔ larger jitter

Discharge transverse size in SiPM and pulse shape simulation

→ Interesting measurements and hybrid model of avalanche development and signal formation by R.Mirzoyan et al (see E.Popova at IEEE NSS 2013)



Spot size of Avalanche

- 1) $O(10)\mu m$
- 2) independent of over-voltage
- 3) mild dependence from cell size

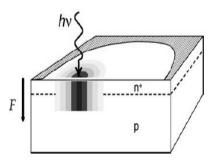
Timing jitter: prompt and delayed components

1) Prompt component: gaussian with time scale O(100ps)

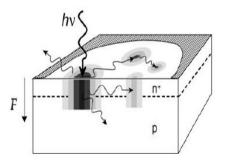
Statistical fluctuations in the avalanche:

- Longitudinal build-up (minor contribution)
- Transversal propagation (main contribution)
 - via multiplication assisted diffusion (dominating in few μm thin devices)

 A.Lacaita et al. APL and El.Lett. 1990
 - via photon assisted propagation (dominating in thick devices O(100μm)) PP.Webb, R.J. McIntyre RCA Eng. 1982 A.Lacaita et al. APL 1992



Multiplication assisted diffusion



Photon assisted propagation

Fluctuations due to

- a) impact ionization statistics
- **b)** variance of longitudinal position of photo-generation: finite drift time even at saturated velocity note: saturated ve ~ 3 vh (n-on-p are faster in general)
- → Jitter at minimum → O(10ps)
 (very low threshold → not easy)

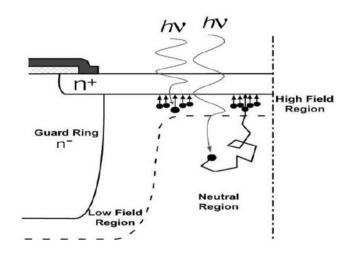
- ► Fluctuations in shock-wave due to
- **c)** variance of the transverse diffusion speed v_{diff}
 - **d)** variance of transverse position of photo-generation: slope of current rising front depends on transverse position
 - → Jitter → O(100ps)
 (usually threshold set high)

Timing jitter: prompt and delayed components

2) delayed component: non-gaussian tails with time scale O(ns)

Carriers photo-generated in the neutral regions above/beneath the junction and reaching the electric field region by diffusion

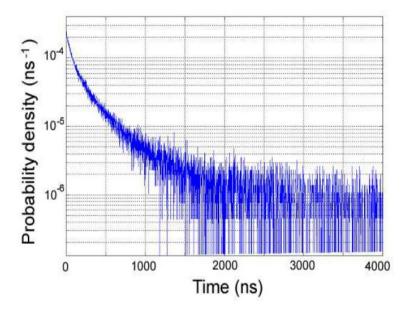
G.Ripamonti, S.Cova Sol.State Electronics (1985)



tail lifetime: $\tau \sim L^2/\pi^2 D \sim up$ to some ns

L = effective neutral layer thickness

D = diffusion coefficient



S.Cova et al. NIST Workshop on SPD (2003)

- → **Neutral regions** underneath the junction : timing tails for long wavelengths
- → **Neutral regions** in APD entrance: timing tails for short wavelengths

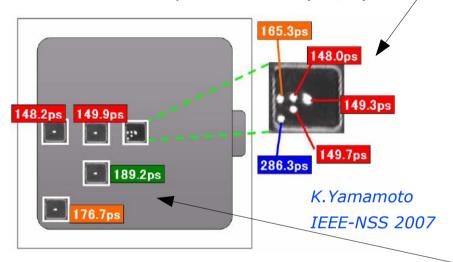
Timing

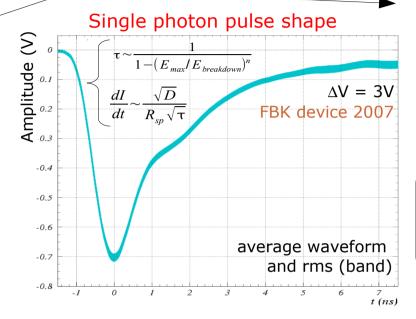
- 1) SiPM are intrinsically very fast
- Two timing components (related to avalanche developement)
- prompt → gaussian time jitter below **100ps** (depending on ΔV , and λ)
- delayed \rightarrow non-gaussian tails up to **few ns** (depending on λ)
- 2) Factors affecting practical timing measurements

3) Optimization of devices for timing

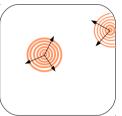
Factors affecting timing measurements

- Leading edge signal shape depends on ΔV, T and impact position:
- 1) ARC/CFD partially effective in canceling time walk effects
- 2) **digital timing filter** might account for shape variations (ΔV , T)





- Due to:
 1) slower
 propagation
 of avalanche
 front
- 2) lower E field at edges



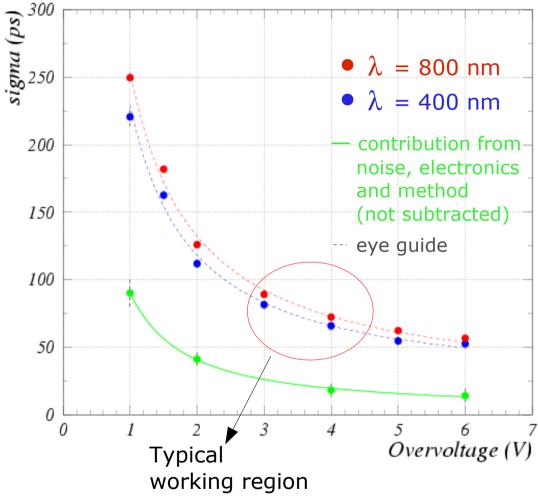
- Additional fluctuations of signal front from non-uniformity among cells in terms of:
 - 1) electric field profile
 - 2) break-down voltage
 - 3) quenching Rq

- 5) parasitic capacitance parallel to Rq
- 4) inductive trace lines from cell to signal pad (see improvements by using Trough Silicon Vias in Hamamatsu devices → Sato et al IEEE NSS 2013)
- trailing edge shape fluctuates (after-pulses) and Pulse width depends on ∆V:
 - → falling signal part not useful for timing (detrimental)
 - → better not to use Time-over-Threshold (for single photon)
- Additional contribution from **baseline fluctuations** (dark pulses, afterpulses)
- Very often electronics contribution dominates

Example of Single Photon Timing Res. ("intrinsic")

hole

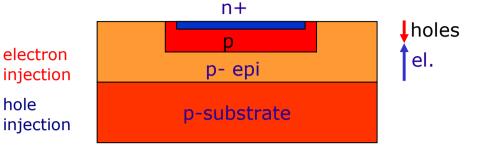
timing measurement with femto-second laser, 2GHz voltage amplifier, 2GHz/20GSs sampling and digital time filtering optimized for SiPM pulse



G.C. et al NIMA 581 (2007) 461

NOTE: good timing performances kept up to 10MHz/mm² photon rates

FBK devices 2007 shallow junction



In general due to drift, resolution differences

- 1) high field junction position
- shallow junction: $\sigma_{t}^{red} > \sigma_{t}^{blue}$
- buried junction: $\sigma_{\scriptscriptstyle +}^{\rm red} < \sigma_{\scriptscriptstyle +}^{\rm blue}$
- 2) n⁺-on-p smaller jitter than p⁺-on-n due to electrons drifting faster in depletion region (but λ dependence)
- 3) above differences more relevant in thick devices than thin

Recent comparative timing measurements:

→ work in progress by Brunner etal at DIRC 2013

Single Photon Time Resolution = gaussian + tails

Time resolution of SiPM is not just a gaussian, but gaussian + tails (in particular at long wavelengths)

G.C. et al NIMA 581 (2007) 461

Data at $\lambda = 400$ nm

A simple **gaussian component** fits fairly

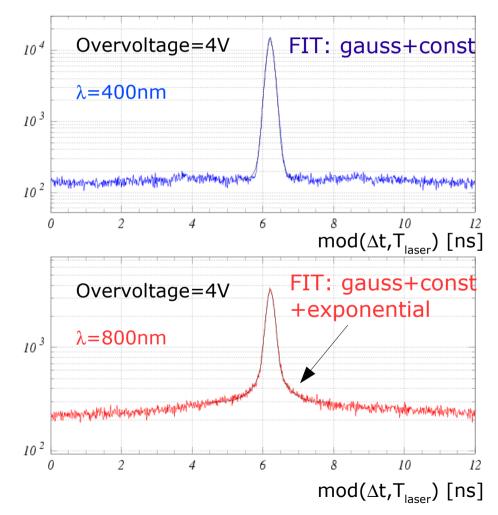
Data at $\lambda = 800$ nm

fit gives reasonable χ^2 in case of an additional exponential term $\exp(-|\Delta t|/\tau)$ summed with a weight

- $\tau \sim 0.2 \div 0.8$ ns (depending on device) in rough agreement with diffusion tail lifetime: $\tau \sim L^2/\pi^2 D$ where L is the diffusion length
- Weight of the exp. tail ~ 10%÷30% (depending on device)

Gaussian + rms ~ 50-100 ps

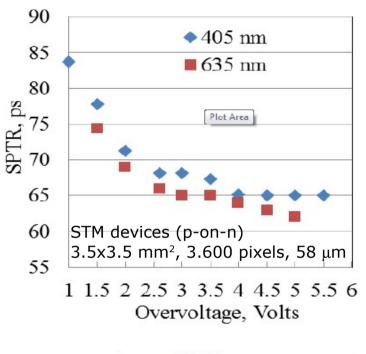
Tails (long λ) ~ exp (-t / O(ns)) contrib. several % for long wavelengths

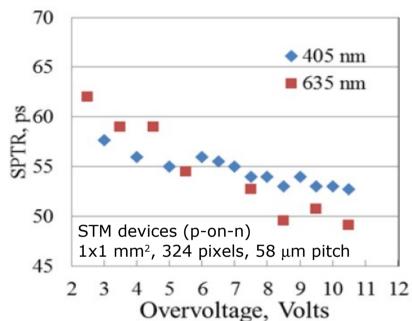


Distributions of the difference in time between successive peaks

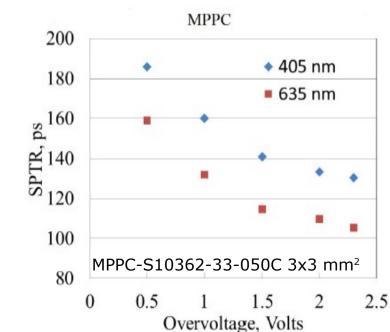
SPTR comparison - various SiPM types

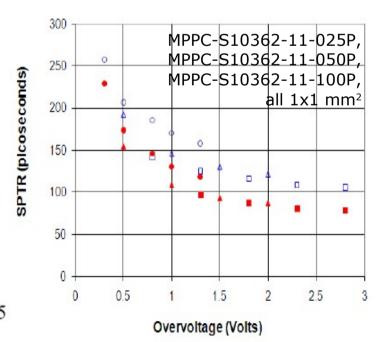
A.Rohzin - PhotoDet 2012





- PiLas (ps) laser
- DRS4 waveform digitizer
- optimized
 timing algorithms
 (library of traces,
 for each SiPM
 type and light
 source)



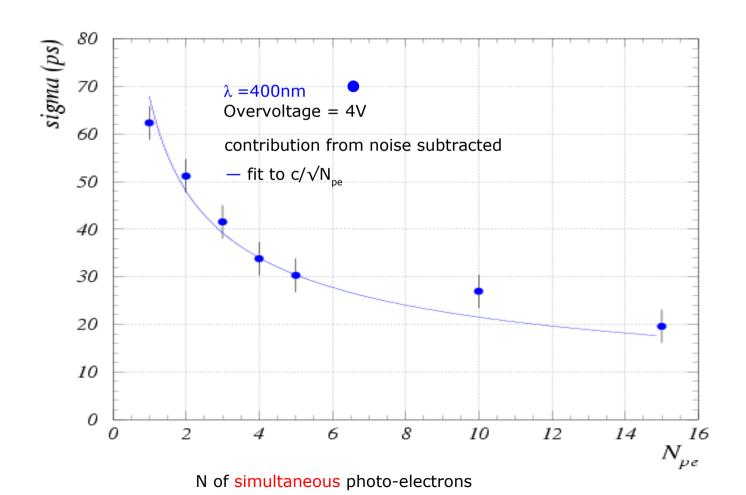




Timing w/ many photons (simultaneous)

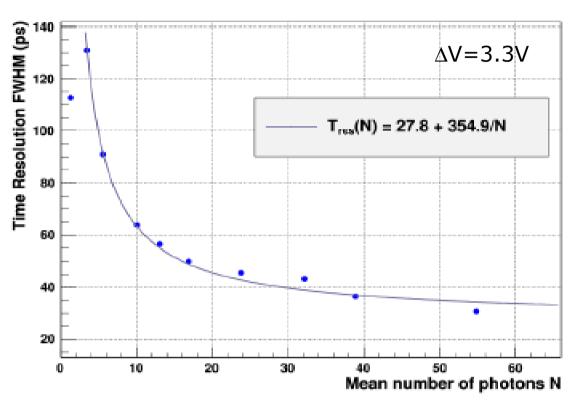
Dependence of SiPM timing on the number of simultaneous photons

Poisson statistics: $\sigma_t \propto 1/\sqrt{N_{pe}}$

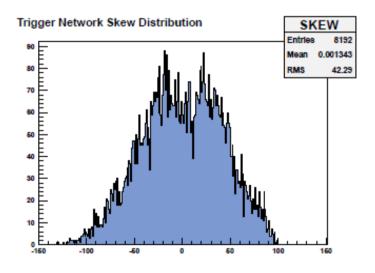


digital-SiPM timing resolution

Time Resolution



T.Frach at LIGHT 2011



- Sensor triggered by attenuated laser pulses at first photon level
- Laser pulse width: 36ps FWHM, λ = 410nm
- Contribution to time resolution (FWHM):

SPAD: 54ps, trigger network: 110ps, TDC: 20ps

Trigger network skew currently limits the timing resolution

Timing at low Temperature

Timing: improves at low T

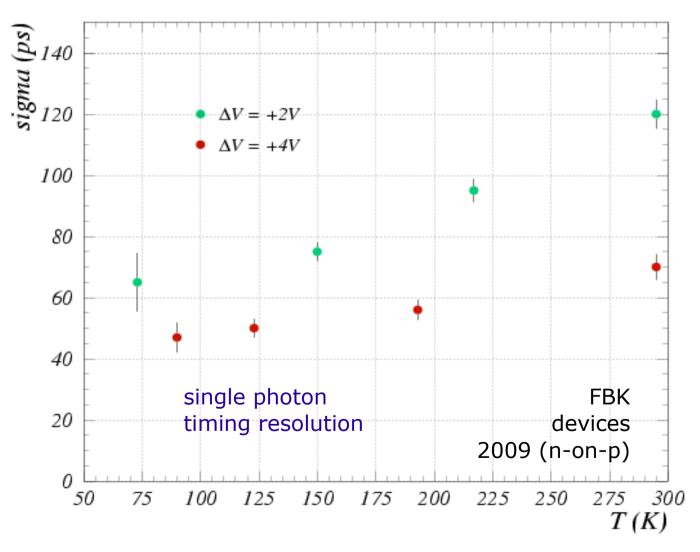
Lower jitter at low T due to

higher mobility:

- a) avalanche process is faster
- b) reduced fluctuations

(Over-voltage fixed)

Note: $\frac{dI}{dt} \sim \frac{\sqrt{D}}{R_{sp}} \sqrt{\tau} \downarrow$



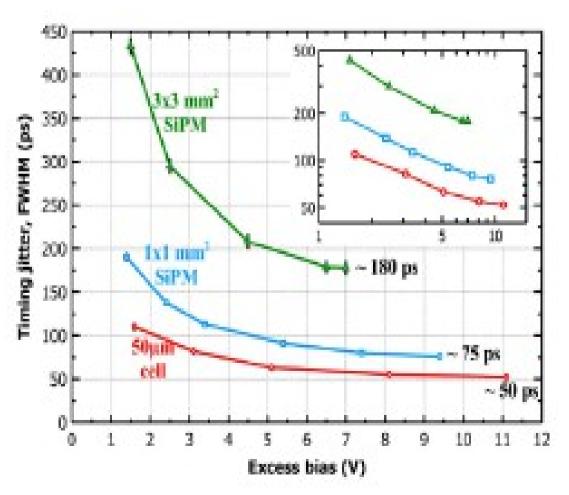
G.C. (2011, unpublished)

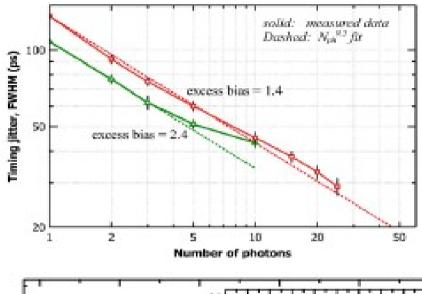
Summary: timing fluctuations

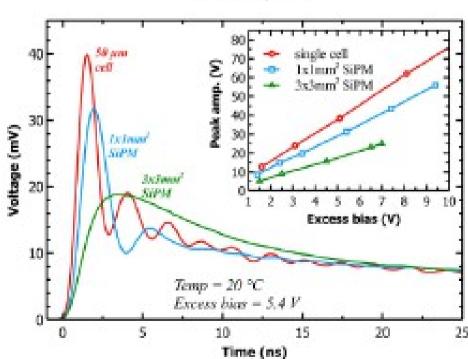
 main contribution at single cell level → lower field at cell edges (with single cell, single photon resolution below 20ps "easily" reached)

2) main contributions at **device level** → **capacitance**

- + X-talk and delayed pulses (multi-photon)
- + signal propagation: second order effect
- + device uniformity: negligible contribution







E.Acerbi et al IEEE TNS 61 5 (2014)

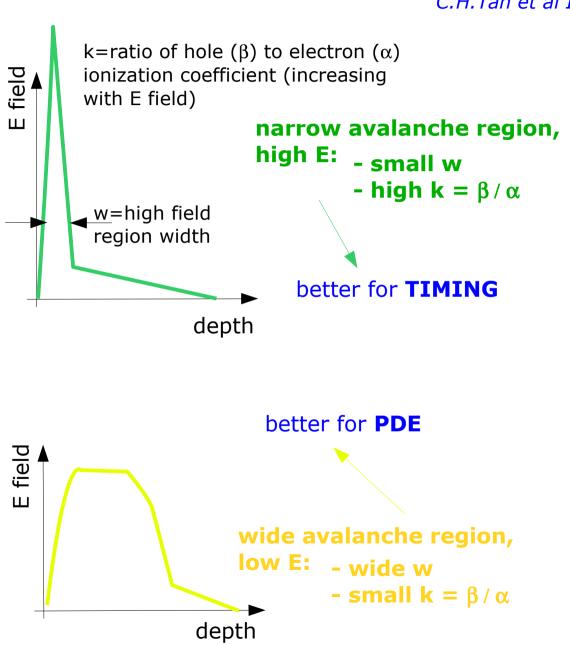
Timing

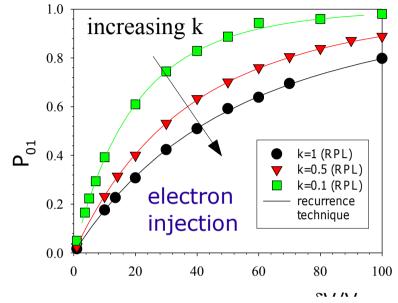
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- 2) Factors affecting practical timing measurements

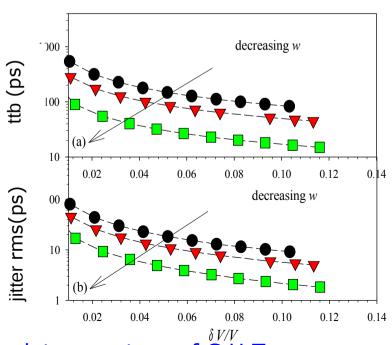
- 3) Optimization of devices for timing
- → trade-off PDE vs Timing
- → enhancing the fast signal component
- → many photons → scintillators

PDE vs timing trade off / optimization

C.H.Tan et al IEEE J.Quantum Electronics 13 (4) (2007) 906



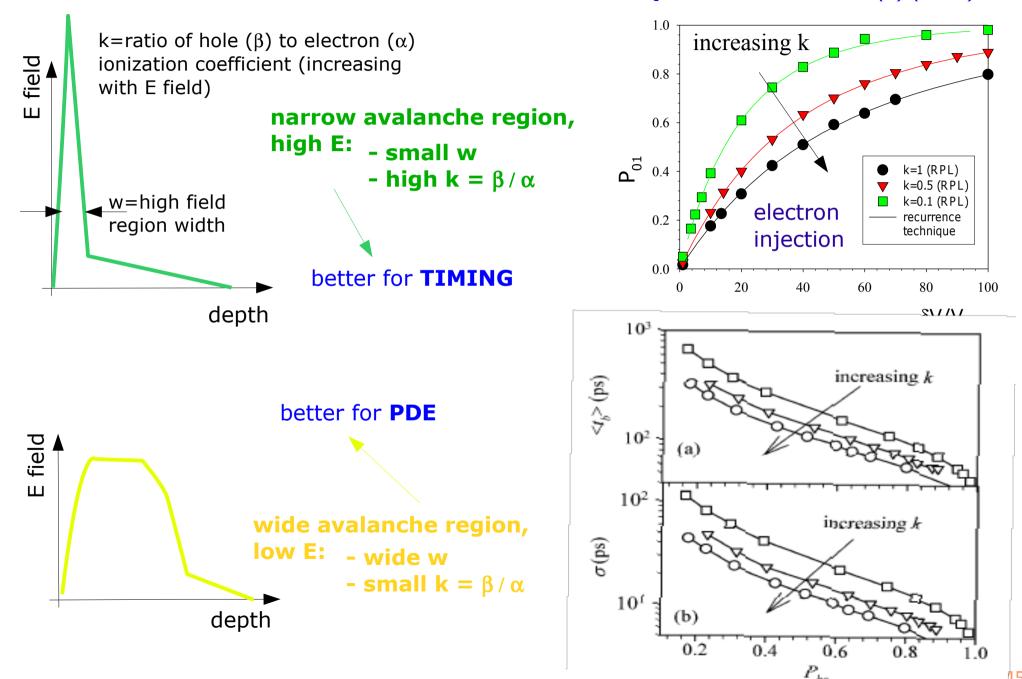




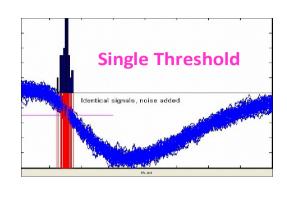
plots: courtesy of C.H.Tan

PDE vs timing trade off / optimization

C.H.Tan et al IEEE J.Quantum Electronics 13 (4) (2007) 906



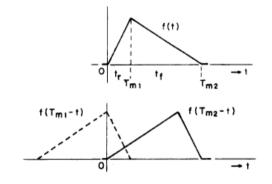
Optimizing signal shape for timing



Timing by (single) threshold:

→ time spread proportional to 1/rise-time and noise (a)

$$\sigma_{time} = \frac{\sigma_{amplitude}}{\frac{df(t)}{dt}}$$



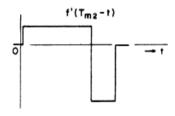
Timing with optimum filtering:

→ best resolution with

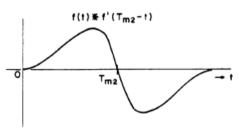
f'(t) weighting function
$$\sigma_{time}^{2} = \frac{\sigma_{amplitude}^{2}}{\int dt \left[\frac{df(t)}{dt}\right]^{2}}$$



(b)



(d)



Pulse sampling and Waveform analysis:

Sample, digitize, fit the (known) waveform

→ get time and amplitude

$$\sigma_{time}^{2} = \frac{\sigma_{amplitude}^{2}}{N_{samples} \int dt \left[\frac{df(t)}{dt}\right]^{2}}$$

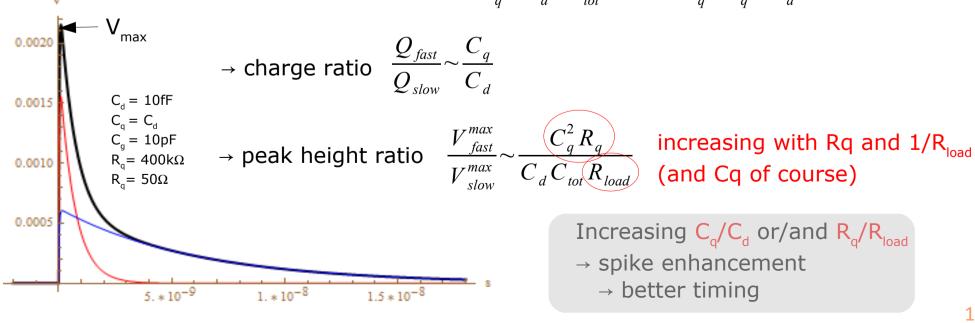
- Optimum filter for timing in presence of white noise (method of derivation).
 - (a) signal waveform
 - (b) optimum filter for amplitude measurements.
 - (c) optimum filter for timing derivative of (b).
 - (d) output waveform.

V.Radeka IEEE TNS 21 (1974)...

Optimizing signal shape for timing

Single cell model \rightarrow (R_d||C_d)+(R_g||C_g) SiPM + load $\rightarrow (||Z_{cell})||C_{arid} + Z_{load}$ Signal = slow pulse $(\tau_{d \text{ (rise)}}, \tau_{q-\text{slow (fall)}})$ + + fast pulse $(\tau_{d \text{ (rise)}}, \tau_{q-\text{fast (fall)}})$ $(N-1)C_d$ $\bullet \tau_{d \text{ (rise)}} \sim R_{d} (C_{a} + C_{d})$ • $\tau_{q-fast (fall)} = R_{load} C_{tot}$ (fast; parasitic spike) • $\tau_{q-\text{slow (fall)}} = R_q (C_q + C_d)$ (slow; cell recovery) **Parasitic** Other **Firing** "grid" microcell microcells capacitance

Pulse shape
$$V(t) \simeq \frac{Q}{C_a + C_d} \left(\frac{C_q}{C_{tot}} e^{\frac{-t}{\tau_{EAST}}} + \frac{R_{load}}{R_a} \frac{C_d}{C_a + C_d} e^{\frac{-t}{\tau_{SLOW}}} \right)$$



Increasing C_d/C_d or/and R_d/R_{load} → spike enhancement → better timing

Opt It can

Optimizing signal shape for timing

It can be shown that there are 2 signal components: fast + slow (recovery) (see eg C. de La Taille at PhotoDet 2012)

$$\frac{V_{fast}^{max}}{V_{slow}^{max}} \sim \frac{C_q^2 R_q}{C_d C_{tot} R_{load}}$$

Increasing C_q/C_d or/and R_q/R_{load}

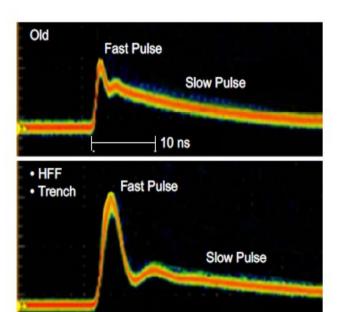
- → spike enhancement → better timing
- → slow recovery tail suppressed
 - → reduced baseline fluctuations

Among new (2013) Hamamatsu structures

- trench insulation (against cross-talk)
- metal resistor
- → enhanced and well controlled amount of "parasitic" Cq

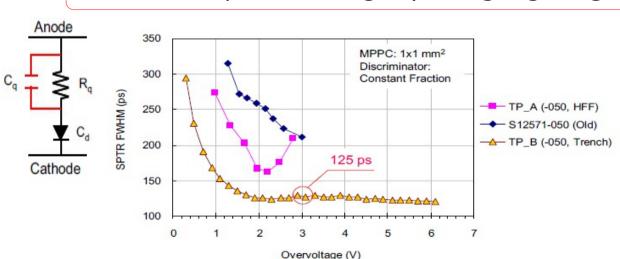
- 1) enhanced fast pulse amplitude
- 2) suppressed slow pulse amplit.





10 ns

- 1) better timing with fast component
- 2) lower sensitivity to baseline fluctuations
- → further improve timing by using higher gain



Optimizing signal shape for timing

SensL new SiPM architecture for fast timing

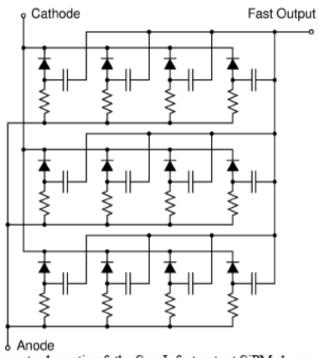


Figure 2: Concept schematic of the SensL fast output SiPM shown as an array of microcells connected in parallel (Courtesy of SensL [9].) Each diode symbol represents an individual p-n junction microstructure. Unlike standard SiPMs, each junction in the SensL device has a connection to a third electrode with a low capacitive coupling.

see also O'Neill et al - PhotoDet 2012

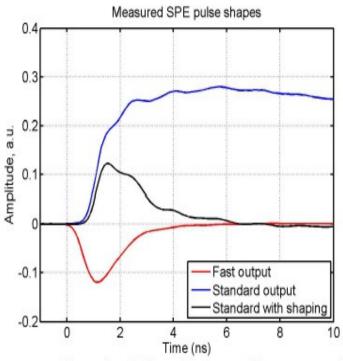


Figure 9: Measured SPE signals from the SensL MicroFB-30035 device (3×3 mm2 area with 35 μ m microcells): fast output (red), standard output (blue), and (black) standard output connected to an external C-R shaping circuit (τ = 2 ns).

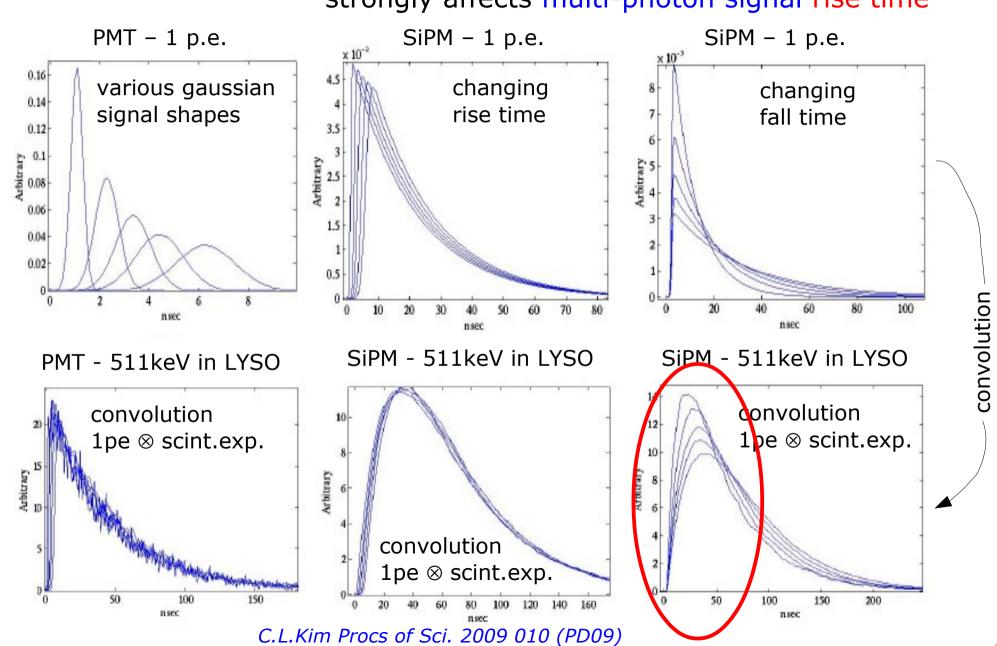
Dolinsky et al – IEEE NSS 2013

Additional **Fast timing output** is shown to be equivalent to external high-pass filtering (clipping) but of more practical use (many photons applications)

For a comparison of timing performances with many photons see Y. Uchiyama et al IEEE NSS 2013

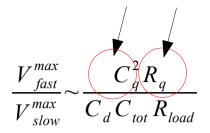
Caveat: Signal shape & timing → scintillators

Single ph.e. signal slow falling-time component $\tau_{fall} = R_q (C_d + C_d)$ strongly affects multi-photon signal rise time



Caveat: Signal shape & timing → scintillators

→ peak height ratio



Enhancing C_q and R_q does improve timing performances

FBK devices type:

- Active area: 4x4mm²;
- Cell size: 67x67µm²;
- Fill factor: 60%;
- C₀+C_D: about 180fF;
- R₀: 1.1M;
- Dark noise rate:
 - \sim 100MHz at DV> 4V

C.Piemonte et al IEEE TNS (2011)

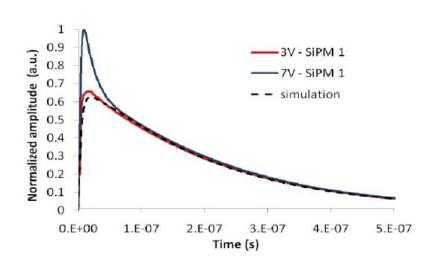




Fig. 2. Test set-up consists of two similar gamma ray detectors (LYSO crystal + SiPM) in coincidence. A ²²Na source (disc in the middle) was used to generate two opposite 511keV photons in coincidence.

- Signal rise-time < 5ns
- CRT ~320ps (*) FWHM triggering at 5% height Both are much better than for different structures with high C_{tot} and/or lower Cq, Rq (rise time up to several x 10ns, CRT > 400ps)
- (*) ~40% from light propagation in crystals

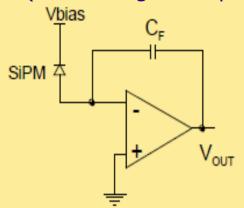
Electronics

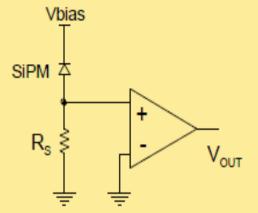
Front-end electronics: general comments

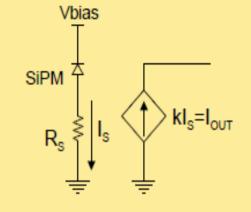
- Strong push for high speed front-end > GHz
- Essential for timing measurements
- Several configurations to get GBW > 10 GHz
- Optimum use of SiGe bipolar transiistors
- Voltage sensitive front-end
- Easiest: 50Ω termination, many commercial amplifiers (MiniCircuits ...)
- Beware of power dissipation
- Easy multi-gain (time and charge)
- Current sensitive front-end
- Potentially lower noise, lower input impedance
- Largest GBW product
- In all cases: importance of reducing stray inductance
- Trend for RO/digitization: ADC/TDC → waveform sampling

Front-end electronics: different approaches

(ASIC integration perspective)







Charge sensitive amplifier

The charge Q delivered by the detector is collected on C_F

If the maximum ΔV_{OUT} is 3V and Q is 50pC (about 300 SiPM microcells), C_F must be 16.7pF

Perspective limitations in dynamic range and die area with low voltage, deep submicron technologies

Voltage amplifier

A I-V conversion is realized by means of R_s

The value of R_s affects the signal waveform

V_{OUT} must be integrated to extract the charge information: thus a further V-I conversion is needed

Current buffer

R_s is the (small) input impedance of the current buffer

The output current can be easily replicated (by means of current mirrors) and further processed (e.g. integrated)

The circuit is inherently fast

The current mode of operation enhances the dynamic range, since it does not suffer from voltage limitations due to deep submicron implementation

ASICs for SiPM signal readout (QDC/TDC)

W.Kucewicz "Review of ASIC developments for SiPM signal readout" - talk at CERN 11-2-2011

Chip Name	Measured quantity	Application	Input configuration	Technology
FLC_SiPM	Pulse charge	ILC Analog HCAL	Current input	CMOS 0,8 μm
FLC_SIFM	ruise charge	ATLAS	current input	CMO3 0,6 μm
MAROC	Pulse charge, trigger	luminometer	Current input	SiGe 0,35 μm
SPIROC	Pulse charge, trigger, time	ILC HCAL	Current input	SiGe 0,35 μm
NITNIO	T :	ALTCE TOP	Differential	CHOC 0.25
NINO	Trigger, pulse width	ALICE TOF	input	CMOS 0,25 μm
PETA	Pulse charge, trigger,time	PET	Differential input	CMOS 0,18 μm
BASIC	Pulse height, trigger	PET	Current input	CMOS 0,35 μm
SPIDER	Pulse height, trigger,			
(VATA64-HDR16)	time	SPIDER RICH	Current input	
RAPSODI	Pulse height, trigger	SNOOPER	Current input	CMOS 0,35 μm

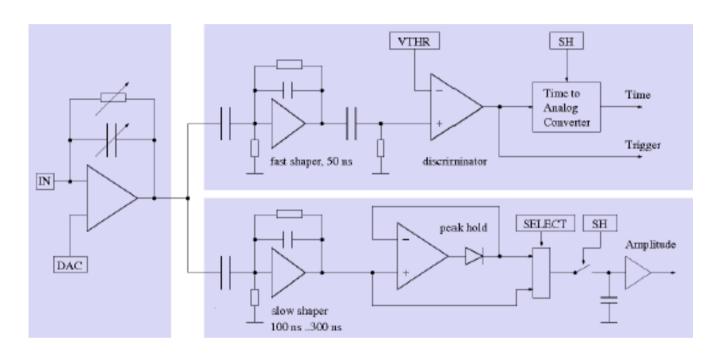
ASICs for SiPM signal readout (QDC/TDC)

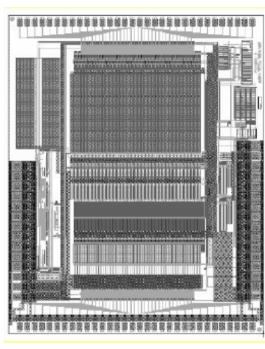
W.Kucewicz - CERN 11-2-2011									
Chip Name	# of channels	Digital output	Power supply	Area [sqr mm]	Dynamic range	Input resistance	Timing jitter	Year	
FLC_SiPM	18	n	5V (0,2W)	10			-	2004	
MAROC2	64	у	5 V	16	80 p <i>C</i>	50 Ω		2006	
SPIROC	36	у	5 V	32				2007	
NINO	8	n	(0,24W)	8	2000 pe	20 Ω	260 ps	2004	
PETA	40	у	(1,2W)	25	8 bit		50 ps	2008	
BASIC	32	у	3,3 V	7	70 p <i>C</i>	17 Ω	~120 ps	2009	
SPIDER (VATA64-HDR16)	64	n		15	12 pC			2009	
RAPSODI	2	у	3,3 V (0,2W)	9	100 p <i>C</i>	20 Ω	-	2008	

⁻ Only a few of the suitable for low light intensity

SPIDER

Chip VATA64-HDR16 was developed for SiPM applied in Ring Imaging Cherenkov Detector of SPIDER (Space Particle IDentifiER) Experiment M.G. Bagliesi et al. "A custom front-end ASIC for the readout and timing of 64 SiPM" Nuclear Physics B (Proc. Suppl.) 215 (2011) 344



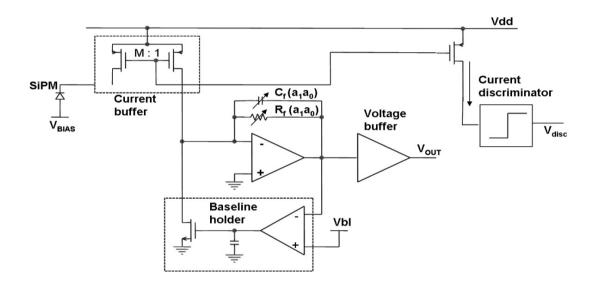


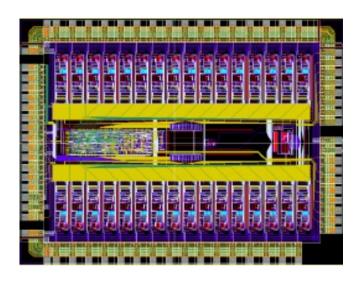
Signal from preamplifier is split in two branches with fast and slow shaper Branch with fast shaper measures time and other one measures charge

- The DAC on the input of preamplifier allows to moderate the bias voltage
- Signal from preamplifier is shaped by fast (50ns) and slow (100-200ns) shapers.
- Discriminator compared the signal from the output of fast shaper and generate the trigger pulse, which start time counter with 40ps resolution
- Signal from slow shaper is sent to peak&hold detector which measure the pulse height

BASIC

BASIC is a 32 channel SiPM readout chip for simultaneous time and energy measurement, made in 0,35 μm CMOS AMS technology (2009). F.Corsi et al "BASIC: a Front-end ASIC for SiPM Detectors" 2009 IEEE NSS Conf Rec





Each front-end channel consists of a current buffer as input, reading on a very low impedance input node the current signal delivered by the detector

The input current buffer is a common gate stage. Feedback applied to increase bandwidth and decrease input resistance. Possible fine tuning SiPM bias by varying Vref. The output of current buffer can be easy replicated by multi-branch current mirrors.

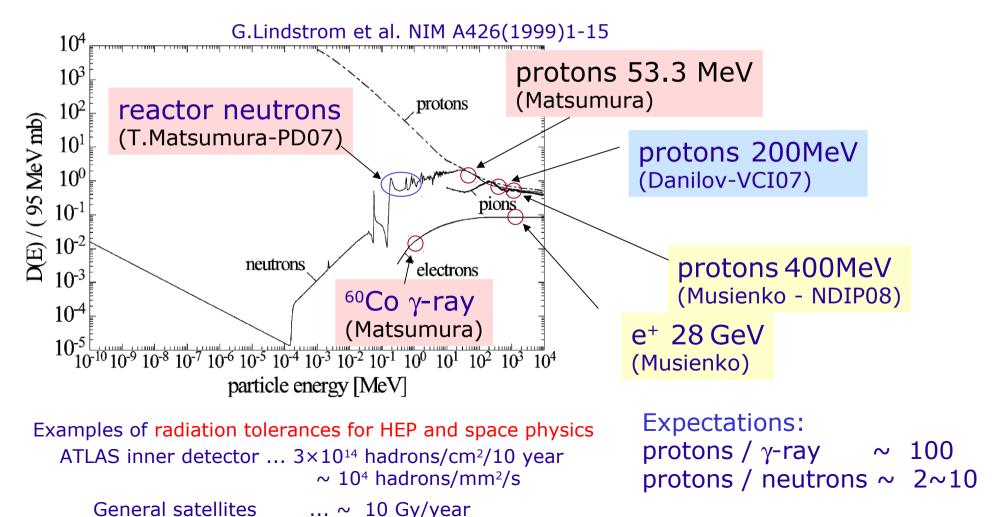
The current mirror at the input allows the splitting of the signal in two branches: one is used to send the output current to a current discriminator, which extracts the trigger signal associated to the timing of the event, while the other is sent to an integrator in order to obtain a voltage proportional to the charge

Radiation damage

Radiation damage: two types

- Bulk damage due to Non Ionizing Energy Loss (NIEL) ← neutrons, protons
- Surface damage due to Ionizing Energy Loss (IEL) $\leftarrow \gamma$ rays (accumulation of charge in the oxide (SiO2) and the Si/SiO2 interface)

Assumption: damage scales linearly with the amount of Non Ionizing Energy Loss (NIEL hypothesis)



Radiation damage: effects on SiPM

1) Increase of dark count rate due to introduction of generation centers

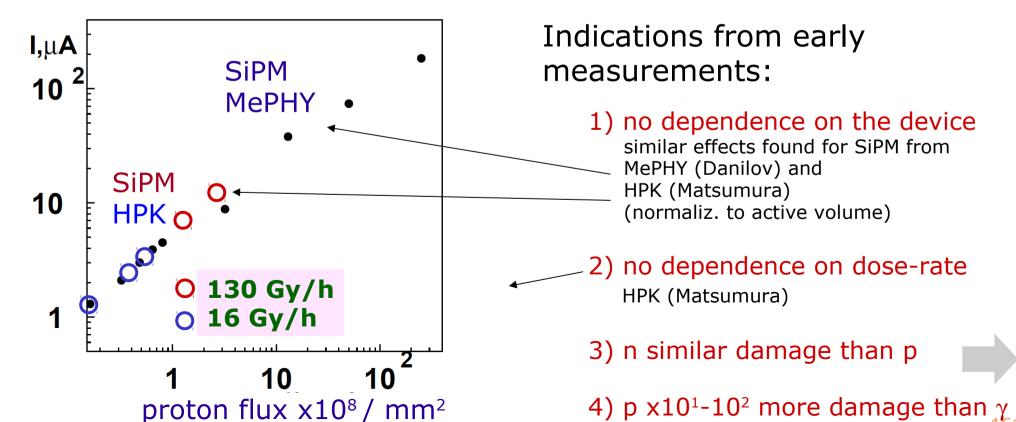
Increase (ΔR_{DC}) of the dark rate: $\Delta R_{DC} \sim (a/q_e) \; \Phi_{eq} \; Vol_{eff} \; P_{01}$ where a $\sim 3 \; x \; 10^{-17} \; A/cm$ is a typical value of the radiation damage parameter for low E hadrons and $Vol_{eff} \sim Area_{SiPM} \; x \; \epsilon_{geom} \; x \; W_{epi}$

NOTE:

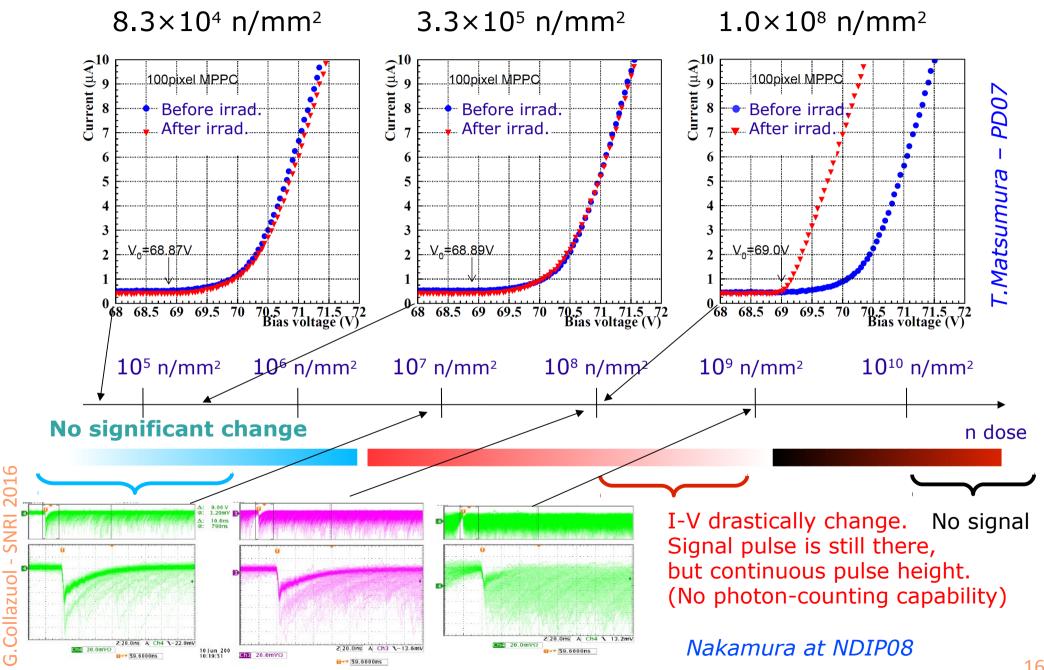
The effect is the same as in normal junctions:

- independent of the substrate type
- dependent on particle type and energy (NIEL)
- proportional to fluence
- 2) Increase of after-pulse rate due to introduction of trapping centers

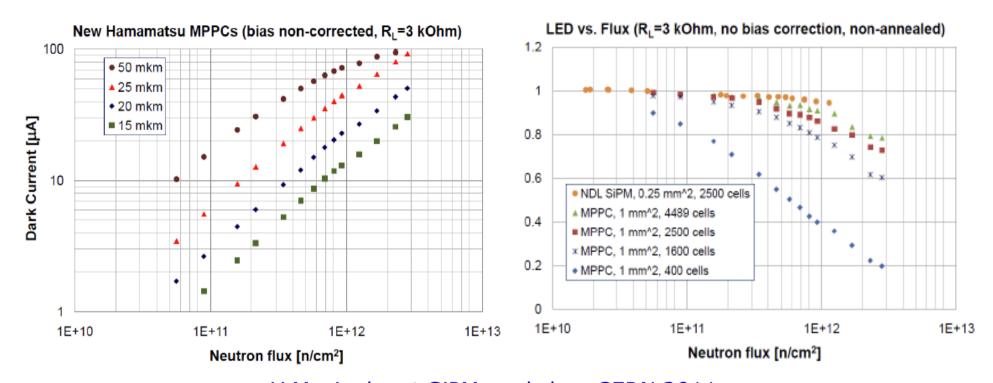
 → loss of single cell resolution → no photon counting capability
- 3) change of breakdown voltage and trigger probability vs Voltage



Radiation damage: neutrons (0.1 -1 MeV)



Radiation damage: recent devices



Y.Musienko at SiPM workshop CERN 2011

- No change of V_{bd} (within 50mV accuracy)
- No change of R_a (within 5% accuracy)
- I_{dark} and DCR significantly increase

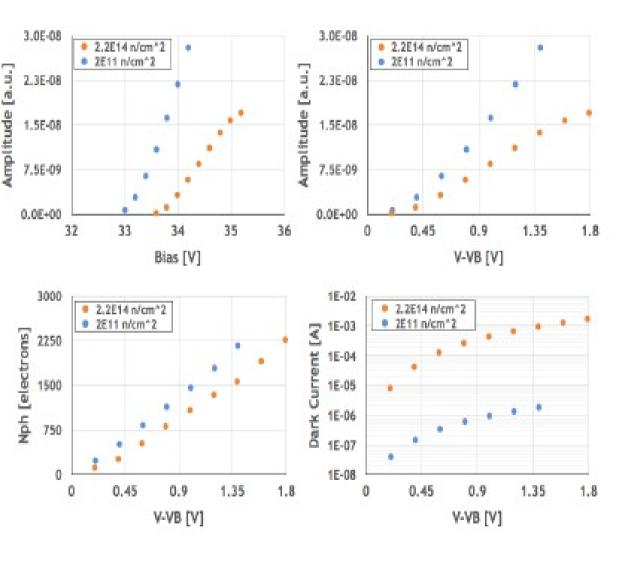
SiPMs with high cell density and fast recovery time can operate up to $3*10^{12}$ n/cm² ($\delta G < 25\%$)

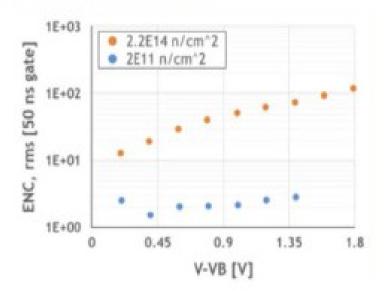
Effects reduced by

- → small cells → smaller gain → small charge flow & trapping
- \rightarrow thin O(1µm) epi-layer... but effective thickness O(10µm)

SiPM irradiated up to 2.2*10¹⁴ n/cm²

Can SiPM survive very high neutron fluences expected at high luminosity LHC? FBK SiPM (1 mm², 12 μ m cell pitch was irradiated with 62 MeV protons up to 2.2*10¹⁴ n /cm² (1 MeV equivalent).





Yu. Musienko, RICH-2016

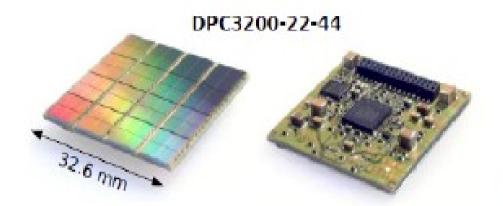
We found:

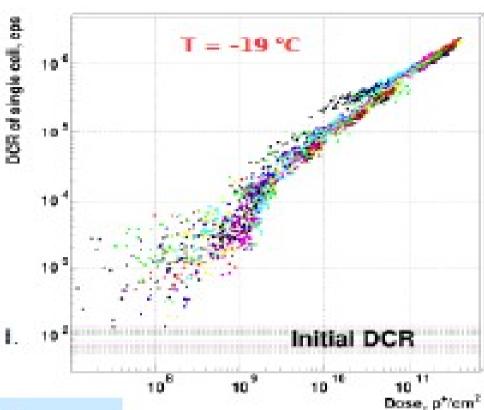
- Increase of VB: ~0.5 V
- Drop of the amplitude (~2 times)
- Reduction of PDE (from 10% to 7.5 %)
- Increase of the current (up to ~1mA at dVB=1.5 V
- ENC(50 ns gate, dVB=1.5V)~80 e, rms
 The main result is that SiPM survived this dose of irradiation and can be used as photon detector!

(A.Heering et al., NIM A824 (2016) 111)

d-SiPM irradiated

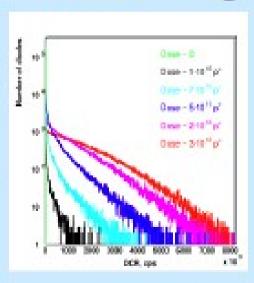
(M.Barnyakov et al., Elba-2015)

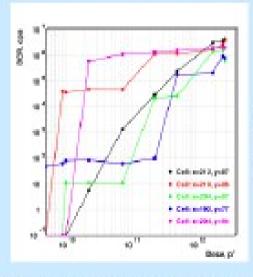




Dark counting rate vs. total dose

DCR spectra after different total doses accumulation

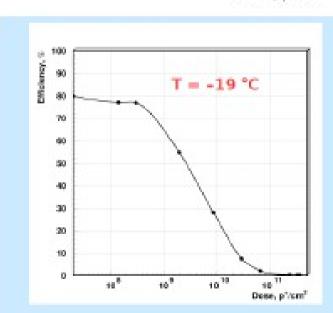




single cells from one subpixel as a function of total dose

DCR of

With the dose accumulation the number of noisy cells increases rather than DCR of each cell. → Cell damage caused by single interaction of p* with Si lattice.



Optimal efficiency of <u>single photons</u> detection as a function of proton fluence.

Applications

- low light intensity
- fast timing
- large area (low T)

ארטר IONIDI אויירי

-rays

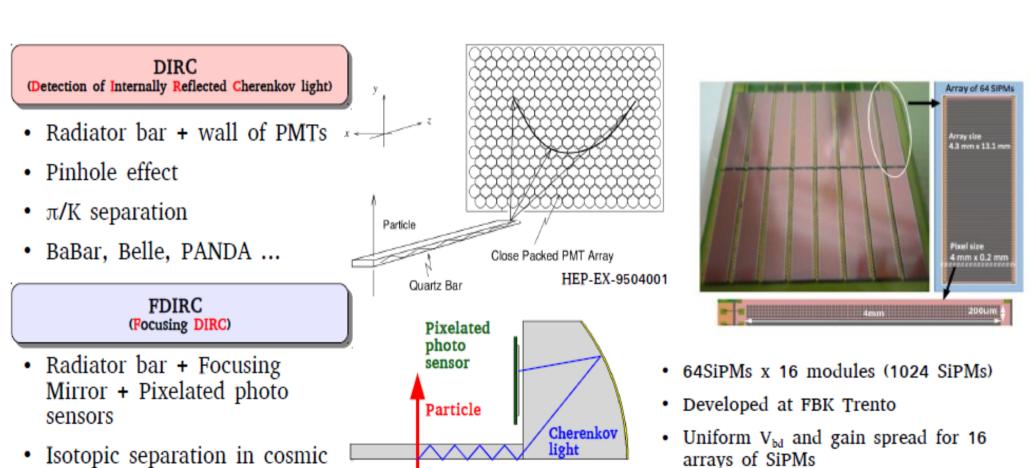
Low Light Intensity - Cherenkov

A Digital FDIRC Prototype for Isotopic Identification in Astroparticle Physics

JE Suh, PS Marrocchesi et al RICH 2016

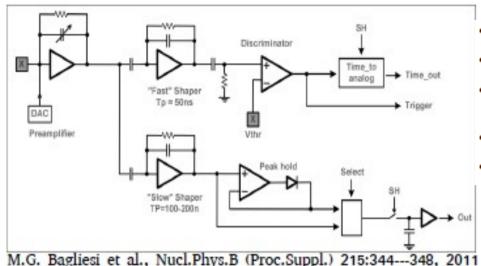
DCR (<100kHz/mm²)

PDE ~ 35% @ 420nm, (OV 4.4V)

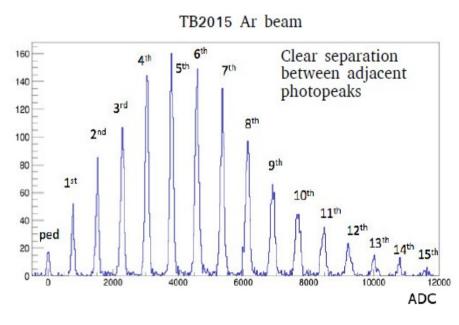


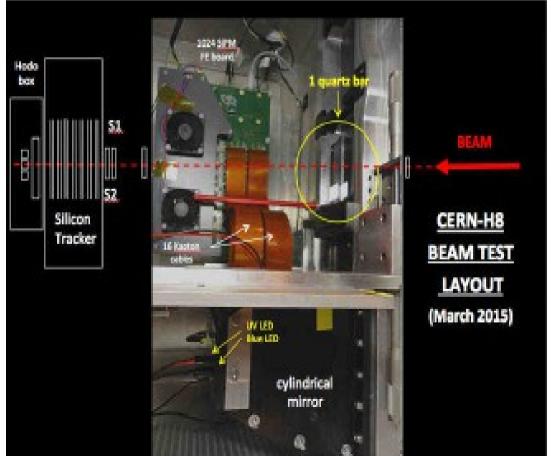
Low Light Intensity - Digital FDIRC

JE Suh, PS Marrocchesi et al RICH 2016



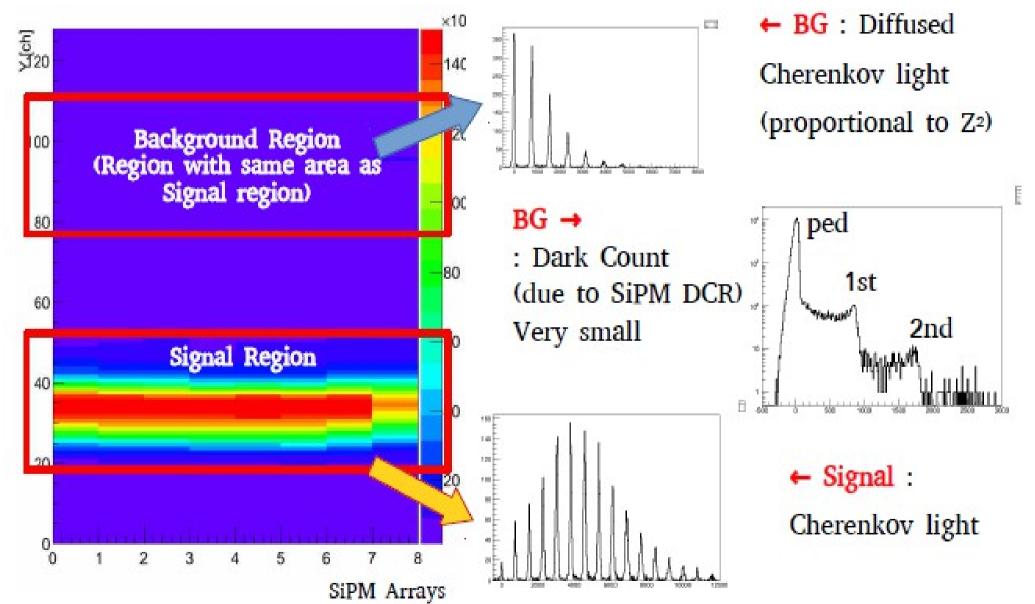
- 16 custom ASIC VaTa64HDR16 chips for each array of SiPMs
- 16 bits ADC
- 16 chips x 64 channels supply 1024 digitized signals with 1024 time stamps
- · Low power and large dynamic range
- Auto-trigger + 2 external triggers





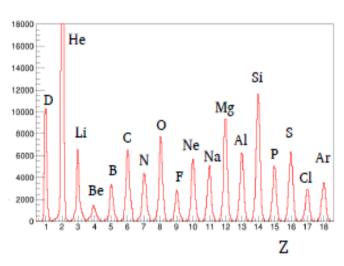
Low Light Intensity - Digital FDIRC

JE Suh, PS Marrocchesi et al RICH 2016

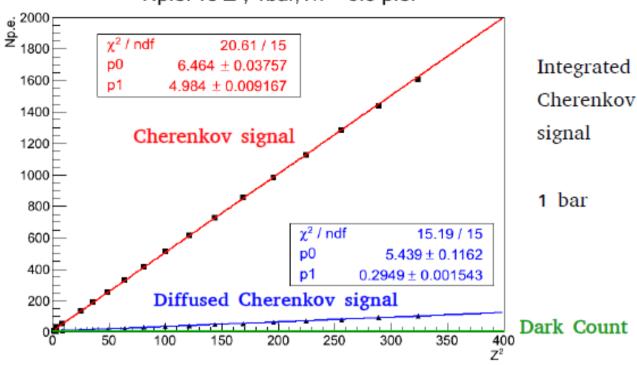


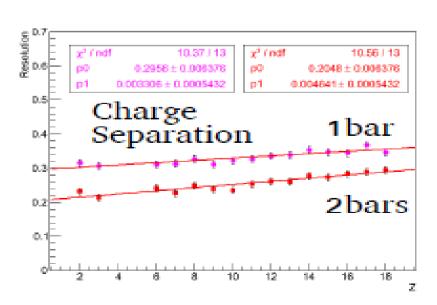
JE Suh, PS Marrocchesi et al RICH 2016

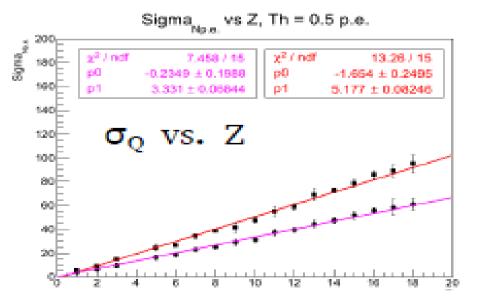
Np.e. vs Z^2 , 1bar, Th = 0.5 p.e.



charge tagging

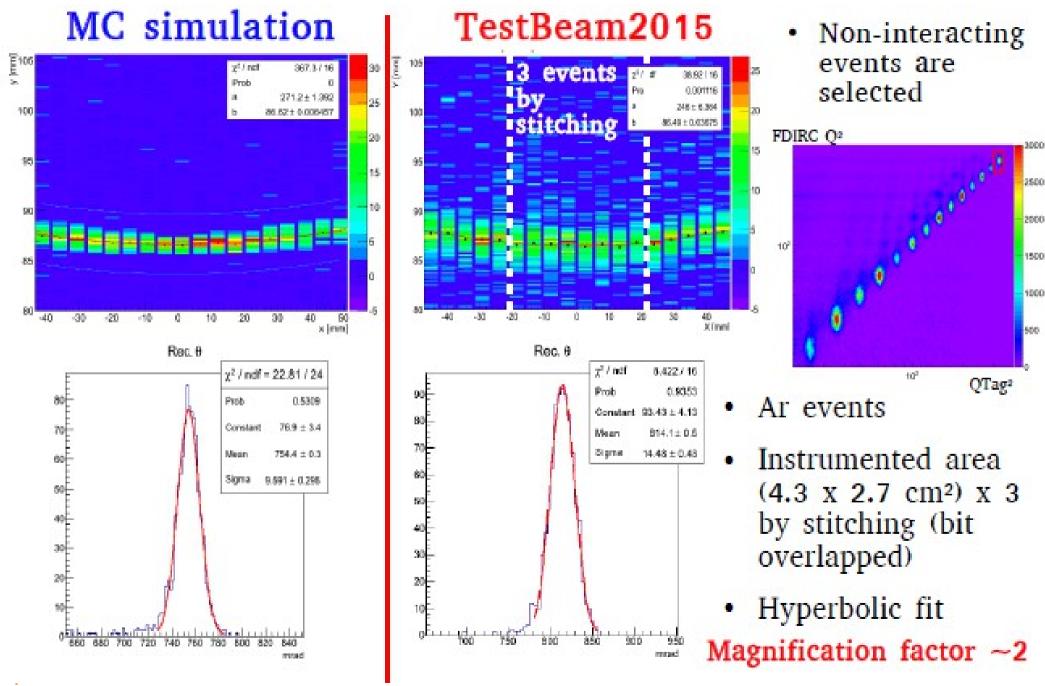






Low Light Intensity - Digital FDIRC

JE Suh, PS Marrocchesi et al RICH 2016

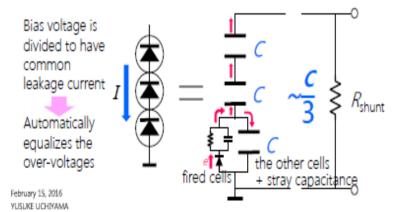


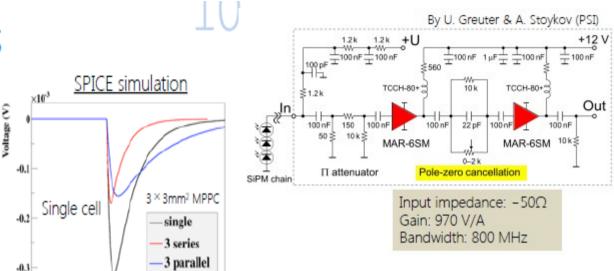
Timing applications: fast counter MEG-II

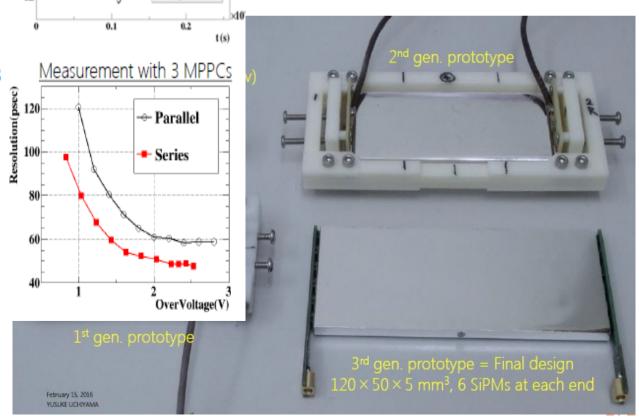
Uchiyama et al VCI 2016

Series connection of SiPMs

- SiPMs have high capacitance
 - Terminal capacitance -300 pF for 3 × 3 mm² SiPM
- This forms a slow RC time-constant with amplifier input impedance
 - $3 \times 3 \text{ mm}^2 : 300 \text{pF} \times 50 \Omega = 15 \text{ ns}$
 - $3 \times 9 \text{ mm}^2 : 900 \text{pF} \times 50 \Omega = 45 \text{ ns} !!$
 - → One of limitations for large area SiPMs or array of SiPMs with parallel connection
- This large capacitance works as capacitive coupling when connected in series

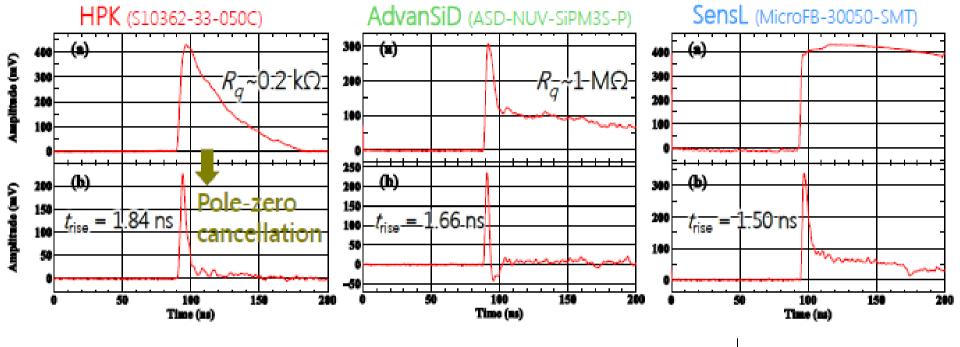






Timing applications: fast counter MEG-II

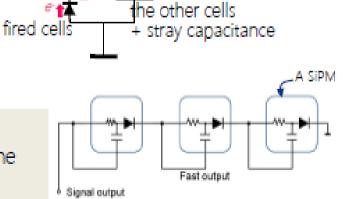
Uchiyama et al VCI 2016



- Pulse shape depends on $R_q \& C_q$
 - \square C_{σ} is important for the fast signal
 - \square Slow tail by larger R_q can be omitted with pole-zero cancellation

 SensL's fast output terminal can be used to make very fast rise time

A method to combine the fast output into the normal signal line (by N. Pavlov)



February 15, 2016 YUSUKE UCHIYAMA

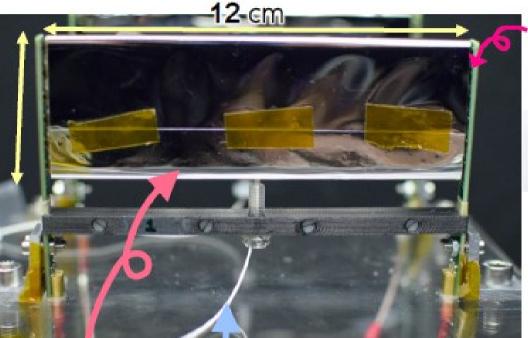
cm or 5

9

Timing applications: fast counter MEG-II

Uchiyama et al VCI 2016

Final Design



6 SiPMs in series at the both ends AdvanSiD 3x3 mm², 50x50 μm² pixels V_{breakdown} ~ 24 V (145 V for 6 series)



Fast Plastic Scintillator BC422

wrapped in ESR2 film

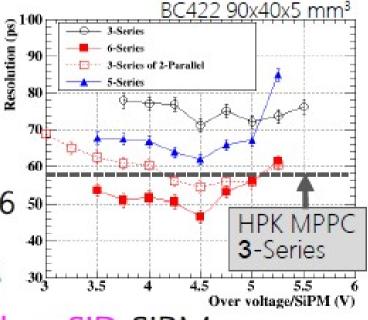
Optical Fiber
Inter-counter time
calibration with laser

Resolution

HPK \times 3 > AdvanSiD \times 6 50

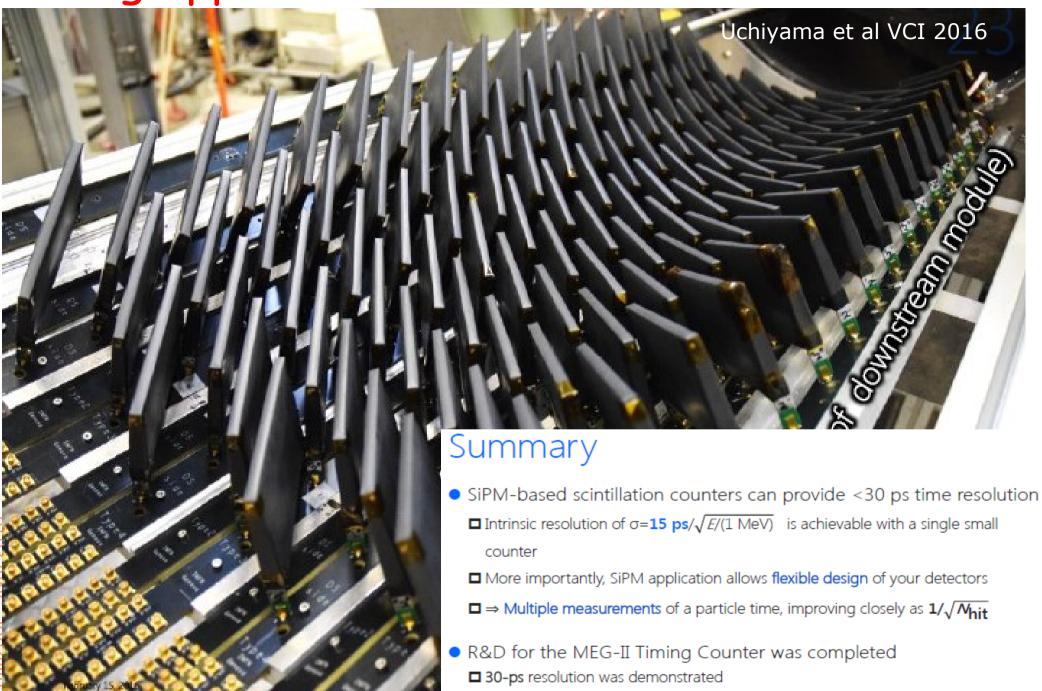
Cost

 $HPK \times 3 \approx AdvanSiD \times 6$



February 15, 2016 YUSUKE UCHIYAMA We decided to employ AdvanSiD SiPM

Timing applications: fast counter MEG-II



□ Tested first ¼ detector in 2015

Large Area

Low temperature large volume cryogenic experiments based on noble liquid scintillation

→ are adopting SiPM

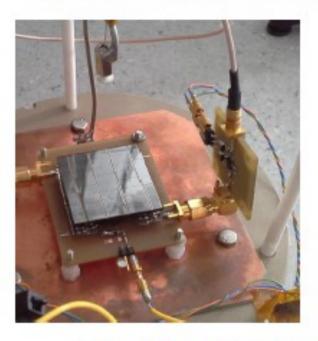
Single photon spectrum with a 10cm^2 tile at 80K

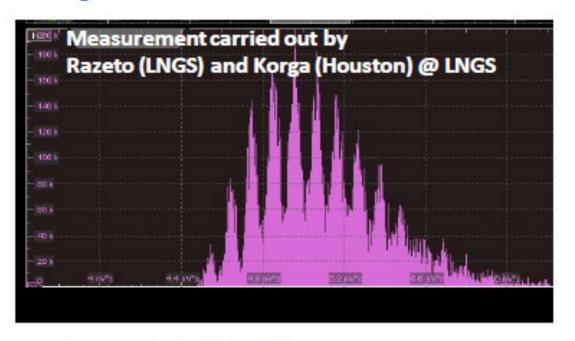
Test for DarkSide experiment (LAr)

LAr scintillation (128nm) light WLShifted to 400nm readout on 15m² of surface (with SPE sensitivity)

All channels connected together on one front-end. Tile illuminated with laser. Integration time = 6us.

A.Gola et al IEEE NSS 2015





Single-photon spectrum visible!!

- low noise
- very uniform behavior of the SiPMs!!

2016

Large Area readout electronics - eg for nEXO

- LXe scintillation (178nm) light readout on 4 m² of surface (with SPE sensitivity and extremely low background)
- Done by using photosensitive readout made of SiPM (dictated by radiopurity issues)
 - Low bias voltage
 - High charge amplification
 - SPE-Capable
 - High capacitance
 - High dark count rate (DCR)

Preliminary analysis by

L.Fabris, G.De Geronimo, S.Li,

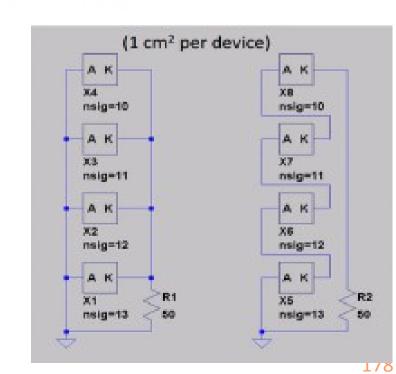
V.Radeka, and G.Visser, L.Yang

Parallel connection:

- -Requires fewer number of external parts, but
- -Capacitance increases by N: potential for more electronics noise and lower SNR

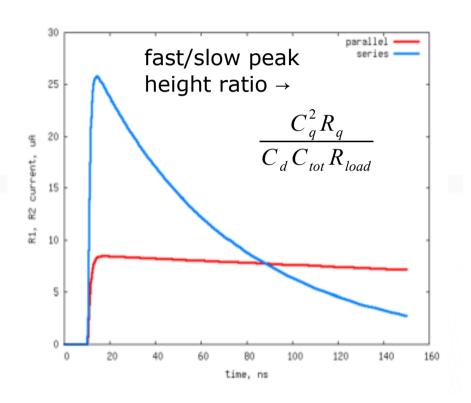
Series connection

- -Requires higher bias voltage
- -Reduces capacitance by 1/N: potential for less electronics noise and better SNR



Large Area readout electronics - eg for nEXO

For finite input Z in the preamp, the current signal is LOWER in the parallel connection



Preliminary analysis by L.Fabris, G.De Geronimo, S.Li, V.Radeka, and G.Visser, L.Yang

From deterministic point of view (no noise) the parallel connection for small input Z (up to ohm) supplies the most charge, while the series is better at larger Z (tens of ohm) ... Limit is the total DCR

But readouts have noise (depends on C)
Large areas → low signal (photon-starved)
Reading high-C detector w/ small C
front-end → high noise situation

Can only cluster together a limited area

Large Area readout electronics - eg for nEXO

Preliminary analysis by L.Fabris, S.Li, G.De Geronimo, V.Radeka, and G.Visser, L.Yang

Example

(thanks to: V. Radeka, G. De Geronimo, S. Li; BNL)

- Energy threshold 1 photo-electron (Q_{min} = ~100 fC)
- ENC_{goal} < ~Q_{min}/10
- If we set a global limit for power, the ENC for a given C_D is:

$$enc_A^2 = \frac{e_n^2 C_D^2}{t_p}$$

If we have N devices (total area A_{total} = N x A):

$$ENC_{total}^2 = ENC_1^2 + ENC_2^2 + \dots = N \cdot ENC^2 = \frac{A_{total}}{A}ENC^2$$

We want ENC_{total}<ENC_{goal}, hence:

$$A_{total} = \frac{ENC_{goal}^2}{enc_A^2} = \frac{Q_{min}t_p}{10e_n^2C_D^2}$$

... to be continued

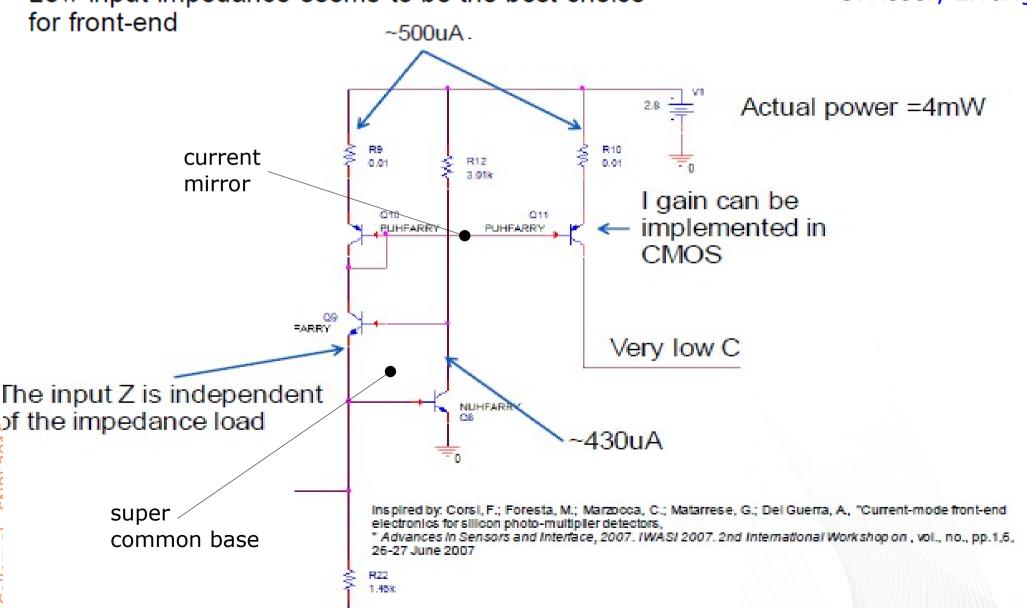
Considering both the ENC limitation and a dynamic range of 10pe/cm2

→ maximum clustering for a total SiPM area ~10cm²

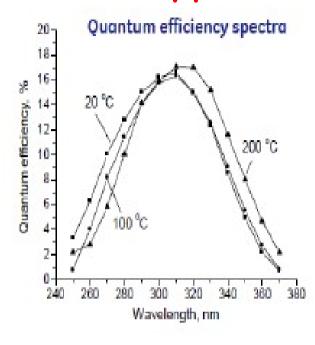
Large Area readout electronics - eg for nEXO

Preliminary analysis by L.Fabris, S.Li,
G.De Geronimo, V.Radeka, and
choice G.Visser, L.Yang

• Low-input impedance seems to be the best choice



Other types of SiPM - SiC



Advantage of SiC: it has larger bandgap than Si (3.26eV)

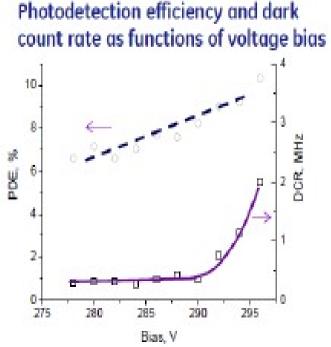
- → Lower leakeage current
- → higher operating T
- → Higher sensitivity to UV

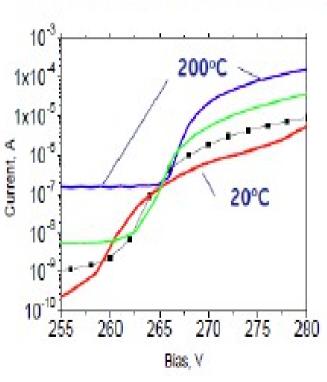
Packaged SiC SSPM



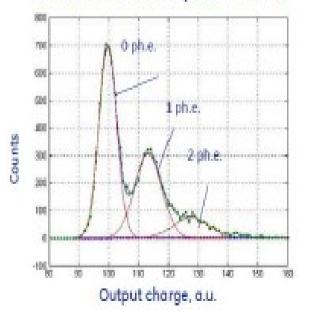
Active area: 4x4 mm²
Pixel size: 60 um
16 sub arrays
Area of sub-array:
1x1 mm²

Dark current vs. temperature



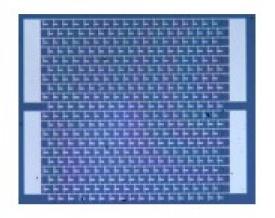


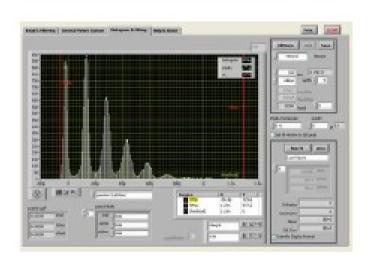
Single Photoelectron spectrum recorded for SiC-PM with 256 pixels (1 mm²)

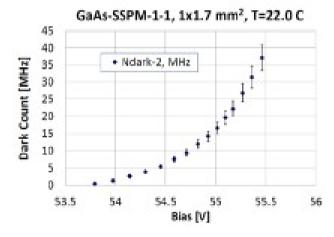


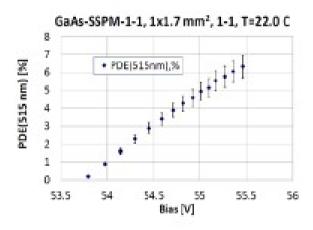
Other types of SiPM - GaAs

LightSpin Photomultiplier Chip™

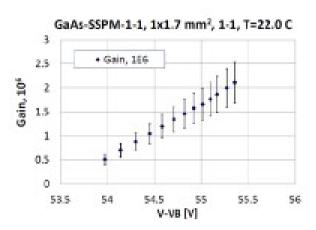


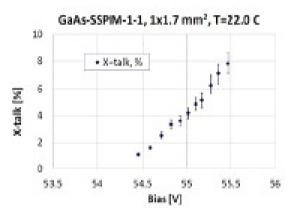


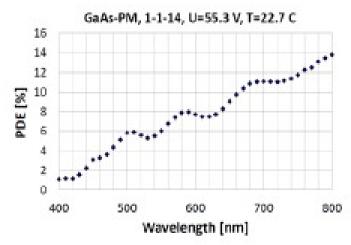




Wide bandgap (1.42 eV): potentially can be more radiation hard than silicon. Timing with GaAs SSPM can be also better (high mobility of electrons and holes, fast avalanche development – direct semiconductor)

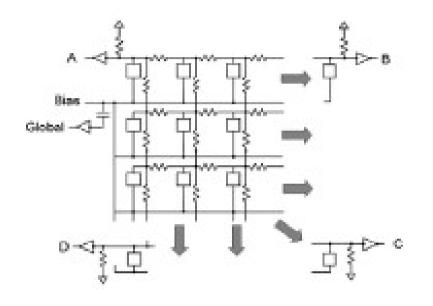






Other types of SiPM - Position sensitive - RMD

RMD designed a $5x5 \text{ mm}^2$ position-sensitive solid-state photomultiplier (PS-SiPM) using a CMOS w/ imaging capability on the micro-pixel level \rightarrow 11,664 micro-pixels (pitch of 44.3 mm)

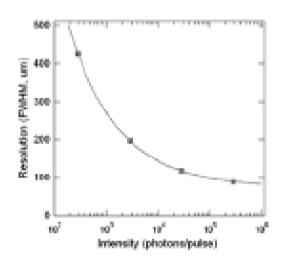


Resistive Network

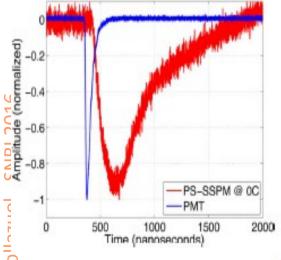
→ Anger logic

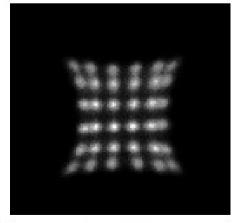
$$x=\frac{(A+B)-(C+D)}{A+B+C+D}$$

$$y=\frac{(A+D)-(C+B)}{A+B+C+D}$$



A plot of the X–Y spatial resolution (FWHM) as a function of the incident beam spot light intensity. Spot size was ~30 micron





High capacitance and high resistance create a RC low-pass filter that results in undesirable signal properties that include: low amplitude, slow rise time, noisy signals, and a pulse shape that varies with position across the device

An image of a 66 LYSO array having 0.5 mm pixels uniformly irradiated with 22Na.

Mc Clish et al IEEE Trans Nucl Sci 61(3) 2014 1074-1083

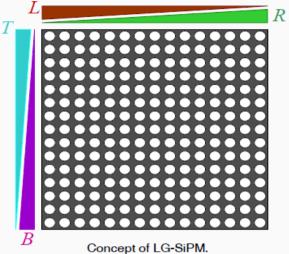
Other types of SiPM - Position sensitive - FBK

- Current of the 4 cathode signals changes linearly with position of the fired microcell
- Position

$$x = \frac{L - R}{L + R} \qquad y = \frac{T - B}{T + B}$$

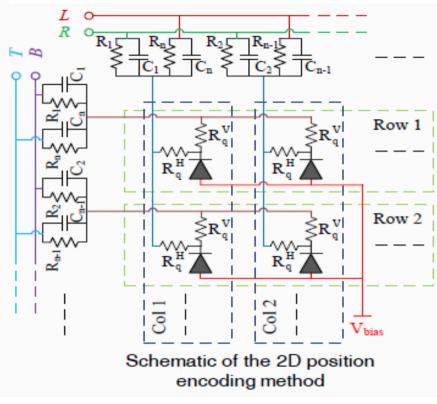
Energy

$$E = L + R + T + B$$



- Each microcell is connected to two identical quenching resistors that split the signal into equal parts, one to encode x the other to encode y.
- By changing the conductance of the two resistors of each current divider, the difference between the currents flowing in L and R (or T and B) changes linearly according to the column (or row) index.

linearly graded



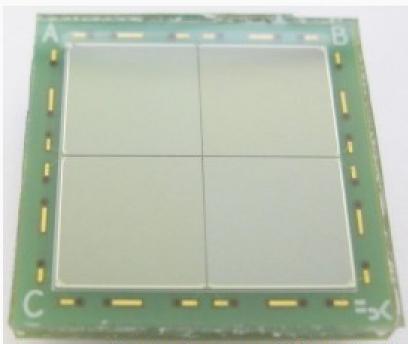
- The parallel conductance of the two resistors is kept constant for each column (row).
- The capacitors preserve the fast rising edge of the microcell signal.

Other types of SiPM - Position sensitive - FBK

- Advantage of PS-SiPM
 - Resolution down to the microcell level.
 - Only 5 output signals (4 for position information and 1 for timing information).
- Tiled multiple LG-SiPMs to form a larger area
 - 2 x 2 array of 7.75 x 7.75 mm² LG-SiPMs, total size of 15.55 x 15.55 mm².
 - Active area of each LG-SiPM: 7.6 x 7.6 mm².
 - RGB-HD Microcell pitch: 20 μm.
 - PDE: 30% at 35.0 V

~0.15 mm gap between LG-SiPMs active area.

C. Piemonte, S.R. Cherry et al IEEE MIC 2015



● L1 R1 ● L2 R2 ●
T1 T2 ●

1 2

■ B1 A1 B2 ●

■ T3 A3 ■ A4 ■

■ B3 ■ L3 R3 ● L4 R4 ●

(Left) photograph (right) schematic of the LG-SiPM arrays.

Other types of SiPM - Position sensitive - FBK

- LYSO array
 30 x 30 array LYSO
 0.445 x 0.445 x 20 mm³
 Polished, Toray reflector
 0.5 mm pitch
 #microcells below each needle (625)
 Packing fraction
 Crystal to SiPM size: 91 %
- Flood Histogram

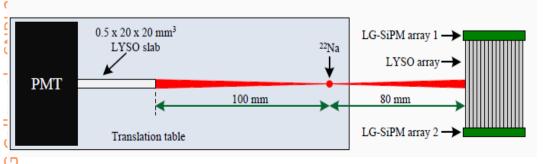
 -10 °C

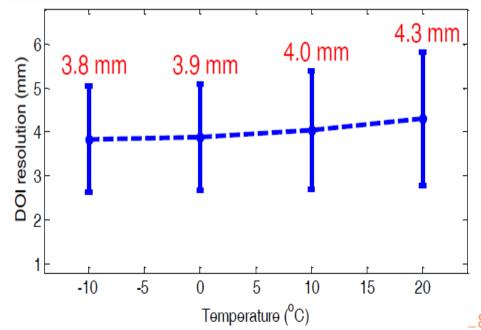
 0 °C

 Dias voltage: 35.0 V
 250-650 keV energy window was applied to each crystal to select events

C. Piemonte, S.R. Cherry et al IEEE MIC 2015

- DOI resolution measured at an bias voltage of 35.0 V.
- Reference detector consists of a Hamamatsu PMT R12844-10 and a 0.5 x 20 x 20 mm³ LYSO slab.
- DOI data was obtained at five depths
 - 2 mm, 6 mm, 10 mm, 14 mm and 18 mm.





Other types of SiPM - Position sensitive - FBK

Linearly-Graded SiPM (LG-SiPM) arrays are an attractive option for high resolution PET:

- LYSO crystals with pitch size of 0.5 mm were resolved using a multiplexed readout method.
- It may be possible to resolve smaller crystals.
- Energy resolution: ~ 22%.
- DOI resolution: 3.8 mm @ –10 °C and 4.3 mm @ 20 °C.
- Global timing resolution: ~ 930 ps.

C. Piemonte, S.R. Cherry et al IEEE MIC 2015

Future Work:

- Develop a four-side tileable detector module.
- NUV-HD version of the LG-SiPM

Thanks for your attention

... and thanks for lots of interesting discussions and courtesy material to C.Piemonte, A.Gola, G.Paternoster, F.Acerbi, T.Frach, Y.Musienko, V.Puill, N.Dinu, A.Nagaii, R.Mirzoyan, A.Para and many more colleagues

Additional material →