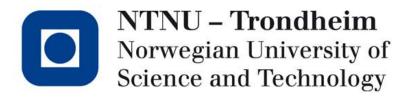
COLA: Computing on Low-Power Architectures, Ferrara, Italy, 25-26 February, 2016



Climbing Mont Blanc – A Prototype System for Training in Energy Efficient Programming

Lasse Natvig,
Dept. of Computer and Information Science
Norwegian University of Science and Technology



Outline

- The inspiration
- What is Climbing Mont Blanc (CMB)?
- Early experience
- Future work





The inspiration

- Using Exynos SoC from Samsung
 - ARM big.LITTLE + Mali GPU
 - Sold in large numbers /
 - Samsung Galaxy mobile phones ++
 - Energy efficiency
 - Very challenging programming

GPU family started by NTNU-students → Falanx → ARM Media Processing Division



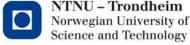
MONT-BLANC

European scalable and power efficient HPC platform based on low-power embedded technology

> Alex Ramirez Barcelona Supercomputing Center **Technical Coordinator**

Now: Filippo

750 million Mali GPUs sold in 2015!



MONT-BLANC

26 TFLOPS – 18KW

Prototype

- Exynos 5 based (32.3GFLOPS CPU+GPU)
- One blade: 15 compute cards (30 Cortex A15 + 15 Mali-T604)
- One chassis: 9 blades (270 Cortex A15 + 135 Mali-T604)
- Prototype: 6 chassis (1620 CPUs + 810 GPUs)



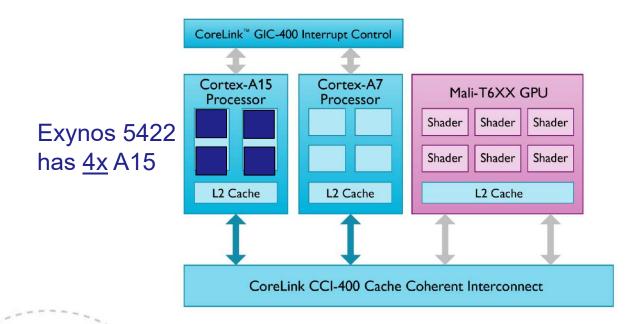


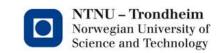




The idea

- 1. The need for energy-efficiency (Mont Blanc project)
- 2. Difficult programming
 - Exynos 5422 is "3-way heterogeneous" with 14 cores
 - − → need for training
- 3. How to get programmers? ...



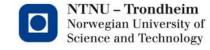


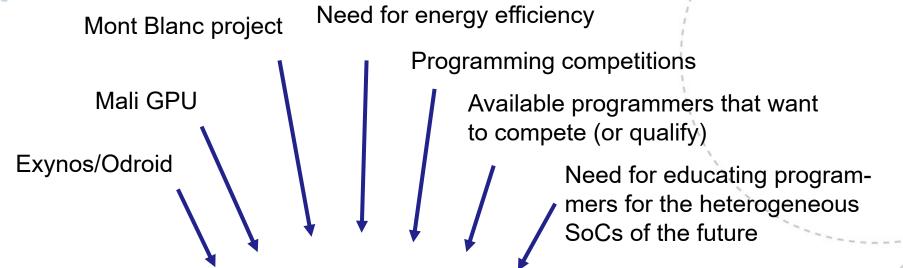
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The force (Background)

- UVA Online (Spain) (16 million submissions)
- PKU (Peking University) (14 million)
- KATTIS (KTH, Sweden)
- Jutge.org
- TopCoder
- Sphere Online Judge
- HackerRank
- CodeChef
- LeetCode
- Timus Online Judge
- A2 Online Judge
- URI Online Judge



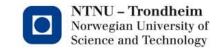




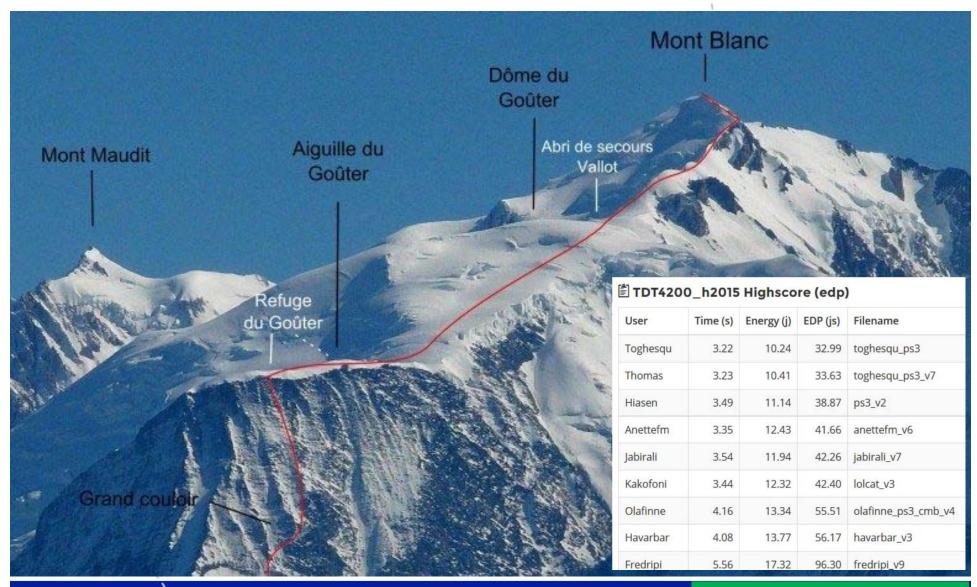




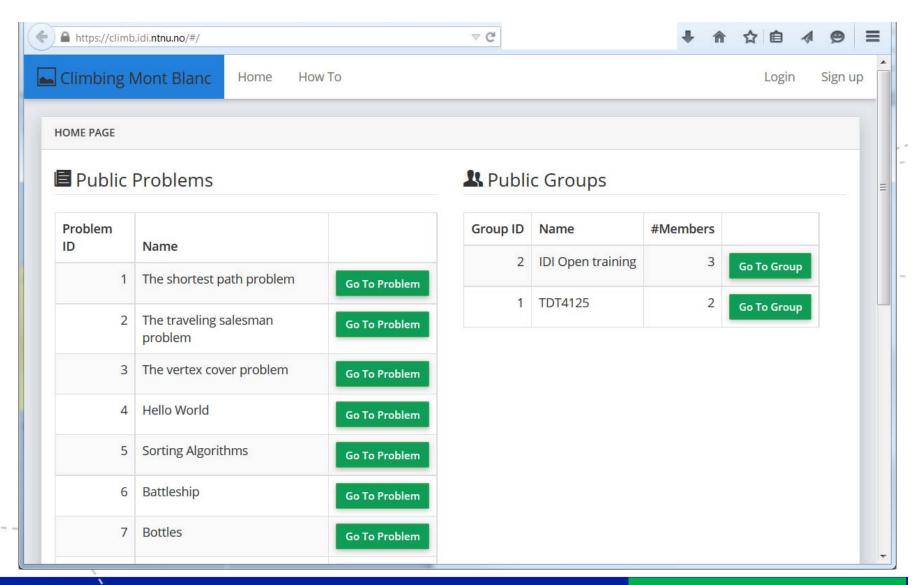
CMB SHORT INTRO



CMB - the name



CMB select group or problem



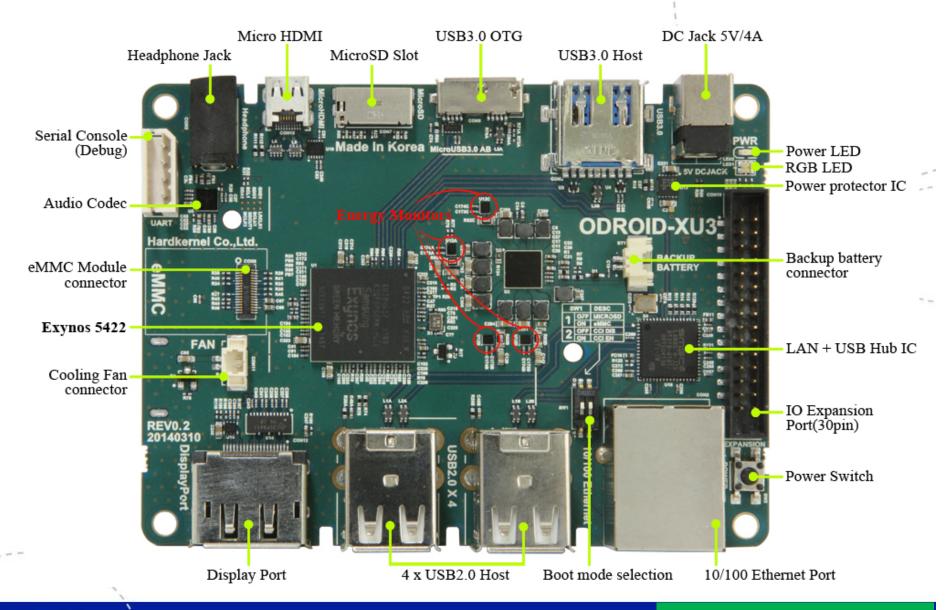
Odroid XU-3

- Odroid board from hardkernel.com
 - Full details at http://www.hardkernel.com/main/products/prdt_info.php?g_code=G140448267127
- Samsung Exynos 5422
 - Cortex[™]- A15 2.0Ghz quad core
 & Cortex[™]-A7 quad core CPUs
 - Mali-T628 MP6 533 MHz
 - ubuntu or android





Odroid XU-3



CMB technical overview

Currently

- C, C++, OpenCL
- Pthreads, OpenMP 4.0

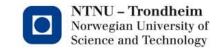
Coming (?)

- OmpSs
- Java, python, Haskell?
- MPI
- More info
 - Workshop paper at arXiv:1511.02240

programmers climb.idi.ntnu.no Frontend User interaction Server Compile solutions Control backend Authenticate Users Database **Backend** XU3

- Compile & run program
- Measure time & energy

EARLY EXPERIENCE



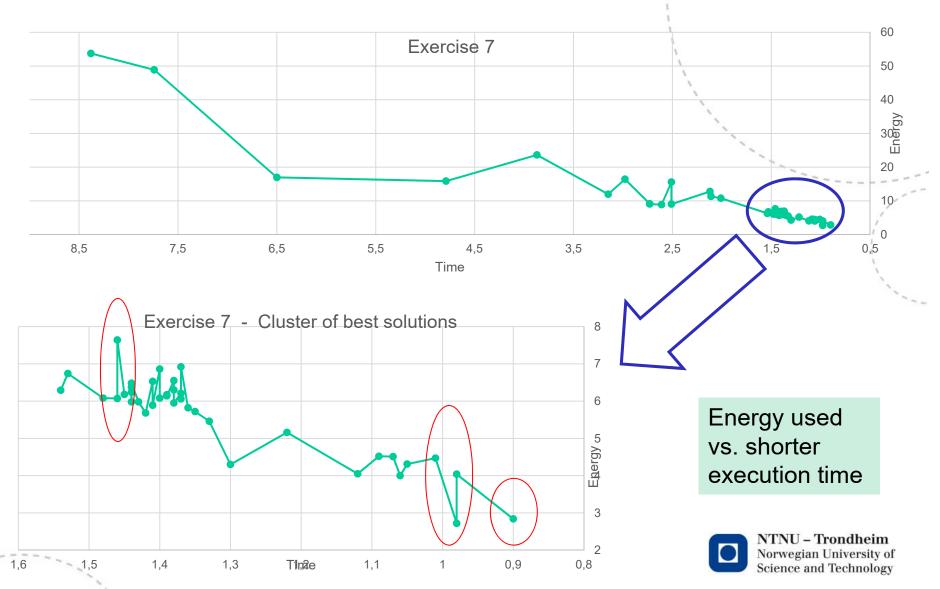
CMB - VERY early experience

- 5 programming exercises in a course on parallel computing
 - Autumn 2015, approx. 65 students
 - CMB as one of three experimental platforms
 - Students also used
 - desktops w/NVIDIA-GPU
 - Supercomputer Vilje, 22000+ cores





Submitted solutions to Exercise 7



Future work

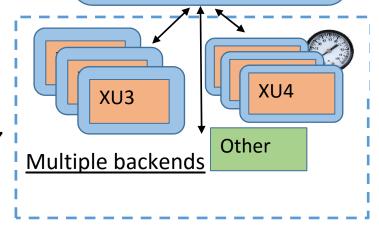
- Spring 2016
 - Optional use in C++ course with 800+ students
 - Improved functionality & capacity
 - Dispatcher/Broker
 - DSE mode
 - More languages
- Sabbatical autumn 2016
 - Developing more problems
 - Parallel programming
- More tests in C++-course spring 2017
- More platforms
 - Newer Odroid-boards? / Exynos?
 - Kirin from Hi-Silicon (Huawei)?

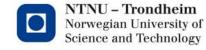
<u>Server</u>

- Compile solutions
- Control backend
- Authenticate Users
- Database
- Queueing

Broker

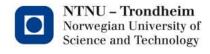
- Load distribution
- DSE mode





Potential models for collaboration

- Application cases/kernels → define problem (now, easy)
 - Precise problem specification
 - Small data set (input, correct output)
 - "Big" data set (Correct output not visible, to avoid cheating)
 - Checker.cpp
 - Checking byte by byte is often not what you want
 - Floating-point operations, approximation problems
 - Optional user-defined "goodness"-parameter
- System development
 - GUI?, statistics?, ...
- Best practice, build experience, textbook? (long term)
- "Stay tuned" at https://www.ntnu.edu/idi/card/cmb



QUESTIONS?

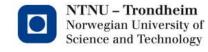
Read more:

arXiv:1511.02240

Vacant position: 30 months (+?) at ARM Norway, 60% at NTNU https://www.hipeac.net/jobs/9025/teaching-and-research-position-at-the-arm-card-lab/

- Systems SW with energy-efficiency
- HW/SW interface, GPU
- Post.doc level
- Application deadline 31 March

Contact: Lasse.Natvig@idi.ntnu.no



Demo ...

