

FTK schedule

Alberto Annovi

FTK HW production status

Red: expected dates, blue: actual dates

	PRR	# to be produced*	Prod. Started	1 st brd @ CERN	All prd. delivered	Prod. tested	Prod. @ cern	Installed
IM S6	✓ `14	80/80	✓ Jan	Apr 20	Mar 1	100%	Oct 15	
IM A7	✓ Apr	80/80	✓ Sep		10/80			
DF	✓ Jan	36/36	✓ Mar	Jul 9	Aug 31	100%	Oct 8 32/36	
AUX	✓ Feb	20/140 *	✓ Mar*	Aug 7	Nov 22			
AM06	✓ Aug	1k/8k+sp. *	✓ Jul*	N/A	Nov 9	Feb 26		
LAMB		80/560 *	*					
AMB		20/140 *	*					
SSB	✓ Aug	10/37 *	*					
FLIC	N/A	3/3	✓ May		Oct 10	Oct 25	Nov 15	

* Production will happen in steps: barrel only $\mu=40$,
full coverage $\mu=40$, more processing power for $\mu>40$

FTK schedule

- **Steps A and B use a combination of production & prototypes boards**
- **3rd step all boards from production except AM board**
- **Blue=done, * in progress green = value when step completed**

Success
oriented

	IM	DF	AUX	AMB	AM ver.	SSB	FLIC	Milestones	Expected
A	4→16	1→4	1*	1	05	1	1*	Included in TDAQ	12/2015
B	128	32	16	1	05	1	2	Included in TDAQ	12/2015
3rd	128	32	16	1	06	1	2	Included in TDAQ	02/2016
4th	128	32	16	16	06	8	2	Full barrel (mu=40)	05/2016
5th	128	32	32	32	06	8	2	Full detector (mu=40)	10/2016
Final	128	32	128	128	06	32	2	TDR Specs	2018 / Lumi driven

Plan for the rest of 2015 is to:

- **Commission a full IM-DF-AUX-AMB-SSB-FLIC processing slice**
- **Commission full Input and Output (all IMs, DFs and FLICs)**

FTK schedule

- **Steps A and B use a combination of production & prototypes boards**
- **3rd step all boards from production except AM board**
- **Blue=done, * in progress green = value when step completed**

Success
oriented

	IM	DF	AUX	AMB	AM ver.	SSB	FLIC	Milestones	Expected
A	4→16	1→4	1*	1	05	1	1*	Included in TDAQ	12/2015
B	128	32	16	1	05	1	2	Included in TDAQ	12/2015
3rd	128	32						Included in Q	02/2016
4th	128	32						Arrel (40)	05/2016
5th	128	32						ector (40)	10/2016
Final	128	32						FTK Specs	2018 / Lumi driven

Reminder: we are now fully FW/SW
integration limited !

Plan for the rest of 2015 is to:

- **Commission a full IM-DF-AUX-AMB-SSB-FLIC processing slice**
- **Commission full Input and Output (all IMs, DFs and FLICs)**

AM board

- AMB v4 submitted
- Currently debugging
 - HOLD propagation AMB->AUX
 - Stable data flow in continuous mode
 - Integration in lab4 on going