8th International Workshop on Semiconductor Pixel Detectors for Particles and Imaging.



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Operational Experience with the ATLAS Pixel detector

Run-2 of the LHC is providing new challenges to track and vertex reconstruction with higher energies, denser jets and higher rates. Therefore the ATLAS experiment has constructed the first 4-layer Pixel detector in HEP, installing a new Pixel layer, also called Insertable B-Layer (IBL). In addition the Pixel detector was refurbished with a new service quarter panel to recover about 3% of defective modules lost during run-1 and a new optical readout system to readout the data at higher speed while reducing the occupancy when running with increased luminosity.

The commissioning and performance of the 4-layer Pixel Detector will be presented.

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