## ATLAS Pixel Detector Design For The HL-LHC

Ben Smart on behalf of the ATLAS Collaboration PIXEL 2016, 5-9 Sept.









• To improve our measurements and searches, the LHC and ATLAS will be upgraded:



 The LHC will become the High-Luminosity-LHC, to produce 3000 fb<sup>-1</sup> of integrated luminosity in 2035.
 Higher Energy → benefits searches for new particles. Higher integrated luminosity → benefits precision measurements and studies of rare processes. • For this talk, I will focus on ATLAS Inner Detector (Silicon pixel) upgrades.



### 2012 ATLAS event, with 25 reconstructed vertices (µ=25)



• At the HL-LHC (upgrade phase 2) we expect  $\mu$ =200 within a similar space.

- Higher track density requires a better Inner Tracker (improved resolution, efficiency, etc.) to maintain tracking and b-tagging performances.
- Existing components will be heavily radiation-damaged by LS3.

### Simulated HL-LHC event, with 200 reconstructed vertices (µ=200)



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- New Inner Detector will be called the 'Inner Tracker' (ITk).
- All sensors in the ITk will be Silicon-based.



- We will also increase η coverage to 3.2 or 4.0 (|η|<2.5 in Run-2) to increase acceptance and reconstruct forward jets for Vector-Boson-Fusion and Vector-Boson-Scattering.
- ITk layout and design are not yet finalised: Four possible pixel barrel layouts are being considered.

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- Pixel end-caps will consist of rings in four layers, (at different radii).
- Each ring consists of a carbon core containing cooling and electrical services. Each ring is built out of two half-rings, for ease of construction.
- Modules are mounted on both sides of each ring to allow overlap.





Titanium CO2 cooling pipe embedded in ring.

Electrical services (flex) embedded in ring.

- Innermost pixel end-cap ring layer needs to be more robust due to higher radiation dosage.
- Number of rings per layer is still under optimisation.

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- 'Traditional' design: all pixel barrel sensors are parallel to beam-line.
- However, with extended layouts, high-η particles will pass through pixel sensors at shallow angles, creating elongated clusters.



- The aim is to then use cluster length to determine incident angle of incident particle.
- In this way, a single cluster becomes a 4D measurement.
- This information can then be fed into track fitting.

- Extended layouts involve joining pairs of pixel layers with support structure, for increased stiffness: Each stave would hold modules for two layers.
- Also looking at carbon pipes for CO<sub>2</sub> cooling pipes, to reduce mass and increase radiation length of the staves.
  (Default CO<sub>2</sub> cooling tube material is Titanium.)









• Inclined layouts aim to have sensors more perpendicular to incoming particles, thus reducing material and cluster size, and increasing number of hits (multiple per layer).



Non-inclined layouts have single transition region between parallel-to-beamline and perpendicular-to-beamline sensor regions.

Inclined layouts aim to improve on this by inclining sensors towards interaction point in this transition region.



- Inclined layouts have multiple options for inclined sensor support.
- Layers to be joined in pairs: each stave would support two layers of pixel sensors, leading to increased stiffness.



- ITk has a significant reduction in material (and so X/X<sub>0</sub>) compared to current Run-2 Inner Detector.
- Largest source of X/X<sub>0</sub> in current ITk layouts comes from cables and cooling pipes for pixel end-caps, (not in the pixel end-cap rings themselves, but in the cables and cooling pipes connecting the end-cap layers to the outer boundaries of the ITk volume).



- Pixel sensors must be kept sufficiently cool to avoid thermal runaway.
- Particularly challenging for inclined designs.
- Lowering CO<sub>2</sub> temperature becomes more costly at lower temperatures.



 Pixel staves and rings have been tested, with heaters in place of pixel sensors, to ensure that they allow for efficient cooling, with cooling efficiency quantified by a 'Thermal Figure of Merit' (TFM):



Conductive TFM vs. Radii, T evap=-30 C

 $\text{TFM} = \frac{\Delta T}{P_{\text{heater}}} A_{\text{heater}}$ 

 $[^{\circ}C \ cm^2 / W]$ 

- ITk pixel detector will require 5 Gbps data read-out rate.
- Can currently achieve 6.25 Gbps.
- Choice of technology: Twin-axial, Flex, and Twisted-pair.
- Twin-axial and Twisted-pair are simpler to produce.
- Flex contains less material (so has less X/X<sub>0</sub>).



See poster by Andre Rummler





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Conclusion



# Backup

• These upgrades will improve our measurements and searches:



#### Run 1

#### Run 3 and HL-LHC



with current theory uncertainties.

Vertical blue lines are Run 1 values.

## μ=200



 Outside a radius of 362mm, the ITk will contain a Silicon strip detector: 4 barrel layers,

12 end-cap disks (6 per end), each disk built up of 9 'petals'.



(Strips end-cap petal components)

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• Measured temperature difference in TFOM is not only due to heater power, (blackbody radiation also present, for example), so modified TFOM definition has been proposed to account for 'parasitic' heat sources:  $\Delta T$ 

$$\Gamma FOM = \frac{\Delta T}{P_{\text{heater}} + P_{\text{parasitic}}} A_{\text{heater}} \qquad \text{[°C cm²/W]}$$

- Operational Experience with the ATLAS Pixel detector
- The ATLAS Insertable B-Layer: from construction to operation
- Physics performance of the ATLAS Pixel Detector
- ATLAS track reconstruction in dense environments and its inefficiency measurements using pixel DE/DX
- Primary vertex reconstruction with the ATLAS detector
- Toward HL-LHC: strategies for DAQ and track triggers
- 4D fast tracking for experiments at HL-LHC
- From LHC to HL-LHC...challenges and routes to solutions
- CMS Pixel Detector design for HL-LHC
- 3D sensors for the HL-LHC
- Optimization of thin n-in-p planar pixel modules for the ATLAS upgrade at HL-LHC
- · Test beam results of 3D detectors constructed with single-crystal and poly-crystalline diamond
- Radiation damage caused by neutron capture in boron doped silicon pixel sensors
- Application of a Transient-Current-Technique based on a Two- Photon-Absorption process to the characterization of a HV-CMOS deep n-well
- Development of CdTe Pixel Detectors Combined with an Aluminum Schottky Diode Sensor and Photon-Counting ASICs
- Overview and perspectives of HR CMOS
- Status of HVCMOS Sensor Developments for ATLAS
- Total Ionization Dose effects in the FE-I4 front-end chip of the ATLAS Pixel IBL detector
- Radiation-Hard/High-Speed Parallel Optical Links
- Ultra-light mechanical supports for pixel detectors
- The Atlas IBL CO2 Cooling System
- 3D stacking technology
- Development of n+-in-p planar pixel sensor flip-chip modules with quad FE-I4 readout ASIC's
- Pixel hybridization technologies for the HL-LHC
- Thin hybrid pixel assembly with backside compensation layer on roic
- Poster sessions
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