



**B. Checcucci, D.Magalotti,
INFN Perugia - Italy**

- ◆ **Introduction: Requirements**
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Introduction: Requirements

- ◆ **INFN Perugia has decided to take the responsibility of the new PRM for AM06 chip PCB production ONLY;**
- ◆ **PRM IS NOT A STANDARD FMC BOARD BECOUSE IT DOES NOT RESPOND TO THE ANSI/VITA STANDARD;**
- ◆ ***Before start the AM06 electrical scheme HAVE TO BE DEFINED ALL THE SPECIFICATIONS (in particular the issues relative to the power consumption of the AM chips);***
- ◆ ***Considering the power consumption.***
As a matter of fact Pulsar give to PRM power over 2 rails: 3.3V @14A and 12V 4@A.
If the AM06 has a power consumption @ 1V of 30W this imply that we are in a upper limit
If the AM06 has a power consumption @ 1V of more than 30 W this imply that we are not any more capable to power the Am06 chips.
In this case the pulsar has to change the power rails toward PRM.
PLEASE TAKE ACTION ON IT;



- ✓ ***Please consider that the current occupancy of the board is 95% and that imply NO SPACE for adding new components.***
PLEASE TAKE ACTION ON IT;
- ✓ ***Concerning the memory, beside the fact of access time is more important to define the size and the type of the memory which has to be used;***
- ✓ ***Has to be clear the all the logic functions requested by the internal FPGA are enough for the selected device.***

PLEASE consider that we should decouple the 2 scenarios:

- 1) PCB production and test to prove it functionality
Perugia responsibility***
- 2) Functional test of the Board Not Perugia Resp.***



Details concerning the modifications and so the definition of the new PRM have to be discussed and have been sent yesterday.

A test check list for the PCB functions will be defined soon.

A Gantt for the PCB prototype will be defined soon.



Conclusions

- ◆ First of all we want to thanks to everybody that will help us in this effort;
- ◆ Has to be clear that our responsibility will be only for the PCB production issues;
- ◆ Test of the PRM functions are out of Perugia responsibility.