

Readiness for Test-beam 2012: trigger the discussion

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SPS Operation

Period 6 2012 Oct 27 to Dec

Schedule issue date: 12-September-2012 Version 1.0

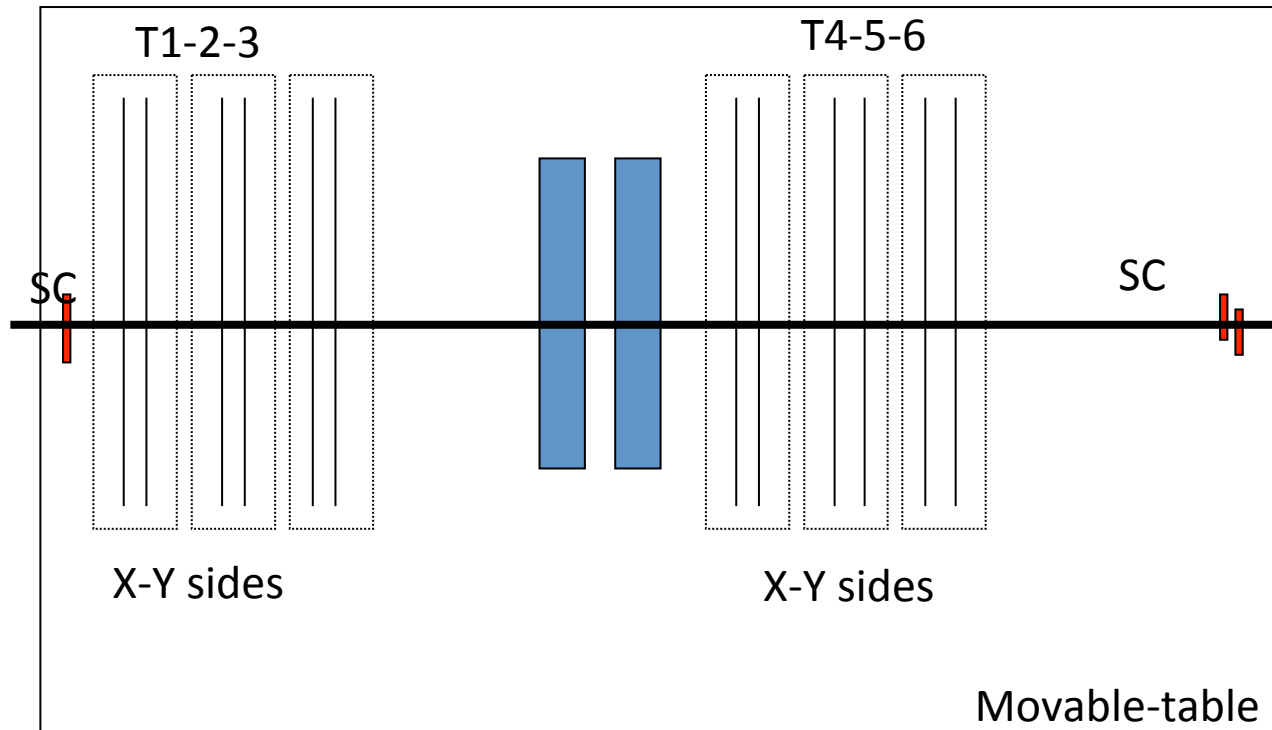
(colour code: purple (dark) = scheduling m

		Sat 27	Sun 28	Mon 29	Tue 30	Wed 31	Thu 1	Fri 2	Sat 3	Sun 4	Mon 5	Tue 6	Wed 7	Thu 8	Fri 9	Sat 10	Sun 11	Mon 12	Tue 13	Wed 14	Thu 15	Fri 16	Sat 17	Sun 18	Mon 19	Tue 20	Wed 21	
		Oct	Oct	Wk44	Oct	Oct	Nov	Nov	Nov	Nov	Wk45	Nov	Nov	Nov	Nov	Nov	Nov	Nov	Wk46	Nov	Nov	Nov	Nov	Nov	Nov	Wk47	Nov	Nov
Machine		717					820					717					717											
		WED MD					BIGUE MD					TWICE MD					WED MD					TUE MD						
ATH AREA	T2 -H2	8h D Lazic					8h CMS-CALSIBT					8h M Prest					8h I Laktineh					8h M Chefde						
	T2 -H4	8h A P					8h H R Schmid					8h M Tsipolitis					8h M Calviani					8h RD51						
	T4 -H6	8h H Wilkens					8h A Ceccucci					8h R Lemmon AT					8h S Bet/R Pes					8h WP9 Garcia						
	T4 -H8	8h H Schindler					8h W Klempt					8h H Schindler					8h H Schindler					8h R						

Logistics

- We'll use H6B station as in 2011
- Sharing the beam-time: minimized the interferences because Pestonik uses H6A
- Contacting Lemmon to ask if possible to mount our equipment in advance
- Same “mechanics” as in 2011: same table (To), mechanical supports for telescope/DUTs (w/o striplets)
- Not perform a system test in Bo (only a whole DAQ test)
- Use car/small truck (s) of the INFN sections Bo/Pi/To/Ts to take to CERN (via France) our equipment

Test beam 2012: which DUTs?



List of possible DUTs

- **Analog MAPS:**
 - INMAPS(High- Ω and Low- Ω irr. and not irr.)
 - V.I. 3D MAPS?
 - **Digital MAPS:**
 - INMAPS 32x32 (High- Ω and Low- Ω not irr.)
 - SuperPix0
- Lower THR
(peace-keeper working?)
- 3D-TC
 - DIASuperPix0

To be set a priority list!
(available only 5 days of beam)

Work in Lab. to be done

- Characterization:
 - With TLA:
 - 3 chips INMAPS 32x32 high- Ω (Pi)
 - 3 chips 3D-TC (Pi)
 - 2 chips DIASuperpix0 (Pv/Lecce)
 - Superpix0:
 - THR correction tests with chips TB2011 (Pi)
- DAQ(Bologna):
 - Writing the firmware for INMAPS 32x32
(debug possible: apsel4D board + inmaps chip)
 - IDEM for chip 3D-TC
 - Understand if the peace-keeper can work with lower THRs w.r.t. TB 2011

ManPower

- Analysis: Alberto L. and Marcin available during the TB and after to have results soon (before Christmas!). N.N.?
- On Analog MAPS: Eugenio
- Torino: D. Gamba et al. available to take the movable table to CERN and mount/dismount that
- Bologna: let's hear from Filippo/Carla/Mauro
- PV: Stefano Z. ready for shift!
- Boards/carriers: Fabio M. ready to go!
- Must converge on the list of people who can/want to contribute