12th Pisa Meeting on Advanced Detectors Radiation Tolerance of a Moderate Resistivity Substrate in a Modern CMOS Process

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The LePix project [1] aims at developing monolithic detectors integrating reverse biased detecting diodes and readout circuitry by porting a standard 90 nm CMOS process to higher resistivity substrates. Large diodes (about 1 mm²) and pixel matrices prototypes were manufactured and exposed to both X-ray and neutron irradiations to investigate the radiation tolerance of the base material, which is an order of magnitude higher doped than standard high resistivity detectors.

The diode is formed by an N-well implant in the high resistivity p-type substrate. It is equipped with a guard ring structure, which includes rings in poly silicon (imposed because of density rules in this deep sub micron technology) over field oxide which can be biased using a resistor.

P-Sub





Schematic view of the diode with its

Detector bias (V

Neutron irradiations^{*} have been carried out at the neutron irradiation facility in Ljubljana, with fluences varying between to $1 \cdot 10^{12}$ n/cm² and $2 \cdot 10^{15}$ n/cm². CV and IV measurements have been carried out on the irradiated large diode samples, from which doping level variation and leakage current damage rate ($\alpha = J/\Phi_{eq}$) have been extracted.



Diode picture



Comparisons between the real and ideal capacitance and the real and the ideal depletion region. The ideal capacitance has been obtained by supposing a planar and abrupt junction, a doping level of 1.5.10¹³ cm⁻³ and a built-in potential of 0.7 V.

CV and IV curves for different neutron fluence values



Doping level vs depletion thickness for four different fluence value.

Doping level variation and leakage current damage rate estimation for $V_{sub} = -24$ V. Due to its exponential dependence on the temperature, the current is expressed at the reference temperature of $T_{R} = 20^{\circ}C$ following [2]:

 $I(T_R) = I(T) \cdot R(T) \longrightarrow R(T) = \left(\frac{T_R}{T}\right)^2 \cdot \exp\left[-\left(\frac{E_g}{2k_h}\right) \cdot \left(\frac{1}{T_R} - \frac{1}{T}\right)\right]$

The value obtained for the leakage current damage rate is $\alpha = 3.18 \cdot 10^{-16}$ A/cm which is higher than the value usually observed for traditional detector-grade silicon [3].

A 32x32 matrix of 50x50 µm² pixels with different collection electrode (N-well) sizes and different input transistors has been fabricated with a low power readout circuit based on correlated double sampling.





Matrices have been irradiated under bias with X-rays (10 keV) up to a 10 Mrad dose at low rate (5 krad/min) and with neutrons with no bias up to a 10^{14} n/cm² fluence.



collection electrode (N-well) in the pixel ($T = -24^{\circ}C$).

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[2] S.M. Sze, Semiconductor Devices Physics and Technology, John Wiley & Sons

[3] M Moll et all, Relation between microscopic defects and macroscopic changes in silicon detector properties after hadron irradiation, Nuclear Instruments and Methods

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