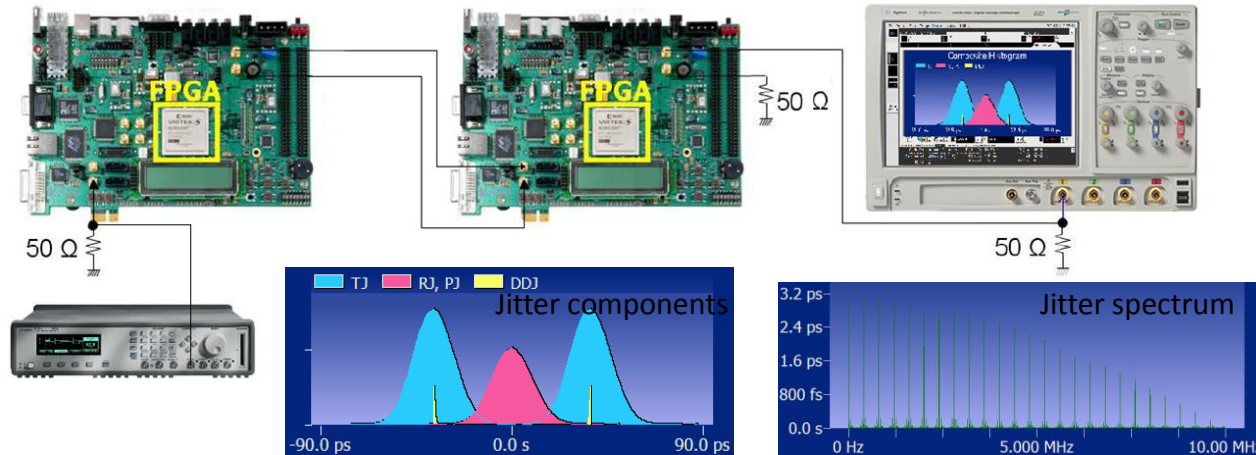
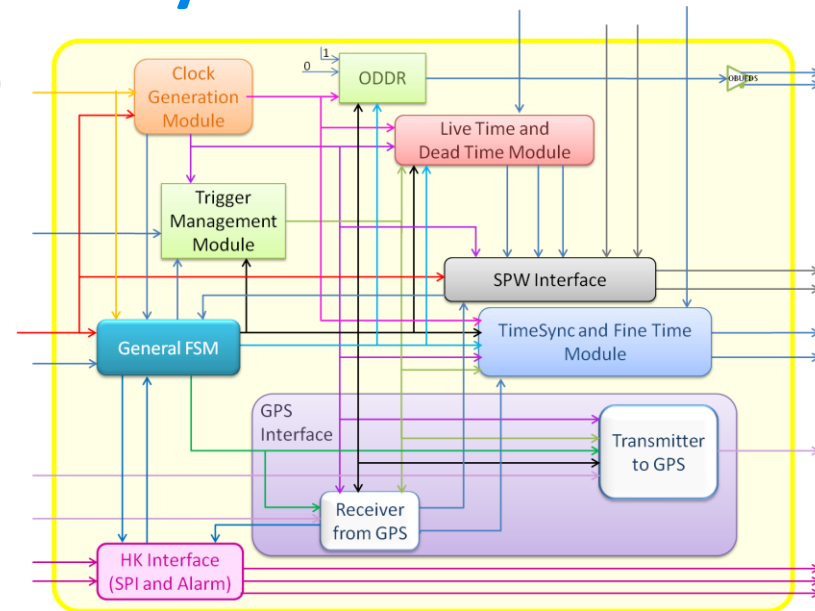


The time synchronization system

1. Generates and distributes system clock (40 MHz), GTU clock (400 KHz) and the Time-Sync to all the devices of the FS
2. Provides for time synchronization of the event and measures the arrival time of the particles on a scale of few μs
3. Interfaces with the JEM EUSO GPS system
4. Manages the trigger signals: receives CCB 2nd level trigger signals and forwards to CCBs any triggers coming from CPU
5. Measures live-time and dead-time

After the implementation on a Virtex-5 FPGA, we carried on:

- Interfaces with GPS test
- Space-Wire and SPI protocol tests
- Jitter measurement through the CLK-Board and after an additional level of boards



Next step: we are developing the CLK-Board for two pathfinder missions planned for 2013: EUSO-BALLOON and a prototype that will be installed in the TA site.