

BAM implementation challenges



F. Loehl, M. Bock, V. Arsov, M. Felber, P. Geßler, K. Hacker, B. Lorbeer, F. Ludwig, K-H. Matthiesen, H. Schlarb, B. Schmidt, A. Winter, J. Zemella (Deutsches Elektronen-Synchrotron)



L. Wißmann, S. Schulz (Universität Hamburg)



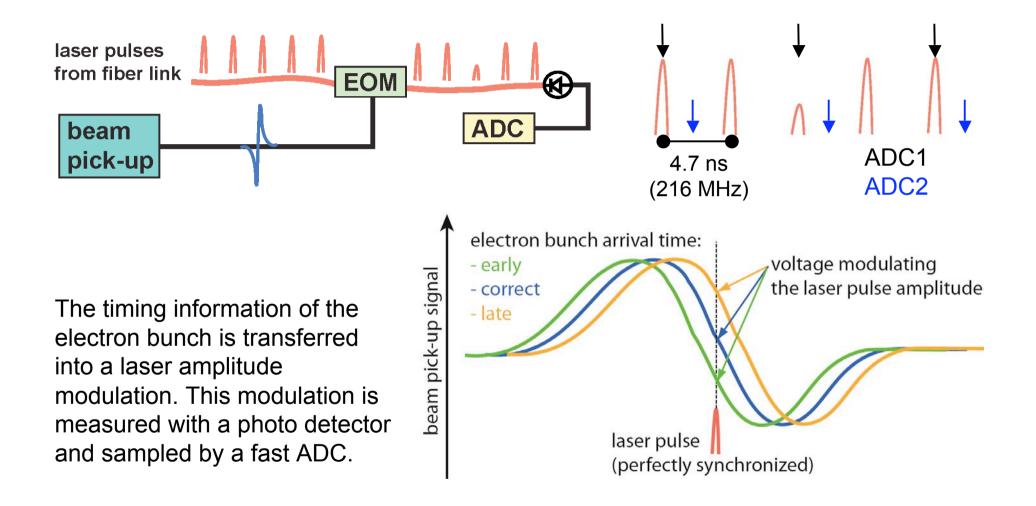
J. Szewinski (Warsaw University of Technology Institute of Electronic Systems)



W. Jalmuzna (Technical University of Lodz)

BAM principle



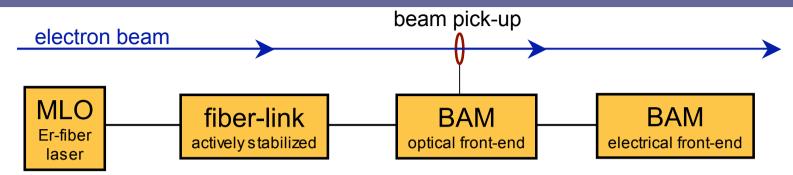


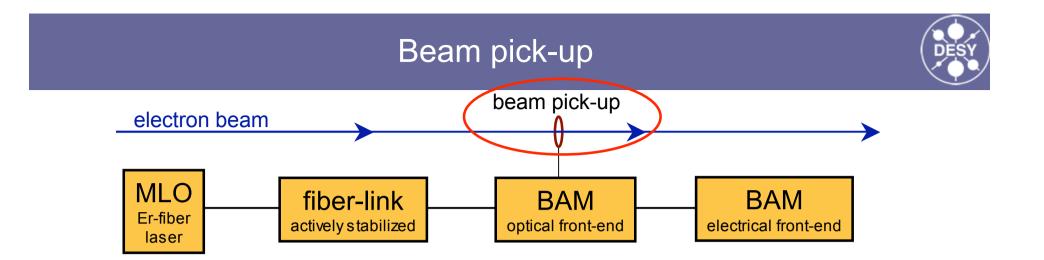
Patented in 2006 by DESY

2nd Timing and Synchronization Workshop, Trieste, March 9, 2009

General layout



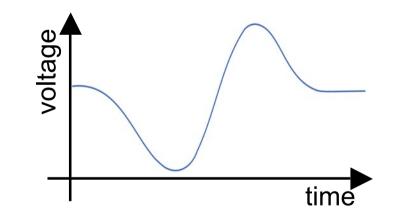




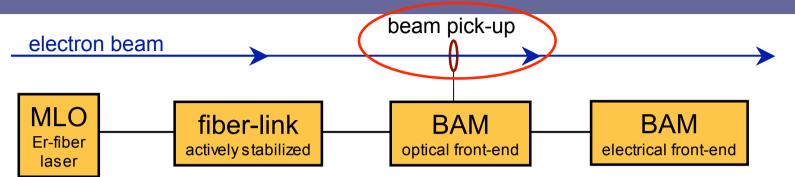
Wish-list for beam induced pick-up signal:

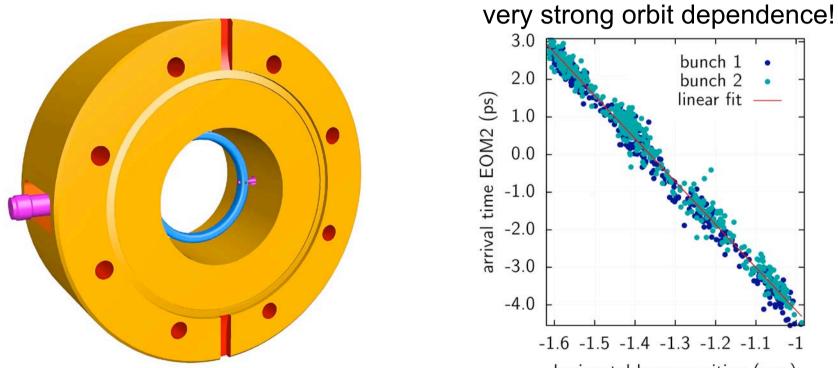
- steep slope at zero-crossing
- low peak-peak voltage
 - \rightarrow EOMs stand ± 5 V
 - → limiters are non-linear
- low bandwidth
 - → reduces dependence of zerocrossing position on bunch shape

Not all conditions can be fulfilled at the same time!



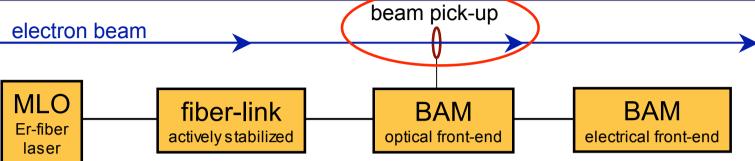
Beam pick-up experience: ring-type





horizontal beam position (mm)

Beam pick-up experience: ring-type



Enables high precision beam position measurements:

$$t_{\text{arrival}} = t_{\text{meas},1} + a_{x,1}x + a_{y,1}y$$

$$t_{\text{arrival}} = t_{\text{meas},2} + a_{x,2}x + a_{y,2}y$$

$$a_{x,1} = (-6.94 \pm 0.05)\frac{\text{fs}}{\mu\text{m}} \qquad a_{x,2} = (10.7 \pm 0.02)\frac{\text{fs}}{\mu\text{m}}$$

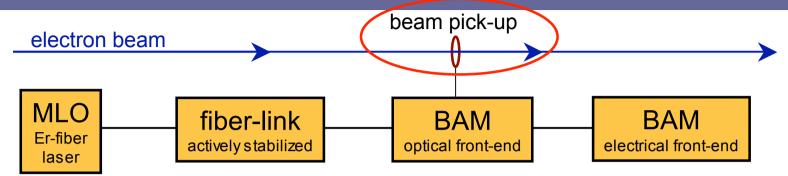
$$a_{y,1} = (-0.16 \pm 0.07)\frac{\text{fs}}{\mu\text{m}} \qquad a_{y,2} = (0.29 \pm 0.02)\frac{\text{fs}}{\mu\text{m}}$$

$$a_{y,2} = (0.29 \pm 0.02)\frac{\text{fs}}{\mu\text{m}}$$

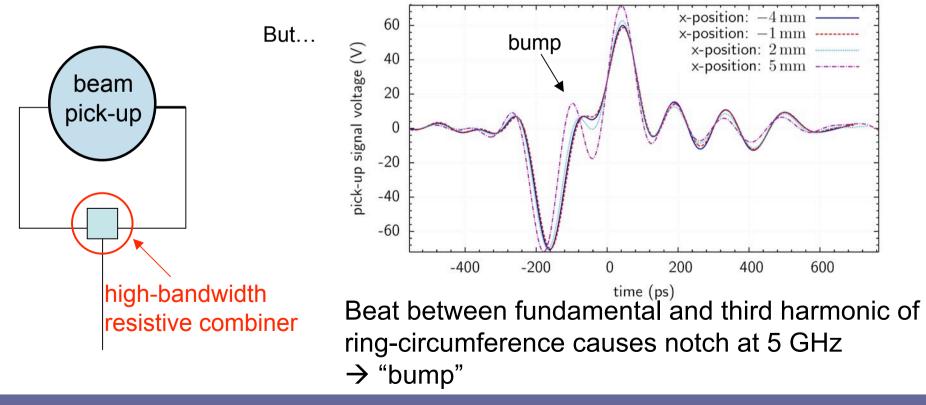
$$\Rightarrow 3 \ \mu\text{m} \text{ resolution}$$

But: arrival-time measurement is sensitive to calibration constant errors. \rightarrow reduce orbit dependence

Beam pick-up experience: ring-type

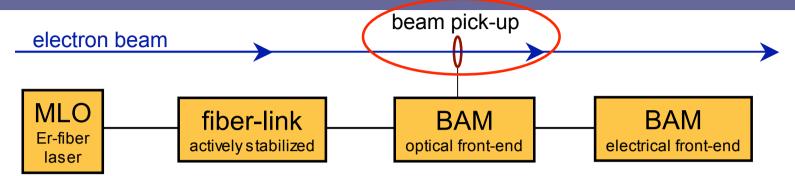


Combining both outputs reduces orbit dependence by a factor of 30-50



2nd Timing and Synchronization Workshop, Trieste, March 9, 2009

Beam pick-up experience: button-type



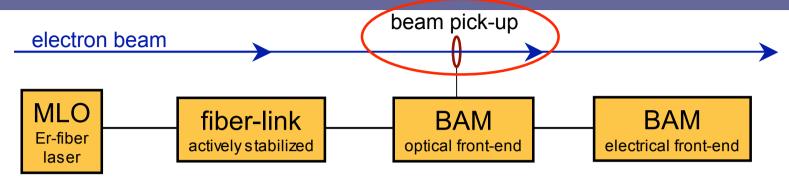


- opposite outputs are combined
- optimized for steep zero-crossing slope and low peak voltage

Design: K. Hacker

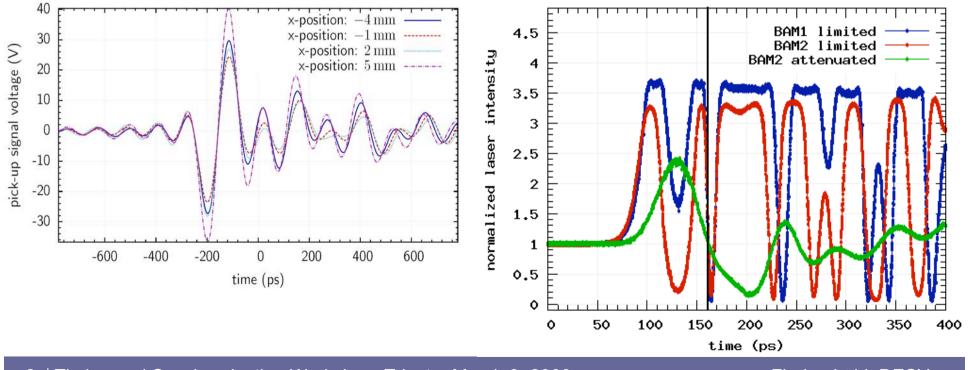
2nd Timing and Synchronization Workshop, Trieste, March 9, 2009

Beam pick-up experience: button-type



8 GHz oscilloscope measurement

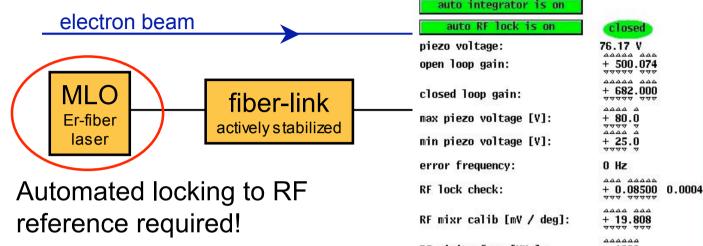
BAM measurement



2nd Timing and Synchronization Workshop, Trieste, March 9, 2009

MLO automation

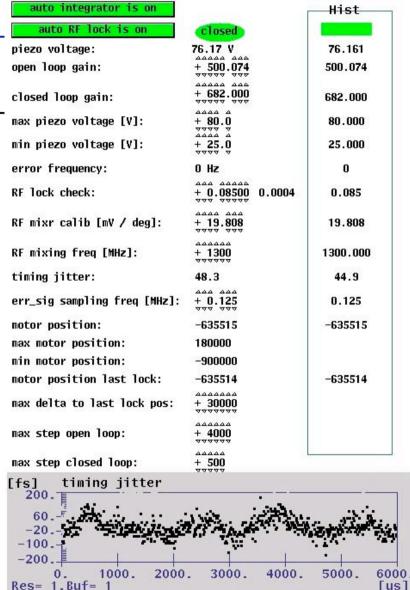




 \rightarrow DOOCS server

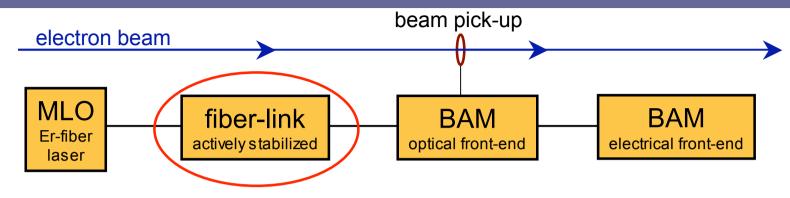
- controls movement of MLO piezo mirror delay stage
 - locks RF feedback loop
 - keeps piezo voltage in acceptable range
- control of digital regulation parameters

• ...



Fiber-link automation

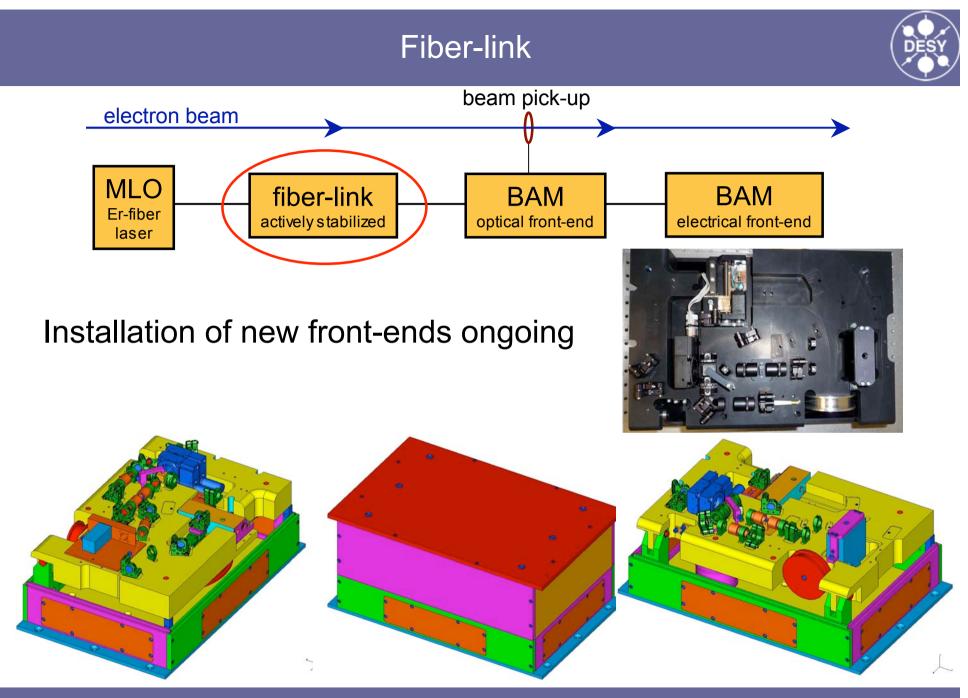




Automated cross-correlator feedback required!

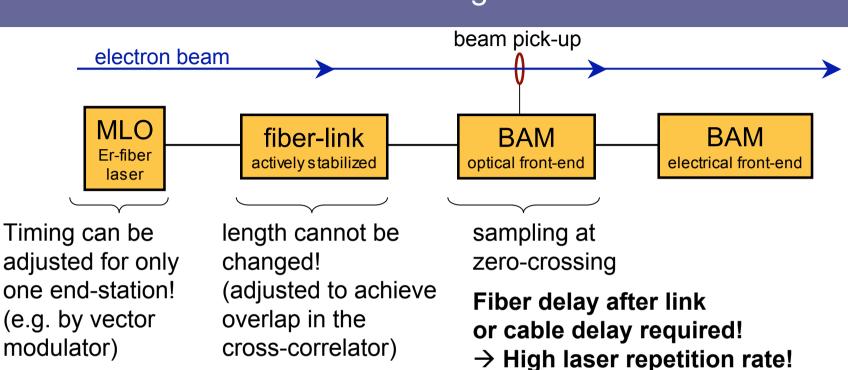
- \rightarrow DOOCS server
- controls movement of optical delay stage
 - locks feedback loop
 - keeps piezo voltage in acceptable range
- communicates with MLO server

• ...



2nd Timing and Synchronization Workshop, Trieste, March 9, 2009





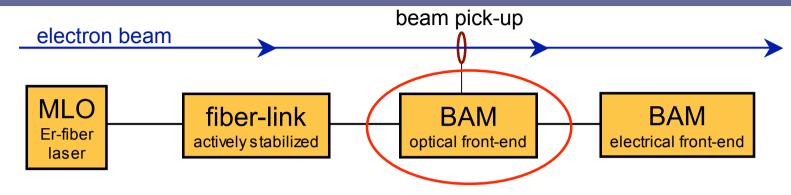
(dispersive length ~8 cm)

The timing reference of the optical synchronization system is fixed!

- → All timings have to be adjusted for by delays outside the fiber-link (vector modulator for MLO needed to measure delays!)
- → Delay stages needed for timing changes (bunch compressors!)

2nd Timing and Synchronization Workshop, Trieste, March 9, 2009

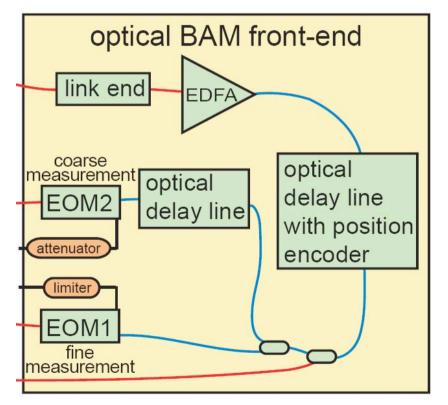




Polarization maintaining setup→ Special splice equipment needed

Installed in accelerator tunnel

- → Radiation shielded
- \rightarrow Everything remote controlled

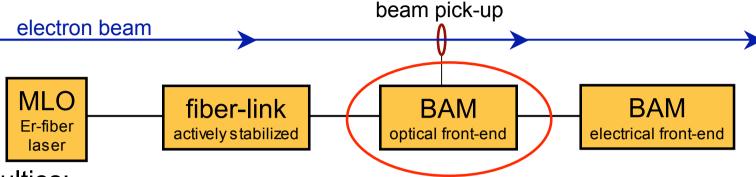






2nd Timing and Synchronization Workshop, Trieste, March 9, 2009





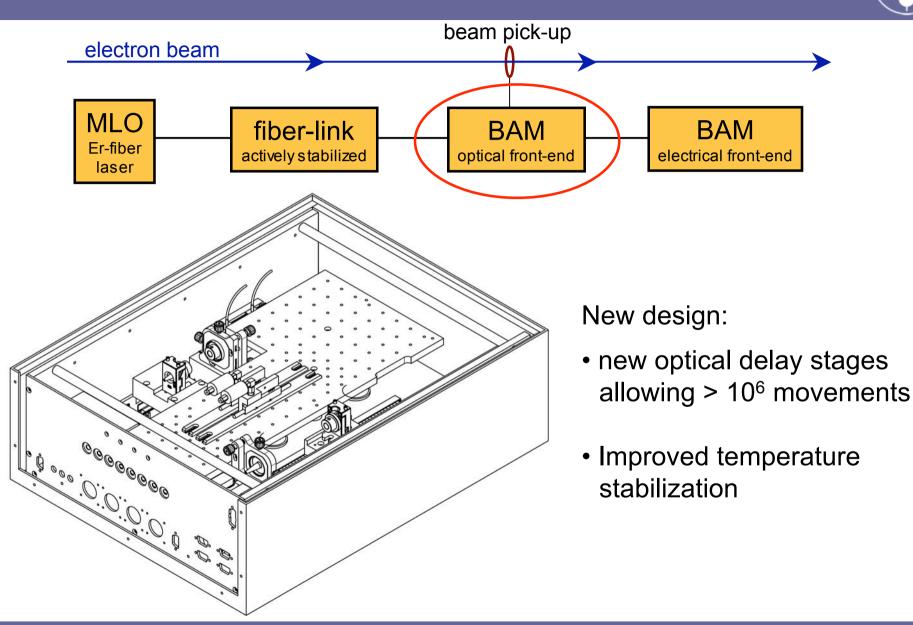
Difficulties:

• Position encoders / readout fail from time to time

→ radiation? EMI? ground currents? ...

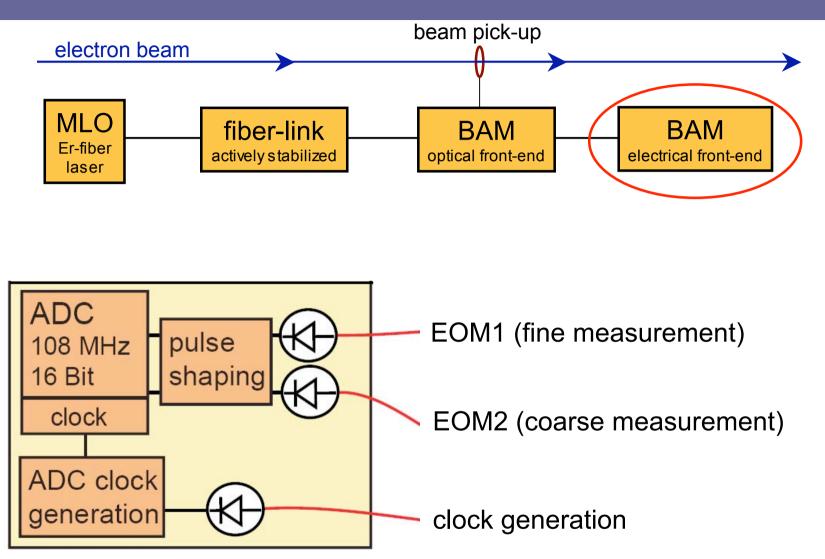
- VME crates for laser diode drives crash from time to time
 - → radiation? EMI? ground currents? ...
- Installation problems with EDFA pump diodes:
 - Tested in laboratory \rightarrow \bigcirc
 - Tested with drivers in the tunnel \rightarrow \odot
 - EOMs connected to pick-up → ⊗
 different ground potentials of beam pipe and VME crate
- Optical delay stages are not robust enough





BAM electrical front-end





BAM electrical front-end

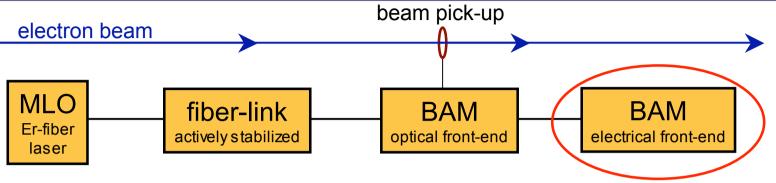




- RocketIO ports for fast feedbacks
- ADC clock input (integrated clock generation on the way)
- Analog front-end with
- 4 ADCs (16 bit, 130 MSPS)
- 2 photo detectors
- VME carrier board with
- FPGA: Xilinx Virtex II Pro (xc2vp30)
- 3x AD9510 clock managers

BAM server



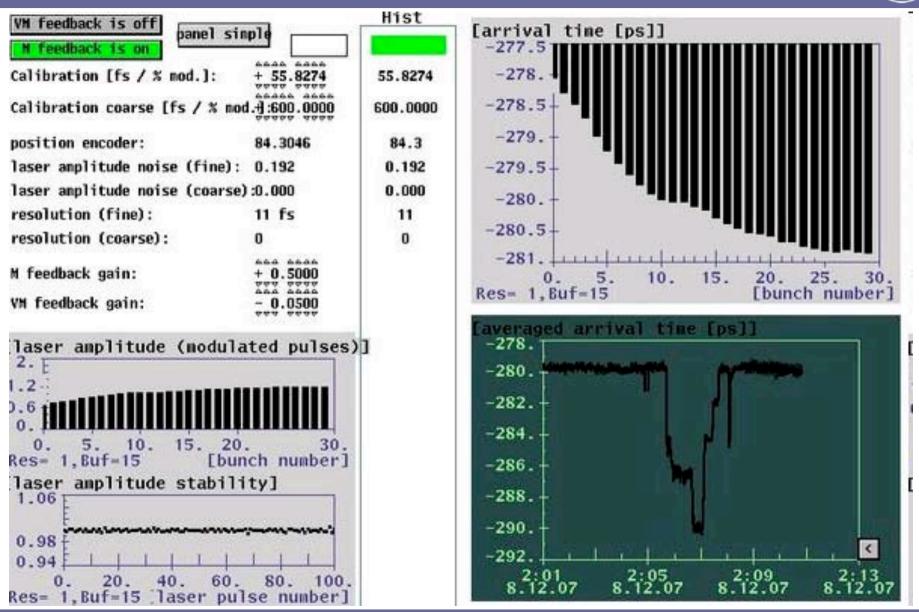


BAM DOOCS server:

- Online arrival-time calculations
- provides time-stamps for correlations with other machine data
- Slow feedback to maintain sampling at the zero-crossing
- Automated, online calibration
- communication with MLO and fiber-link server
- ...

BAM server

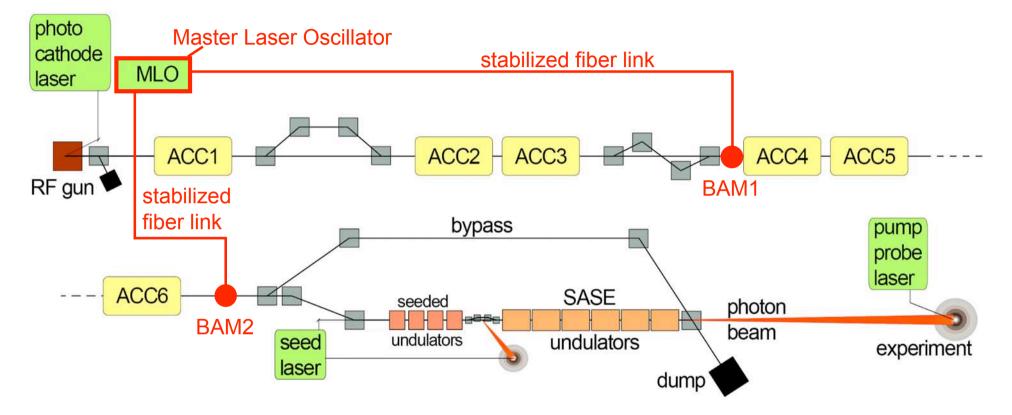




2nd Timing and Synchronization Workshop, Trieste, March 9, 2009

BAM resolution measurement

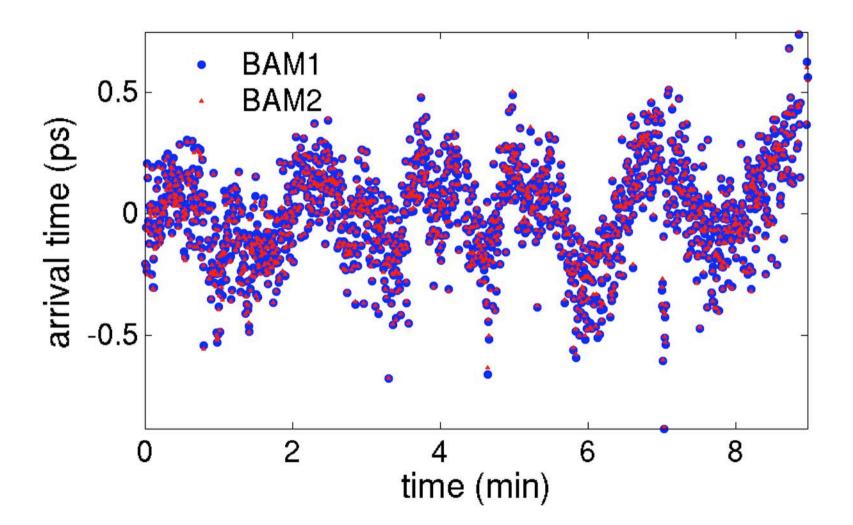




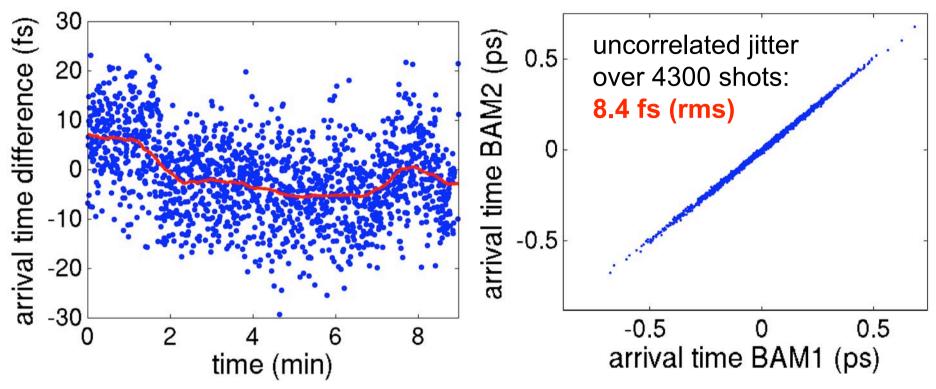
Two BAMs in a straight section are used to measure the arrival times of the same bunches

The BAMs are separated by 60 m.

Arrival time correlation between two BAMs



Arrival time correlation between two BAMs



Arrival time difference contains:

- high frequency laser noise (~3 MHz 108 MHz)
- stability of two fiber links
- two BAMs

Single bunch resolution of entire measurement chain: < 6 fs (rms)